IOM_PWM_Monitor_1
for KIT_AURIX_TC275_LK
PWM monitor via IOM
Scope of work

A PWM signal’s duty cycle is monitored using the IOM module.

The PWM signal is routed through the port to the Input Output Monitor (IOM) module. Its duty cycle is continuously compared with a threshold. If the value of the duty cycle goes below the threshold, an LED is switched on.
Introduction

› In this example, the following modules are used:
  - IOM (Input Output Monitor)
  - SMU (Safety Management Unit)
  - IR (Interrupt Router)
  - GTM (Generic Timer Module)

› All the modules above are presented in this tutorial, although a specific focus is given to the IOM and the SMU modules
Introduction

The Input Output Monitor (IOM) serves as a smart I/O comparison unit:
- Checks for the correct operation of system peripheral outputs that might serve and/or control externally-attached hardware
- Monitors the correct operation of the hardware itself, including any sensors whose signals might serve as an input to the monitoring function

The IOM can be used to either monitor a signal for a desired behaviour or compare a monitor signal with a reference signal for safety requirements

Up to 16 monitoring points can be configured, with the capability to generate a system event that can be driven from:
- one individual configuration
- several individual configurations
- multiple occurrences of a monitored condition
- combination of several conditions depending upon configuration

Features of IOM:
- 16 Filter & Prescaler Channels
- 16 Logic Analyzer Modules (LAMs)
- 1 EXOR combiner, configurable, to select a range up to 8 GTM inputs
- 1 Event Combiner Module (ECM), taking the 16 local event signals and generating a system event signal from a single, combination or multiple local event(s)
- System Peripheral Bus (SPB) interface for configuration and status register interaction
Introduction

The Safety Management Unit (SMU) provides a generic interface to manage the behaviour of the microcontroller under the presence of faults.

It centralizes all the alarm signals related to the different hardware and software-based safety mechanisms.

The alarms are classified into alarm groups. This does not define any hierarchy but only a logical mapping to internal configuration registers. The configuration options therefore specify the behaviour of the SMU when an alarm event is detected:
- The alarm event can trigger an internal action
- The alarm event can indicate a fault to the external environment via the Error Pin

Internal action resulting from an alarm event can be interfaced to the Interrupt Router via the following signals:
- SMU Interrupt Request 0
- SMU Interrupt Request 1
- SMU Interrupt Request 2

The SMU in combination with the embedded safety mechanisms enables the detection of more than 99% of the critical failure modes of the microcontroller.
Introduction

› The interrupt system in the AURIX™ TC2xx devices is implemented in the **Interrupt Router (IR)**

› **Interrupt Requests** (or Service Requests) can be serviced either by the CPUs or by the DMA module (both called Service Providers)

› An interrupt can be triggered by:
  – Each module connected to the IR
  – External peripherals
  – Software via General Purpose Service Requests (GPSR)

› Each Service Provider supports **up to 255** service priority levels:
  – 0 to disable the interrupt
  – 255 for highest priority
### Introduction

- The Generic Timer Module (GTM) is a modular timer unit designed to accommodate many timer applications.

- It has an in-built Advanced Router Unit (ARU) that can be used to exchange specific data between sub-modules without CPU interaction.

- The ARU-connected Timer Output Module (ATOM), which is part of the GTM, is able to generate complex output signals.

- The Clock Management Unit (CMU) is responsible for clock generation of the GTM. The Configurable Clock Generation Subunit (CFGU) provides eight clock sources for the GTM submodules: TIM, TBU, MON and ATOM.
Hardware setup

This code example has been developed for the board KIT_AURIX_TC275_LITE.

Connect the two pins P00.0 PWM signal and P33.1 IOM monitor input to each other.
Implementation

1. The GTM module generates a PWM signal.
2. The IOM monitors the PWM signal and compares it with an internal configured Event Window.
3. If an event is generated, an SMU internal alarm is triggered.
4. The IR handles the SMU alarm as software interrupt.
Configuring the PWM signal from the GTM – \textit{init\_Gtm\_Pwm()}

The PWM signal is generated by the ATOM submodule. The following steps are performed at first in the example:

- Enable the GTM by calling the function \textit{IfxGtm\_enable()}
- Set the CMU clock 0 frequency to 1 MHz with the function \textit{IfxGtm\_Cmu\_SetClkFrequency()}
- Enable the CMU clock 0 by calling the function \textit{IfxGtm\_Cmu\_enableClocks()}

Secondly, the function \textit{IfxGtm\_Atom\_Pwm\_initConfig()} initializes an instance of the structure \textit{IfxGtm\_Atom\_Pwm\_Config} with its default values. The user can then choose specific values for the following parameters:

- \textit{atom} – Selection of the ATOM which is counting (ATOM 0 in this example)
- \textit{atomChannel} – Selection of the ATOM channel (channel 0 in this example)
- \textit{period} – Setting of the period for the PWM signal to the desired value (in \(\mu\)s)
- \textit{pin.outputPin} – Selection of the port pin P00.0 as output pin
- \textit{synchronousUpdateEnable} – Enabling of Synchronous Update of the timer
- \textit{dutyCycle} – Selection of the PWM duty cycle length (in \(\mu\)s)

After applying the specified parameters via a call to \textit{IfxGtm\_Atom\_Pwm\_init()}, the PWM starts by calling the function \textit{IfxGtm\_Atom\_Pwm\_start()}

The above functions can be found in the \textit{IfxGtm.h}, \textit{IfxGtm\_Atom\_Pwm.h} and \textit{IfxGtm\_Cmu.h}. 
Implementation

Configuring the IOM – init_lom()

Such as the other modules, the IOM needs at first to be enabled. This is done via the iLLD function IfxIom_enableModule().

Before starting to configure the parameters, the configuration drivers need to be initialized (see code for more detailed information):

- IfxIom_Driver – Declare IOM driver
- IfxIom_Driver_Config – Used to configure the IOM
- IfxIom_Driver_Lam – Declare LAM driver
- IfxIom_Driver_LamConfig – Used to configure the LAM

The configuration of the LAM continues by setting the following key parameters:

- mon.input selecting the monitor input port
- eventWindow.controlSource determining the signal generating the events
- eventWindow.clearEvent defining when to start a new event window
- eventWindow.threshold defining the event window size
- eventWindow.inverted defining whether the event should happened before or after the threshold
- event.source defining the sourced signal
- event.trigger defining on what is the triggering event
- systemEventTriggerThreshold setting the amount of events before generating an alarm

The configuration is thereafter applied to the module via the iLLD function IfxIom_Driver_initLam() and the LAM module can be enabled with IfxIom_Driver_enableLamEvent().

All the functions can be found in the iLLD headers: IfxIom.h and IfxIom_Driver.h.

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Implementation

Configuring the SMU – *init_Smu()*

The SMU is first enabled and started with the iLLD function `IfxSmu_start()`. To allow the configuration of the SMU, the module must be unlocked. This can be done with the iLLD function: `IfxSmu_unlock()`.

The function `IfxSmu_setAlarmConfig()` has to be called with the following parameters:
- `IfxSmu_Alarm_IomPinMismatchIndication` is used to identify the alarm refering to the IOM
- To configure the SMU behavior when an alarm occurs, an Interrupt Generation Configuration Set (IGCS) has to be selected. In this example, IGCS0 (IGCS1-2 are also available) identifies with `IfxSmu_AlarmConfig_interruptSet0`

Since IGCS0 has been selected, it has to be enabled in SMU Alarm Global Configuration (AGC) setting to 1 the bit 0 of the SMU_AGC register:

```
MODULE_SMU.AGC.U = 0x1;
```

Lastly, the SMU module has to be relocked with the function `IfxSmu_lock()`. All the above functions are contained in the header file `IfxSmu.h`. 

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Implementation

Configuring the Interrupt Router – *init_IR()*

To handle the Software interrupt generated by the SMU module, the Service Request Register (SRC) from the SMU0 (identifying the SMU Interrupt Generation Configuration Set 0) has first to be fetched. Then calling *IfxSrc_init()* allows:

› The selection of the Service Provider (CPU0 in this example)
› The interrupt priority level (10 in this example)

The function *IfxSrc_init()* can be found in the header file *IfxSrc.h*. 

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Example workflow

Starting conditions:

- The duty cycle is decreased by 5% every second
- When the duty cycle of the monitored PWM signal goes below the accepted threshold of 37%, the alarm is triggered and an LED is switched on
- When the duty cycle reaches 25%, it is set back to 50% and the example routine starts again
Run and Test

After code compilation and flashing the device, perform the following steps:

1. Make sure that the port pin P00.0 (PWM signal) and P33.1 IOM (monitor input) are connected to each other

2. (optional) An oscilloscope can be used to observe the behavior of the PWM signal and that the LED is triggered when the duty cycle goes below 37%
Run and Test

3. LED1 is on when the monitored PWM signal’s duty cycle is not respecting the minimum 37% ratio.
References

› AURIX™ Development Studio is available online:
  › [https://www.infineon.com/aurixdevelopmentstudio](https://www.infineon.com/aurixdevelopmentstudio)
  › Use the „Import...“ function to get access to more code examples.

› More code examples can be found on the GIT repository:
  › [https://github.com/Infineon/AURIX_code_examples](https://github.com/Infineon/AURIX_code_examples)

› For additional trainings, visit our webpage:
  › [https://www.infineon.com/aurix-expert-training](https://www.infineon.com/aurix-expert-training)

› For questions and support, use the AURIX™ Forum:
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