Getting Started with AURIX[™] Development Studio Installation and first steps

AURIX[™] Development Studio Training V1.0.18





Scope of work

This tutorial provides a guide for the user to:

- > Install AURIX[™] Development Studio V1.9.20
- > Create new project
- Import project (Infineon Code Examples Repository)
- > Build project
- > Debug project
- > Additional material



Download

The installation package of AURIX[™] Development Studio can be found here: <u>https://www.infineon.com/aurixdevelopmentstudio</u>



<u>B</u>ack <u>I</u>nstall

Cancel

> To install AURIX[™] Development Studio, launch the installation package and follow the steps:

🖓 Setup - AURIX Development Studio version 1.9.20 — 🗌 🗙	🗠 Setup - AURIX Development Studio version 1.9.20 — 🗆 🗙	🛃 Setup - AURI	IX Development Studio version 1.9.20 —	×
License Agreement Please read the following important information before continuing.	Select Destination Location Where should AURIX Development Studio be installed?	Select Addition	onal Tasks ional tasks should be performed?	
Please read the following License Agreement. You must accept the terms of this agreement before continuing with the installation.	Setup will install AURIX Development Studio into the following folder.	Select the a Developmen	additional tasks you would like Setup to perform while installing AURIX nt Studio, then click Next.	x
Software License Agreement	To continue, click Next. If you would like to select a different folder, click Browse.	Additional sh	hortcuts:	
BY DOWNLOADING, INSTALLING AND/OR USING (INCLUDING COPYING) THE	C:\Infineon\AURIX-Studio-1.9.20 Browse	Create :	a desktop shortcut	
LICENSED SOFTWARE, LICENSEE AGREES TO BE BOUND BY THE TERMS AND CONDITIONS OF THIS AGREEMENT, IF LICENSEE				
DOES NOT AGREE TO ALL TERMS AND CONDITIONS OF THIS AGREEMENT,				
LICENSEE (INCLUDING YOU) SHALL NOT DOWNLOAD, INSTALL AND/OR USE (INCLUDING COPYING) THE LICENSED SOFTWARE				
I accept the agreement				
I do not accept the agreement	At least 3,69 GB of free disk space is required.			
Next Cancel	Back Next Cancel		<u>B</u> ack <u>N</u> ext	Cancel
	Setup - AURIX Development Studio version 1.9.20 — 🗌 🔿	⊵ Setup - AURI	IX Development Studio version 1.9.20 —	□ ×
	Completing the AURIX	Ready to Ins		EY
	Development Studio Setup Wizard	Setup is nov computer.	w ready to begin installing AURIX Development Studio on your	
	Setup has finished installing AURIX Development Studio on your computer. The application may be launched by selecting the installed shortcuts.	Click Install change any	to continue with the installation, or click Back if you want to review or settings.	or
	Click Finish to exit Setup.	Destination	n location: ineon\AURIX-Studio-1.9.20	
		Additional t		
	Launch AURIX Development Studio	Additio	nal shortcuts: ate a desktop shortcut	
				×
				-

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Install AURIX[™] Development Studio - 2

If DAS64 is not installed or outdated, it will be installed automatically during the AURIX™ Development Studio installation:

Installazione di DAS Installer	×	Installazione di DAS Installer	×	DAS Installazione di DAS Installer	
Installazione - DAS		Cartella di installazione		Contratto di licenza Leggere il seguente contratto	il licenza. È necessario accettare i termini contenuti in questo contratto prima di continuare l'installazione.
Installazione - DAS Installazione guidata d DAS. Cartella di installazione Contratto di licenza Collegamenti del menu Start Pronto all'installazione Installing Finished		Installazione - DAS Cartella di installazione Contratto di licenza Collegamenti del menu Start Pronto all'installazione Installing Finished	Specificare la drectory in cui DAS verà installato. C: Program Files DAS64 Stopla	Installazione - DAS Cartella di installazione Contratto di licenza Collegamenti del menu Start Pronto all'installazione Installing Finished	Important Notice and Terms of Use Flease read the following important notice as well as the following terms and conditions carefully. By checking the accept checkbox below has forth belowing the state of the state of the state of the state as forth belowing terms and conditions below, chick the button do not agree to the terms and conditions below, chick the button "Cancel" and the installation procedure will not be started. If you of the Software in your possession or control. Any copies of the Software thay you have already distributed, where permitted, and do not destroy will continue to be governed by these Terms of Use. Your prior use will also continue to be governed by these Terms of Use.
	Successivo > Esci		< Precedente Successivo > Annula		< Precedente Successivo > Ann
nstallazione di DAS Installer	×	installazione di DAS Installer	×	📧 Installazione di DAS Installer	
Installazione di DAS Installer Jompletamento della procedura guidata DAS	×	installazione di DAS Installer Pronto all'installazione	X	Collegamenti del menu Start	si desidera creare i collegamenti del programma. È anche possibile immettere un nome per creare una nuova direct
	×	_	X S 4 ora pont per indere l'Installazione di DAS nel computer. L'installazione utilizzerà 50.39 MB di spazio au disco.	Collegamenti del menu Start	si desidera creare i collegamenti del programma. È anche possibile immettere un nome per creare una nuova direc DAGI Accessibility Accessibility Accessibility Administrative Tools Administrative Tools Administrative Free Entry Tool Chain v4.9.3.0-infineon-1.0 DAVE IDE 4.5.0.202103191637 Eclippe Theia Infineon Infiscape InfanView Maintenance MSYS2 Omekye 2.55.0.202303101300



Workspace definition

→ After launching the AURIX[™] Development Studio, it is necessary to select a workspace

🚳 AURIX Development Studio Launcher			×
Select a directory as workspace			
AURIX Development Studio uses the workspace directory to store its preferences and developmer	nt artifa	icts.	
C:\Users\MyUser\AURIX-v1.9.20-workspace		× B	rowse
			rowse
Use this as the default and do not ask again			
Recent Workspaces			
Launch		Can	icel



Data evaluation consent

- > On the first start you are asked for the consent to > You can later decide to modify your consent by send data to Infineon for evaluation purposes.
- You can choose which data to send.

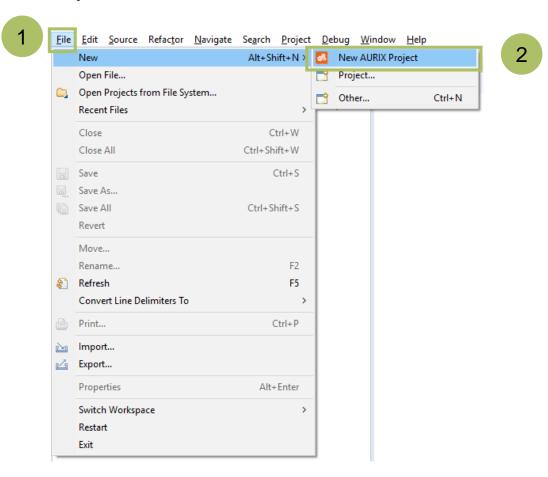
Data Evaluation
We process data, which is necessary for the functioning of our software.
provide technically required data to provide the desired functionality
With your consent, we collect optional telemetry data to improve our services. You can revoke your consent at any time within the settings for "data evaluation". Please select and confirm your choice:
 provide personalized data provide diagnosis and performance data
Please visit Infineon's Privacy Policy for more information.
Accept

clicking on "Open Data Evaluation Settings" on the toolbar

M 🛃 Preferences	— D 3
type filter text > General > AURIX Development Studio ADS ChipCoach Library F Data Evaluation > C/C++ > Help > Install/Update iSYSTEM > Run/Debug > TASKING > Terminal > Version Control (Team)	Data Evaluation
	Restore Defaults Apply



Once the program is started, a new project can be created by selecting File >> New >> "New AURIX Project"





- > From the "New AURIX Development Studio Project" window, choose a name for the new project (3)
- The "Use default location" checkbox should be set in order to create the project inside the current selected workspace

New AURIX Development Studio Project	— 🗆 X
ew AURIX Project	
pecify the name and the location of the new project	
Project name: MyProject	
Use default location	
ocation: C:\Users\MyUser\AURIX-v1.2.0-workspace\MyProject	B <u>r</u> owse
4	<u>N</u> ext > <u>F</u> inish Cancel



From the "New AURIX Development Studio Project" window, choose the device or the board. A specific device (5) or board (6) can be chosen from the left or right list. Furthermore, while selecting a board, the tool highlights the supported devices for that board and vice versa

TC26xDA_B-Step< AURIX TC2xx	Device	Board		
TC23xLP_A-StepKIT_AURIX_TC297_TFT_BC-StepAPPLICATION KIT TC2X7 V1.1TC23xLA/LX_A-StepKIT_AURIX_TC277_TFT_DC-StepAPPLICATION KIT TC2X7 V1.1TC22xL_A-StepKIT_AURIX_TC275_LITEAURIX TC275 lite KitTC21xL_A-StepKIT_AURIX_TC275_ARD_SBhitex ShieldBuddyURIX TC3xxKIT_AURIX_TC255_TFT_BC-StepAPPLICATION KIT TC2X7 V1.1TC39xXX_B-StepKIT_AURIX_TC237_TFT_AC-StepAPPLICATION KIT TC2X7 V1.1TC39xXP_B-StepKIT_AURIX_TC234_TFT_AC-StepAPPLICATION KIT TC2X4 V1.0TC38xQP_A-StepCustom BoardCustom BoardTC37xTX_A-StepV AURIX TC3xxV AURIX TC3xx	TC26vDA R-Sten			
TC23xLA/LX_A-StepKIT_AURIX_TC277_TFT_DC-StepAPPLICATION KIT TC2X7 V1.1TC22xL_A-StepKIT_AURIX_TC275_LITEAURIX TC275 lite KitTC21xL_A-StepKIT_AURIX_TC275_ARD_SBhitex ShieldBuddyURIX TC3xxKIT_AURIX_TC265_TFT_BC-StepAPPLICATION KIT TC2X5 V2.0TC39xXX_B-StepKIT_AURIX_TC237_TFT_AC-StepAPPLICATION KIT TC2X7 V1.1TC39xXP_B-StepKIT_AURIX_TC234_TFT_AC-StepAPPLICATION KIT TC2X4 V1.0TC38xQP_A-StepCustom BoardCustom BoardTC37xTX_A-StepV AURIX TC3xxV	ICZOXDA_B-Step	✓ AURIX TC2xx		
TC22xL_A-Step KIT_AURIX_TC275_LITE AURIX TC275 lite Kit TC21xL_A-Step KIT_AURIX_TC275_ARD_SB hitex ShieldBuddy URIX TC3xx KIT_AURIX_TC265_TFT_BC-Step APPLICATION KIT TC2x5 V2.0 TC39xXX_B-Step KIT_AURIX_TC237_TFT_AC-Step APPLICATION KIT TC2X7 V1.1 TC39xXP_B-Step KIT_AURIX_TC234_TFT_AC-Step APPLICATION KIT TC2X4 V1.0 TC38xQP_A-Step Custom Board Custom Board TC37xTX_A-Step V AURIX TC3xx V AURIX TC3xx	TC23xLP_A-Step	KIT_AURIX_TC297_TFT_BC-Step	APPLICATION KIT TC2X7 V1.1	
TC21xL_A-Step KIT_AURIX_TC275_ARD_S8 hitex ShieldBuddy URIX TC3xx KIT_AURIX_TC265_TFT_BC-Step APPLICATION KIT TC2x5 V2.0 TC39xXX_B-Step KIT_AURIX_TC237_TFT_AC-Step APPLICATION KIT TC2X7 V1.1 TC39xXP_B-Step KIT_AURIX_TC234_TFT_AC-Step APPLICATION KIT TC2X4 V1.0 TC38xQP_A-Step Custom Board Custom Board TC37xTX_A-Step V AURIX TC3xx V AURIX TC3xx	TC23xLA/LX_A-Step	KIT_AURIX_TC277_TFT_DC-Step	APPLICATION KIT TC2X7 V1.1	
URIX TC3xx KIT_AURIX_TC265_TFT_BC-Step APPLICATION KIT TC2x5 V2.0 TC39xXX_B-Step KIT_AURIX_TC265_TFT_AC-Step APPLICATION KIT TC2x7 V1.1 TC39xXP_B-Step KIT_AURIX_TC234_TFT_AC-Step APPLICATION KIT TC2X4 V1.0 TC38xQP_A-Step Custom Board Custom Board TC37xTX_A-Step V AURIX TC3xx KIT_AURIX_TC3xx	TC22xL_A-Step	KIT_AURIX_TC275_LITE	AURIX TC275 lite Kit	
TC39xXX_B-Step KIT_AURIX_TC237_TFT_AC-Step APPLICATION KIT TC2X7 V1.1 TC39xXP_B-Step KIT_AURIX_TC234_TFT_AC-Step APPLICATION KIT TC2X4 V1.0 TC38xQP_A-Step Custom Board Custom Custo	TC21xL_A-Step	KIT_AURIX_TC275_ARD_SB	hitex ShieldBuddy	
TC39xXP_B-Step KIT_AURIX_TC234_TFT_AC-Step APPLICATION KIT TC2X4 V1.0 TC38xQP_A-Step Custom Board TC37xTX_A-Step ✓ AURIX TC3xx	✓ AURIX TC3xx	KIT_AURIX_TC265_TFT_BC-Step	APPLICATION KIT TC2x5 V2.0	
TC38xQP_A-Step Custom Board TC37xTX_A-Step V AURIX TC3xx	TC39xXX_B-Step	KIT_AURIX_TC237_TFT_AC-Step	APPLICATION KIT TC2X7 V1.1	
TC37xTX_A-Step VAURIX TC3xx	TC39xXP_B-Step	KIT_AURIX_TC234_TFT_AC-Step	APPLICATION KIT TC2X4 V1.0	
	TC38xQP_A-Step	Custom Board		
TC27-TD & CALL ADD TC207 EVITET ADD TC207 EVITET	TC37xTX_A-Step	✓ AURIX TC3xx		
IC3/XTP_A-Step ^KT_A2G_IC397_5V_TFT APPLICATION KTT IC3X7 V2.0	TC37xTP_A-Step	* KIT_A2G_TC397_5V_TFT	APPLICATION KIT TC3X7 V2.0	
TC36xDP A-Step KIT A2G TC387 5V TFT APPLICATION KIT TC3X7 V2.0	TC36xDP_A-Step	KIT_A2G_TC387_5V_TFT	APPLICATION KIT TC3X7 V2.0	
	TC33xLP_A-Step	KIT_A2G_TC377_5V_TFT	APPLICATION KIT TC3X7 V2.0	
TC33xLP_A-Step KIT_A2G_TC377_5V_TFT APPLICATION KIT TC3X7 V2.0	CPUs : 6 TriCore	SRAM Size : 6.5 MB	ALIDAZ ZOSZE IN IZY	
	TC36xDP_A-Step TC33xLP_A-Step	KIT_A2G_TC387_5V_TFT KIT_A2G_TC377_5V_TFT	APPLICATION KIT TC3X7 V2.0 APPLICATION KIT TC3X7 V2.0	
TC36xDP A-Step KIT A2G TC387 5V TFT APPLICATION KIT TC3X7 V2.0	TC36xDP_A-Step	KIT_A2G_TC387_5V_TFT	APPLICATION KIT TC3X7 V2.0	
			APPLICATION KIT TC3X7 V2.0	
	TC33xLP_A-Step	KIT_A2G_TC377_5V_TFT	APPLICATION KIT IC3X7 V2.0	
			ALIONY TOOTE IN	



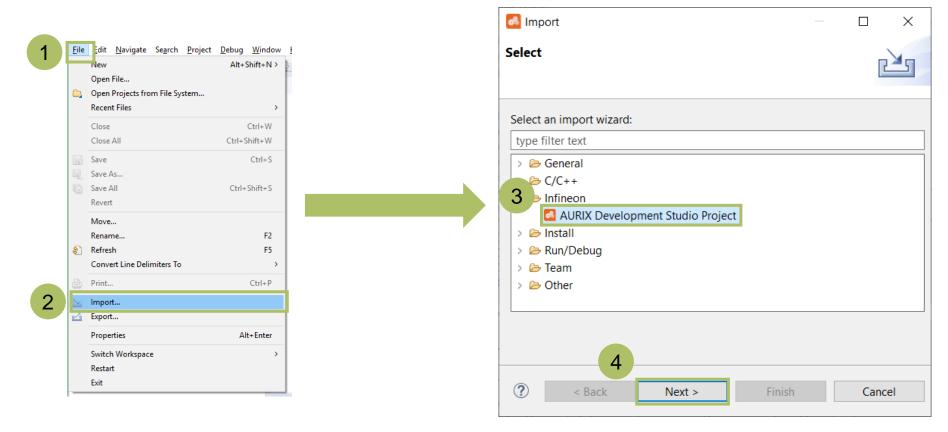
> By pressing "Finish" (7), a new project is created

🚳 New AURIX Development Studio Project			— 🗆	\times	
New AURIX Project					
Search Search boards and devices					File Edit Navigate Search Project De
Device TC29xTP_B-Step TC29xTA/TX/TF_B-Step TC27xTP_D-Step TC27xTF_D-Step TC26xD_B-Step TC26xD_B-Step TC23xLP_A-Step TC23xLP_A-Step TC23xLA/LX_A-Step TC21xL_A-Step TC21xL_A-Step TC21xL_A-Step TC20xVD_P_Cten CPUS : 6 TriCore Frequency : 300 MHz Flash Size : 16 MB DataFlash0 Size : 1024 KB	Board KIT_AURIX_TC275_LITE KIT_AURIX_TC275_ARD_SB KIT_AURIX_TC265_TFT_BC-Step KIT_AURIX_TC237_TFT_AC-Step Custom Board ✓ AURIX TC3xx KIT_A26_TC397_5V_TFT KIT_A26_TC397_5V_TFT KIT_A26_TC375_SV_TFT KIT_A26_TC375_SV_TFT KIT_A26_TC375_ARD_SB KIT_A26_TC375_SV_TET SRAM_Size : 6.5 MB	AURIX TC275 lite Kit hitex ShieldBuddy APPLICATION KIT TC2x5 V2.0 APPLICATION KIT TC2X7 V1.1 APPLICATION KIT TC3X7 V2.0 APPLICATION KIT TC3X7 V2.0 APPLICATION KIT TC3X7 V2.0 AURIX TC375 lite Kit hitex ShieldBuddy ADDUCATION KIT TC3Y7 V2.0	Can		C/C++ Projec X Project Explo C/C++ Projec X Project Explo X X MyProject [Active - Debug] X X MyProject [Active



Import project (Infineon Code Examples Repository) - 1

- Alternatively, it is possible to import an example project using File >> "Import..." utility (1-2) and selecting Infineon >> "AURIX Development Studio Project" type (3)
 At the end proces "Next" (4)
- > At the end, press "Next" (4)





Import project (Infineon Code Examples Repository) - 2

5

> Hint: Clicking on an example project (5) in the list shows the example description (6)

Select a Code Examples rep	pository		Repository root			
nfineon Code Examples Re	pository	~				Brov
earch Code Examples						
Search Code Examples						
elect a project to import						392 Pr
Name	Abstract	Boards		Last Updated	Documents	Keywords
ADC_Filtering_1_KIT_TC	³⁹ Four EVADC channels are used to convert a analog signal with different filters enabled	APPLICATION KIT	TC3X7 V2.0, KIT_A2G_T	18.12.2020	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_F
ADC_Group_Scan_1_KIT	is configured to measure multiple analog	AURIX IC275 lite	Kit, KIT_AURIX_TC275_L	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_C
ADC_Group_Scan_1_KIT	is configured to measure multiple analog		TC2X7 V1.1, KIT_AURIX	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_G
ADC_Queued_Scan_1_K	is configured to measure multiple analog		Kit, KIT_AURIX_TC275_L	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
ADC_Queued_Scan_1_K	is configured to measure multiple analog	APPLICATION KIT	TC2X7 V1.1, KIT_AURIX	18.12.2020	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
ADC_Queued_Scan_1_K	The Enhanced Versatile Analog-to-Digital (EVADC) is configured to measure multiple	AURIX TC334 lite	Kit, KIT_A2G_TC334_LIT	16.12.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
Description of ADC_Queued	d_Scan_1_KIT_TC275_LK	,				
he Queued Request of the can the analog inputs chanr	Versatile Analog-to-Digital Converter (VADC nels 5, 6 and 7 of group 4.) module is used to	o continuously	6		



Import project (Infineon Code Examples Repository) - 3

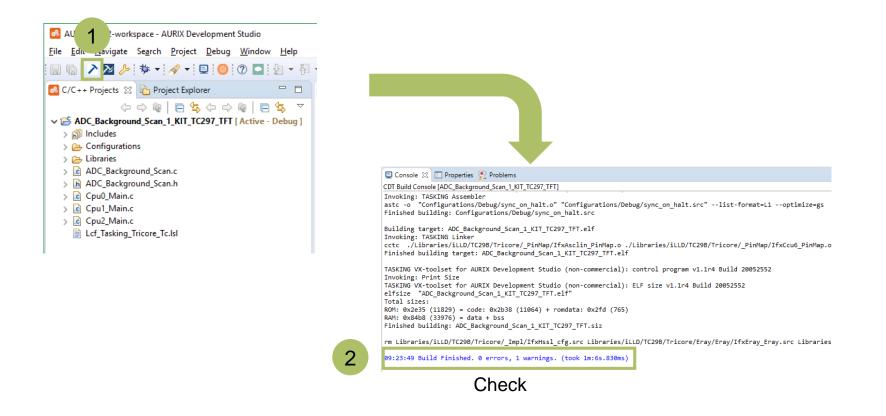
Select (double-click) an example project (7) from the list and press "Finish" (8). This creates a local copy of the example in your workspace directory and opens the project

Select a Code Examples rep Infineon Code Examples Re		Repository root			В	rov
earch Code Examples	· · · · · · · · · · · · · · · · · · ·					
Search Code Examples						_
elect a project to import						
Name	Abstract	Boards/Kits	Last Undated	Documents	Keywords	
ADC_Background_Sca	is configured to measure multiple analog signals in	a sec APPLICATION KIT TC2X7 V1	11.02.2020	https://www.infin	e ADC, backgro	un
ADC_Group_Scan_1_KI	The Versatile Analog-to-Digital Converter (VAUC)			https://www.infin	e ADC, ADC_Gr	ou
ADC_Single_Channel_1	The Versatile Analog-to-Digital Converter (VADC)			https://www.infin	ADC, ADC_Sir	gl
ASCLIN_LIN_Master_1_	An ASCLIN module is configured as LIN master to send "Hello World!"	APPLICATION KIT TC2X7 V1		https://www.infin	ASCLIN, ASCL	IN
ASCLIN_Shell_UART_1_	A Shell is used to parse a command line and call the corresponding command execution. The ASCL	IN mc APPLICATION KIT TC2X7 V1	11.02.2020	https://www.infin	ASC, ASCLIN	Sh
ASCLIN_Shell_UART_1	A Shell is used to parse a command line and call	hiter Chield Buddy, KIT ALID		https://www.infin	ASC, ASCLIN	Sh
ASCLIN_Shell_UART_1_	A Shell is used to parse a command line and call the corresponding command execution. The ASCL		11.02.2020	https://www.infin	ASC, ASCLIN	Sh
ASCLIN_Shell_UART_1_	A Shell is used to parse a command line and call the corresponding command execution. The ASCL			https://www.infin	ASC, ASCLIN	Sh
ASCLIN_SPI_Master_1_	An ASCLIN module configured as SPI master sends two bytes message.	APPLICATION KIT TC2X7 V1		https://www.infin	ASCLIN, ASCL	IN
	An ASCHINI module configured for HAPT communi	ication				
The Background Scan mode	round_Scan_1_KIT_TC297_TFT e of the Analog-to-Digital Converter (ADC) module i e channels 0 to 3 of the group 0.	s configured to measure the				



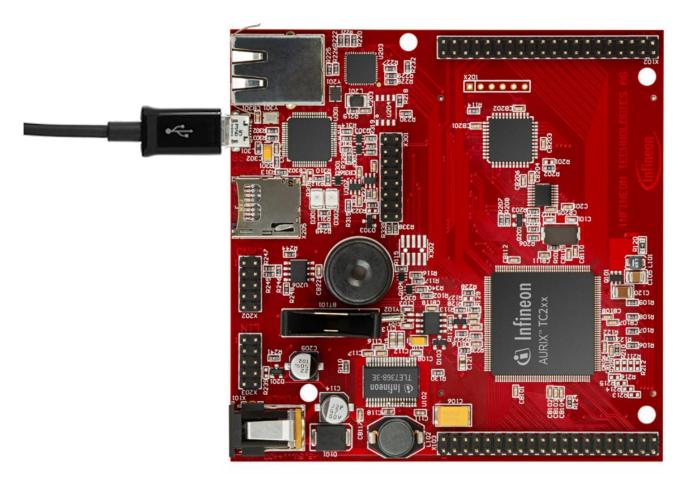


 Before debugging, it is necessary to build the project. Press the "Build Active Project" icon (1) and when the build is finished, check that there are no compiling errors (2)



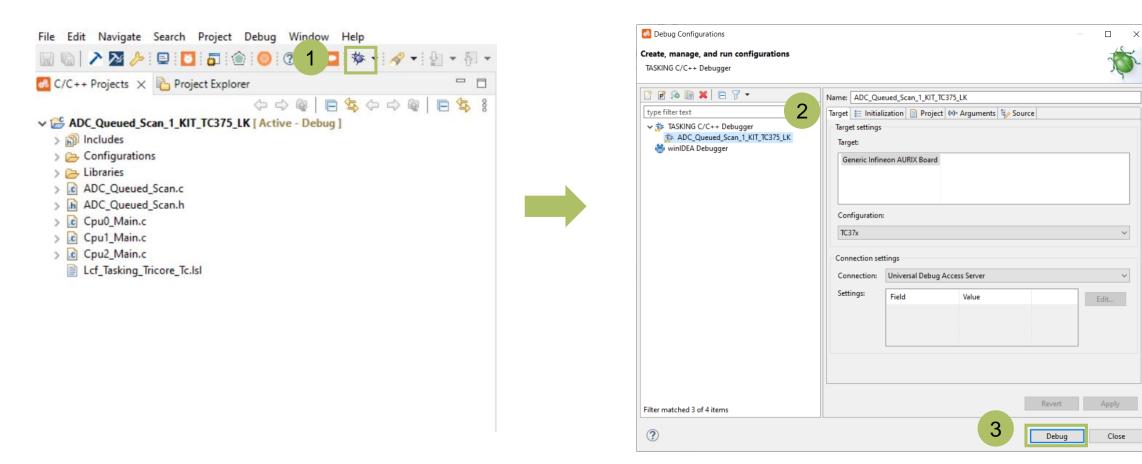


> Connect your device via an USB cable to the PC





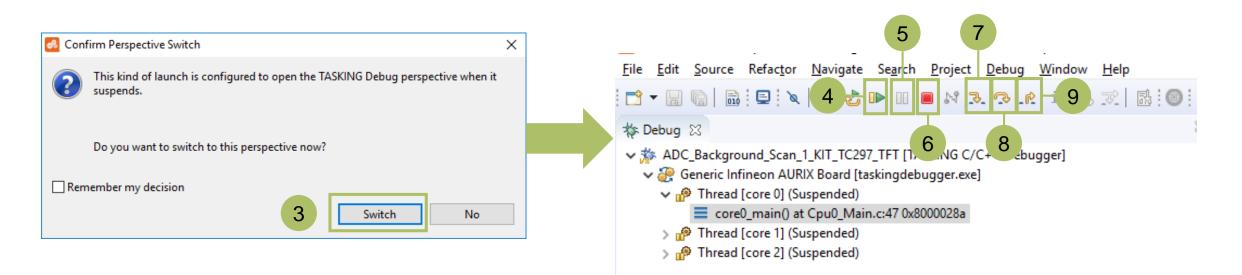
In order to flash and debug the code, press the "Debug Active Project" icon (1), chose a debugger (2) and then press the "Debug" button on the "Debug Configurations" window (3)





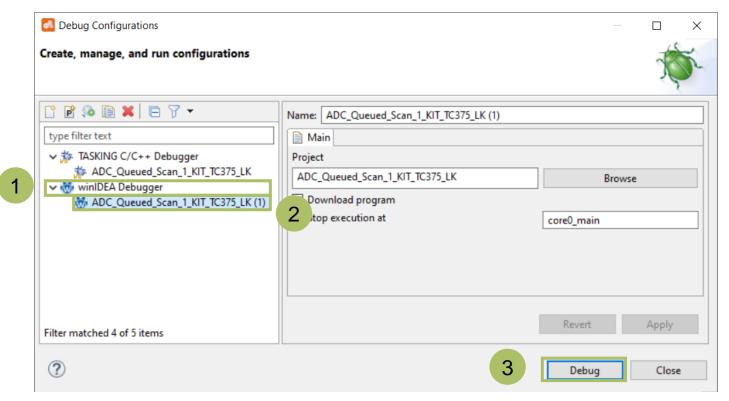
When using TASKING C/C++ Debugger:

- > Switch the perspective when asked (3) and press "Resume" (4) to run the code
- > While running, the code can be stopped with the "Suspend" button (5)
- > To terminate the debug session, press the "Terminate" button (6)
- Additionally, in the Debug perspective, it is also possible to run the code in single or multiple steps with the buttons "Step Into" (7), "Step Over" (8) and "Step Return" (9)



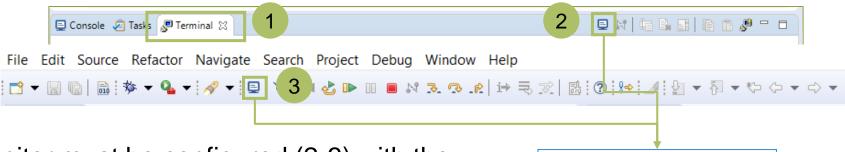


- > To select winIDEA as debugger:
 - Double click on "winIDEA Debugger" to create a configuration (1)
 - Select the configuration (2)
 - Press the Debug button (3)





A serial monitor is open by default (1) in the Debug Perspective inside the AURIX[™] Development Studio, or it can be open manually from the terminal icon (3)



- The serial monitor must be configured (2-3) with the following parameters to enable the communication between the board and the PC:
 - Serial port number
 - Speed (baud rate)
 - Data size
 - Parity
 - Stop bits

_	\		
🚳 Launch Te	rminal —		×
Settings			
Serial port:	COM3		~
Baud rate:	115200		\sim
Data size:	8		\sim
Parity:	None		\sim
Stop bits:	1		\sim
Encoding:	Default (ISO-88	59-1)	\sim
?	OK	Cance	el



Additional material - 1

- All the imported examples from Infineon come with a tutorial explaining the needed HW/SW setup, the code and how to run and test the example
- The tutorial is accessible from the AURIX[™] Development Studio by Ctrl + click on the link (1) in the Cpu0_Main.c file

C Cp	bu0_Main.c 🔀
22 23 24 25 26	* WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN * CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS * IN THE SOFTWARE.
27© 28 29 30 31	<pre>/*\title ADC background scan source * \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request. * \description The Background Scan mode of the Analog-to-Digital Converter (ADC) module is configured to measure the * analog signals applied to the channels 0 to 3 of the group 0. *</pre>
32 33	* \name ADC_Background_Scan_1_KIT_TC297_TFT * \version V1.0.0
34 35 36	<pre>* \board APPLICATION KIT TC2X7 V1.1, KIT_AURIX_TC297_TFT_BC-Step, TC29xTA/TX_BC-step * \keywords ADC_background_scan_conversion_VADC_ADC_Background_Scan_1_AURIX_ * \documents https://www.infineon.com/aurix-expert-training/Infineon-AURIX_ADC_Background_Scan_1_KIT_TC297_TFT-TR-v01_00_00-EN.pdf</pre>
37 38 39	* \documents nccps.//www.infineon.com/aurix-experc-craining/rc230_iccp_on_i_g_i_ir_g.cimm * \lastUpdated 2020-02-11 ***********************************
41	<pre>#include "Ifx_Types.h" #include "IfxCpu.h" #include "IfxScuudt.h"</pre>
43 44	<pre>#include "ADC_Background_Scan.h"</pre>
45 46	<pre>IfxCpu_syncEvent g_cpuSyncEvent = 0;</pre>
	int core0_main(void)
48 49 50	<pre>{ IfxCpu_enableInterrupts(); </pre>
51⊖ 52 53	/* !!WATCHDOG0 AND SAFETY WATCHDOG ARE DISABLED HERE!! * Enable the <u>watchdogs</u> and service them periodically if it is required */
54 55 56	IfxScuWdt_disableCpuWatchdog(IfxScuWdt_getCpuWatchdogPassword()); IfxScuWdt_disableSafetyWatchdog(IfxScuWdt_getSafetyWatchdogPassword());
57 58	/* Wait for CPU sync event */ IfxCpu_emitEvent(&g_cpuSyncEvent);



Additional material - 2

From the same Cpu0_Main.c file, it is possible to download the Infineon Low Level Drivers documentation (2) for the specific device used in the example

Cp	bu0_Main.c 🔀			
22 23	* WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN			
24	* CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS			
25	* IN THE SOFTWARE.			
26				
27⊝	/*\title ADC background scan source			
28	* \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request.			
29	* \description The Background Scan mode of the Analog-to-Digital Converter (ADC) module is configured to measure the			
30	* analog signals applied to the channels 0 to 3 of the group 0.			
31				
32	* \name ADC_Background_Scan_1_KIT_TC297_TFT			
33	* \version V1.0.0			
34	* \board APPLICATION KIT TC2X7 V1.1, KIT_AURIX_TC297_TFT_BC-Step, TC29xTA/TX_BC-step			
35	* \keywords ADC, background scan, conversion, VADC, ADC_Background_Scan_1, AURIX			
36	* \documents_https://www.infineon.com/aurix-expert-training/Infineon-AURIX_ADC_Background_ScrT_TC297_TFT-TR-v01_00_00-EN.pdf			
37	* \documents https://www.infineon.com/aurix-expert-training/TC29B_iLLD_UM_1_0_1_11_0.chm 2			
38	* \lastUpdatter 2020 02 11			
39				
	40 #include "Ifx_Types.h" 41 #include "IfxCpu.h"			
	+			
	42 #Include 'IAC Background Scan.h"			
44	#Include Abc_background_scan.n			
	<pre>IfxCpu syncEvent g cpuSyncEvent = 0;</pre>			
46				
	int core0_main(void)			
48				
49	IfxCpu_enableInterrupts();			
50				
510				
52	* Enable the <u>watchdogs</u> and service them periodically if it is required */			
53 54				
54	IfxScuWdt_disableCpuWatchdog(IfxScuWdt_getCpuWatchdogPassword()); IfxScuWdt disableSafetyWatchdog(IfxScuWdt getSafetyWatchdogPassword());			
56	11x5cumut_u1sablesaretywatchuog(11x5cumut_BetsaretywatchuogPassworu());			
57	/* Wait for CPU sync event */			
58	/ walf for the syncevent /; Ifxcpu emittevent(& cpusyncevent);			



Additional material - 3

 Hint: both the example's tutorial and the iLLD documentation can be opened by Right clicking on the project name and pressing the "Open documentation" utility (3)

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