## Getting Started with AURIX<sup>™</sup> Development Studio Installation and first steps

AURIX<sup>™</sup> Development Studio Training V1.0.15





#### Scope of work

This tutorial provides a guide for the user to:

- > Install AURIX<sup>™</sup> Development Studio V1.9.8
- > Create new project
- Import project (Infineon Code Examples Repository)
- > Build project
- > Debug project
- > Additional material



#### Download

The installation package of AURIX<sup>™</sup> Development Studio can be found here: <u>https://www.infineon.com/aurixdevelopmentstudio</u>



>

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Back

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> To install AURIX<sup>™</sup> Development Studio, launch the installation package and follow the steps:

E🗠 Setup - AURIX Development Studio version 1.9.8 — 🗆 🗙	Club Setup - AURIX Development Studio version 1.9.8 — 🛛 🗙	🛃 Setup - AURIX Development Studio version 1.9.8 — 🗆 🗙
License Agreement Please read the following important information before continuing.	Select Destination Location Where should AURIX Development Studio be installed?	Select Additional Tasks Which additional tasks should be performed?
Please read the following License Agreement. You must accept the terms of this agreement before continuing with the installation.	Setup will install AURIX Development Studio into the following folder.	Select the additional tasks you would like Setup to perform while installing AURIX Development Studio, then click Next.
Software License Agreement BY DOWNLOADING, INSTALLING AND/OR USING (INCLUDING COPYING) THE LICENSED SOFTWARE, LICENSEE AGREES TO BE BOUND BY THE TERMS AND CONDITIONS OF THIS AGREEMENT. IF LICENSEE DOES NOT AGREE TO ALL TERMS AND CONDITIONS OF THIS AGREEMENT, LICENSEE (INCLUDING YOU) SHALL NOT DOWNLOAD, INSTALL AND/OR USE (INCLUDING COPYING) THE LICENSED SOFTWARE	To continue, click Next. If you would like to select a different folder, click Browse.	Additional shortcuts:
○ I accept the agreement		
I do not accept the agreement	At least 4.41 GB of free disk space is required.	
Next Cancel	Back Next Cancel	Back Next Cancel
	Setup - AURIX Development Studio version 1.9.8 — 🗆 🗙	🛃 Setup - AURIX Development Studio version 1.9.8 — 🗆 🗙
	Completing the AURIX Development Studio Setup Wizard	Ready to Install Setup is now ready to begin installing AURIX Development Studio on your computer.
	Setup has finished installing AURIX Development Studio on your computer. The application may be launched by selecting the installed shortcuts.	Click Install to continue with the installation, or click Back if you want to review or change any settings.
	Click Finish to exit Setup.	Destination location: C:\Infinen\AURIX-Studio-1.9.8 Additional tasks: Additional shortcuts: Create a desktop shortcut

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### Install AURIX<sup>™</sup> Development Studio - 2

If DAS64 is not installed or outdated, it will be installed automatically during the AURIX™ Development Studio installation:

Installazione di DAS Installer	×	Installazione di DAS Installer	×	DAS Installaz	azione di DAS installer
Installazione - DAS		Cartella di installazione		Contratt	atto di licenza ggre i seguente contratto di licenza. È necessario accettare i termini contenuti in questo contratto prima di continuare l'installazione.
Installazione - DAS Cartella di installazione Contratto di licenza Collegamenti del menu Start Pronto all'installazione Installing Finished		Installazione - DAS Cartella di Installazione Contratto di Ilcenza Collegamenti del menu Start Pronto all'Imstallazione Installing Finished	Specificare la drectory in cui DAS verrà installato. C: Program Files (DAS64) Stogla	Cartella d Contratt Collegar	not destroy will continue to be governed by these ferms of use. Four
Successivo >	Esci		< Precedente Successivo > Annulla		< Precedente Successive > Annula
stallazione di DAS Installer	×	Installazione di DAS Installer	X	🔤 Installa	llazione di DAS Installer
installazione di DAS installer ompletamento della procedura guidata DAS	×	installazione di DAS Installer Pronto all'installazione	×	Collegar	Ilazione di DAS Installer amenti del menu Start lezonare il menu Start in cui si desidera oreare i collegamenti del programma. È anche possibile immettere un nome per oreare una nuova director
	×	_	S é ora pront per intére l'Installatione d DAS nel computer. L'Installatione utilizzerà 50.39 MB di spazio su disco.	Collegan Selez Cartella C Contretto Collegan	amenti dei menu Start in cui si desidera creare i colegamenti dei programma, È anche possibile immettere un nome per creare una nuova direct tacione : DAS



#### Workspace definition

→ After launching the AURIX<sup>™</sup> Development Studio, it is necessary to select a workspace

🚳 AURIX De	velopment Studio Launcher	×
	ectory as workspace	
AURIX Devel	opment Studio uses the workspace directory to store its preferences and development artifacts.	
Workspace:	C:\Users\MyUser\AURIX-v1.9.8-workspace $\checkmark$ <u>B</u> rowse	
	s the default and do not ask again	
▶ <u>R</u> ecent Wo	<u>L</u> aunch Cancel	



#### Data evaluation consent

- > On the first start you are asked for the consent to > You can later decide to modify your consent by send data to Infineon for evaluation purposes.
- You can choose which data to send.

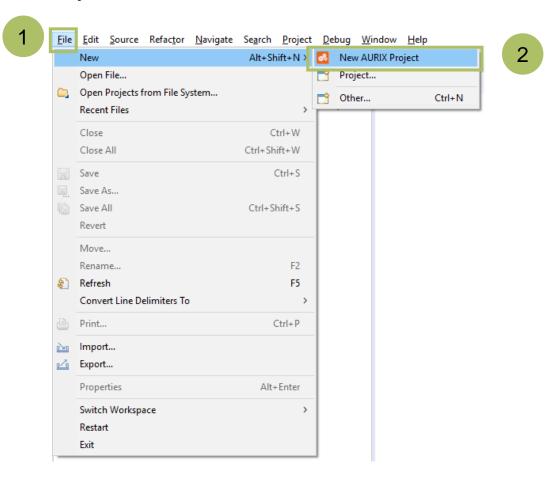
Data Evaluation
We process data, which is necessary for the functioning of our software.
provide technically required data to provide the desired functionality
With your consent, we collect optional telemetry data to improve our services. You can revoke your consent at any time within the settings for "data evaluation". Please select and confirm your choice:
<ul> <li>provide personalized data</li> <li>provide diagnosis and performance data</li> </ul>
Please visit Infineon's Privacy Policy for more information.
Accept

clicking on "Open Data Evaluation Settings" on the toolbar

d COM 👩 Preferences	>
type filter text > General > AURIX Development Studio ADS ChipCoach Library f Data Evaluation > C/C++ > Help > Install/Update iSYSTEM > Run/Debug > TASKING > Terminal > Version Control (Team)	Data Evaluation       Image: Constraint of the second
	Restore Defaults Apply



Once the program is started, a new project can be created by selecting File >> New >> "New AURIX Project"





- > From the "New AURIX Development Studio Project" window, choose a name for the new project (3)
- The "Use default location" checkbox should be set in order to create the project inside the current selected workspace

New AURIX Development Studio Project	— 🗆 X
ew AURIX Project	
pecify the name and the location of the new project	
Project name: MyProject	
Use default location	
ocation: C:\Users\MyUser\AURIX-v1.2.0-workspace\MyProject	B <u>r</u> owse
4	<u>N</u> ext > <u>F</u> inish Cancel



From the "New AURIX Development Studio Project" window, choose the device or the board. A specific device (5) or board (6) can be chosen from the left or right list. Furthermore, while selecting a board, the tool highlights the supported devices for that board and vice versa

Search boards and devices			
Device TC26xDA_B-Step TC23xLP_A-Step TC23xLA/LX_A-Step TC22xL_A-Step TC21xL_A-Step TC21xL_A-Step TC39xXX_B-Step TC39xXY_B-Step TC39xXP_B-Step TC37xTX_A-Step TC37xTY_A-Step TC36xDP_A-Step	Board V AURIX TC2xx KIT_AURIX_TC277_TFT_BC-Step KIT_AURIX_TC277_TFT_DC-Step KIT_AURIX_TC275_LITE KIT_AURIX_TC255_TFT_BC-Step KIT_AURIX_TC252_TFT_AC-Step Custom Board V AURIX TC3xx * KIT_A2G_TC387_5V_TFT KIT_A2G_TC387_5V_TFT	APPLICATION KIT TC2X7 V1.1 APPLICATION KIT TC2X7 V1.1 AURIX TC275 lite Kit hitex ShieldBuddy APPLICATION KIT TC2X5 V2.0 APPLICATION KIT TC2X7 V1.1 APPLICATION KIT TC2X4 V1.0 APPLICATION KIT TC3X7 V2.0	
TC33xLP_A-Step CPUs : 6 TriCore Frequency : 300 MHz Flash Size : 16 MB DataFlashØ Size : 1024 KB	KIT_A2G_TC377_5V_TFT	APPLICATION KIT TC3X7 V2.0	



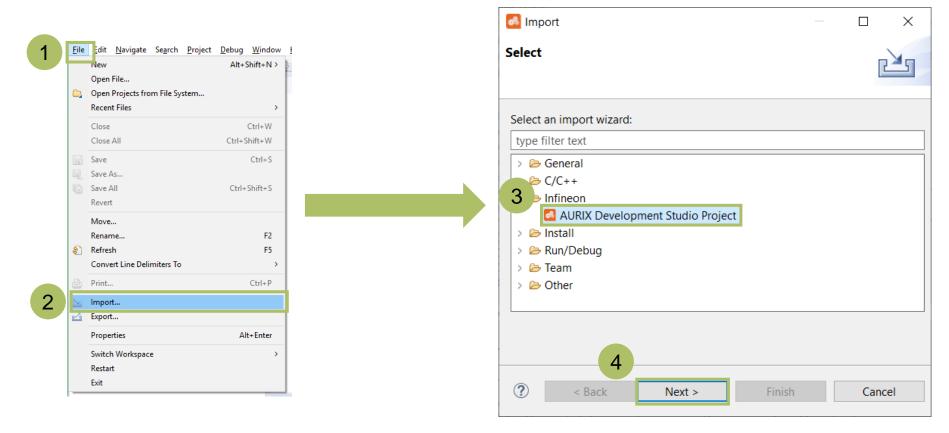
> By pressing "Finish" (7), a new project is created

🚳 New AURIX Development Studio Project			— 🗆	$\times$	
New AURIX Project					
Search Search boards and devices					File Edit Navigate Search Project De
Device TC29xTP_B-Step TC29xTA/TX/TF_B-Step TC27xTP_D-Step TC27xTF_D-Step TC26xD_B-Step TC26xD_B-Step TC23xLP_A-Step TC23xLP_A-Step TC23xLA/LX_A-Step TC21xL_A-Step TC21xL_A-Step TC21xL_A-Step TC20xVD_P_Cten CPUS : 6 TriCore Frequency : 300 MHz Flash Size : 16 MB DataFlash0 Size : 1024 KB	Board KIT_AURIX_TC275_LITE KIT_AURIX_TC275_ARD_SB KIT_AURIX_TC265_TFT_BC-Step KIT_AURIX_TC237_TFT_AC-Step Custom Board ✓ AURIX TC3xx KIT_A26_TC397_5V_TFT KIT_A26_TC397_5V_TFT KIT_A26_TC375_SV_TFT KIT_A26_TC375_SV_TFT KIT_A26_TC375_ARD_SB KIT_A26_TC375_SV_TET SRAM_Size : 6.5 MB	AURIX TC275 lite Kit hitex ShieldBuddy APPLICATION KIT TC2x5 V2.0 APPLICATION KIT TC2X7 V1.1 APPLICATION KIT TC3X7 V2.0 APPLICATION KIT TC3X7 V2.0 APPLICATION KIT TC3X7 V2.0 AURIX TC375 lite Kit hitex ShieldBuddy ADDUCATION KIT TC3Y7 V2.0	Can		C/C++ Projec X Project Explo C/C++ Projec X Project Explo C/C -+ Project [ Active - Debug ] Y S MyProject [ Active - Debug ] N N Includes D Configurations D CoulyMain.c D CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c CoulyMain.c



### Import project (Infineon Code Examples Repository) - 1

- Alternatively, it is possible to import an example project using File >> "Import..." utility (1-2) and selecting Infineon >> "AURIX Development Studio Project" type (3)
  At the and arrase "Next" (4)
- > At the end, press "Next" (4)





### Import project (Infineon Code Examples Repository) - 2

5

> Hint: Clicking on an example project (5) in the list shows the example description (6)

Select a Code Examples rep	pository		Repository root			
nfineon Code Examples Re	pository	~				Brov
earch Code Examples						
Search Code Examples						
elect a project to import						392 Pr
Name	Abstract	Boards		Last Updated	Documents	Keywords
ADC_Filtering_1_KIT_TC	<sup>39</sup> Four EVADC channels are used to convert a analog signal with different filters enabled	APPLICATION KIT	TC3X7 V2.0, KIT_A2G_T	18.12.2020	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_F
ADC_Group_Scan_1_KIT	is configured to measure multiple analog	AURIX IC275 lite	Kit, KIT_AURIX_TC275_L	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_C
ADC_Group_Scan_1_KIT	is configured to measure multiple analog		TC2X7 V1.1, KIT_AURIX	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, ADC_G
ADC_Queued_Scan_1_K	is configured to measure multiple analog		Kit, KIT_AURIX_TC275_L	29.06.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
ADC_Queued_Scan_1_K	is configured to measure multiple analog	APPLICATION KIT	TC2X7 V1.1, KIT_AURIX	18.12.2020	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
ADC_Queued_Scan_1_K	The Enhanced Versatile Analog-to-Digital (EVADC) is configured to measure multiple	AURIX TC334 lite	Kit, KIT_A2G_TC334_LIT	16.12.2021	https://www.infineon.com/aurix-expert-training/l	r ADC, queue
Description of ADC_Queueo	d_Scan_1_KIT_TC275_LK	,				
he Queued Request of the can the analog inputs chanr	Versatile Analog-to-Digital Converter (VADC nels 5, 6 and 7 of group 4.	) module is used to	o continuously	6		



### Import project (Infineon Code Examples Repository) - 3

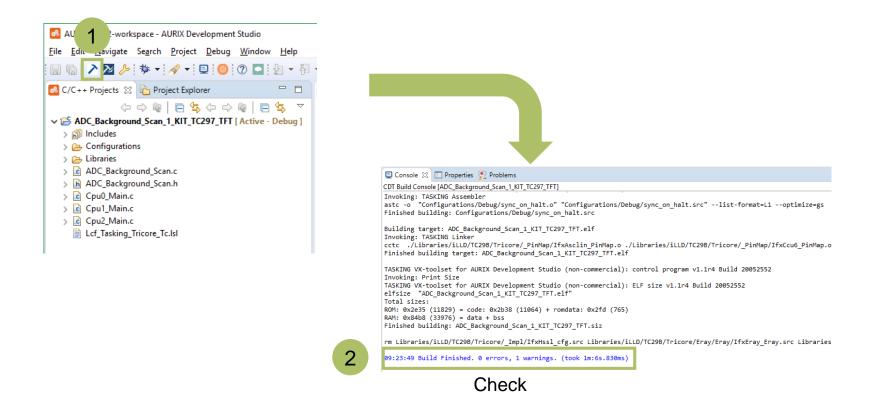
Select (double-click) an example project (7) from the list and press "Finish" (8). This creates a local copy of the example in your workspace directory and opens the project

Select a Code Examples rej Infineon Code Examples Re		Repository root				Brow
earch Code Examples Search Code Examples						
elect a project to import						
Name	Abstract	Roards/Kits	Last Undated	Documents	Keywords	
ADC_Background_Sca	is configured to measure multiple analog signals			https://www.infin	ADC, back	groui
ADC_Group_Scan_1_K	is configured to measure multiple analog signals	in a sec	11.02.2020	https://www.infin	ADC, ADC	Grou
ADC_Single_Channel_	The Versatile Analog_to_Digital Converter (VADC)			https://www.infin	ADC, ADC	Sing
ASCLIN_LIN_Master_1	An ASCLIN module is configured as LIN master to send "Hello World!"	APPLICATION KIT TC2X7 V1		https://www.infin	ASCLIN, AS	SCLIN
ASCLIN_Shell_UART_1	A Shell is used to parse a command line and call the corresponding command execution. The ASC	LIN mc APPLICATION KIT TC2X7 V1	11.02.2020	https://www.infin	e ASC, ASCL	IN_Sł
ASCLIN_Shell_UART_1	A Shell is used to parse a command line and call the corresponding command execution. The ASC	LIN mc hitex ShieldBuddy, KIT_AUR	111.02.2020	https://www.infin	ASC, ASCL	IN_Sł
ASCLIN_Shell_UART_1	A Shell is used to parse a command line and call the corresponding command execution. The ASC	LIN mc APPLICATION KIT TC2X7 V1	11.02.2020	https://www.infin	ASC, ASCL	IN_Sł
ASCLIN_Shell_UART_1	A Shell is used to parse a command line and call the corresponding command execution. The ASC			https://www.infin	ASC, ASCL	IN_Sł
ASCLIN_SPI_Master_1_	An ASCLIN module configured as SPI master send two bytes message.	ds a APPLICATION KIT TC2X7 V1		https://www.infin	ASCLIN, AS	SCLIN
Description of ADC Backg	An ASCUN module configured for UAPT communication of the second s	nication				
The Background Scan mod	e of the Analog-to-Digital Converter (ADC) module le channels 0 to 3 of the group 0.	e is configured to measure the				



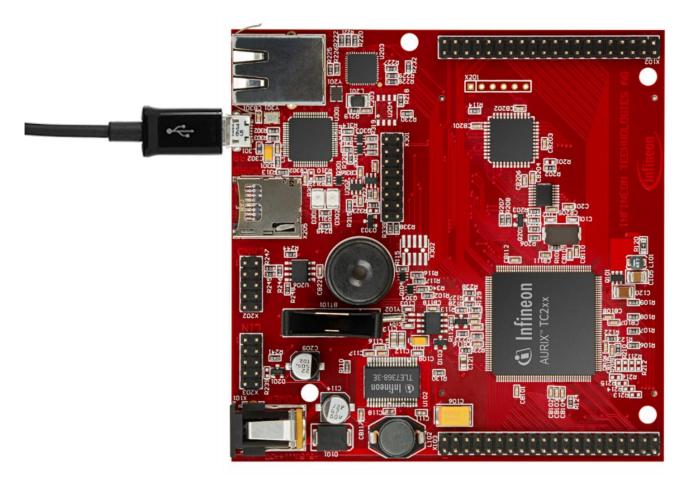


 Before debugging, it is necessary to build the project. Press the "Build Active Project" icon (1) and when the build is finished, check that there are no compiling errors (2)



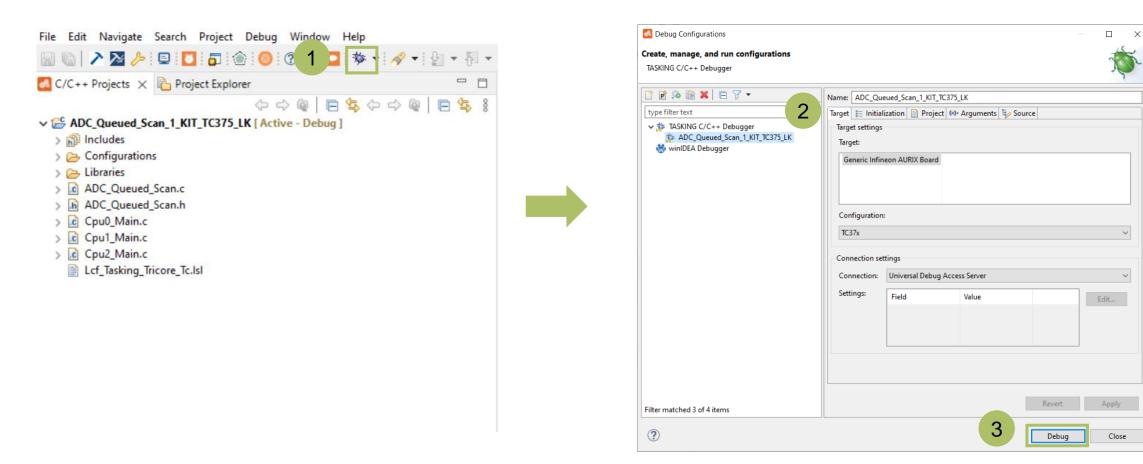


> Connect your device via an USB cable to the PC





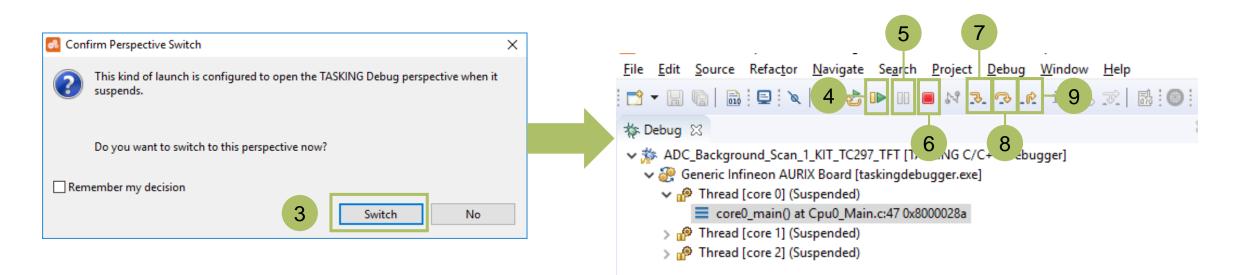
In order to flash and debug the code, press the "Debug Active Project" icon (1), chose a debugger (2) and then press the "Debug" button on the "Debug Configurations" window (3)





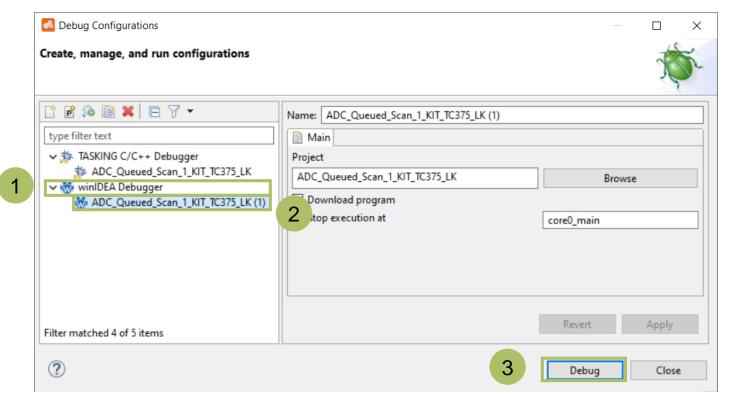
When using TASKING C/C++ Debugger:

- > Switch the perspective when asked (3) and press "Resume" (4) to run the code
- > While running, the code can be stopped with the "Suspend" button (5)
- > To terminate the debug session, press the "Terminate" button (6)
- Additionally, in the Debug perspective, it is also possible to run the code in single or multiple steps with the buttons "Step Into" (7), "Step Over" (8) and "Step Return" (9)



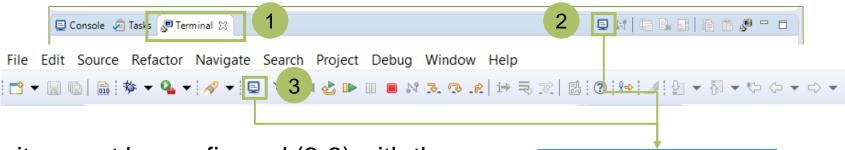


- > To select winIDEA as debugger:
  - Double click on "winIDEA Debugger" to create a configuration (1)
  - Select the configuration (2)
  - Press the Debug button (3)





A serial monitor is open by default (1) in the Debug Perspective inside the AURIX<sup>™</sup> Development Studio, or it can be open manually from the terminal icon (3)



- The serial monitor must be configured (2-3) with the following parameters to enable the communication between the board and the PC:
  - Serial port number
  - Speed (baud rate)
  - Data size
  - Parity
  - Stop bits

💰 Launch Te	rminal —		×
Settings			
Serial port:	COM3		~
Baud rate:	115200		$\sim$
Data size:	8		$\sim$
Parity:	None		$\sim$
Stop bits:	1		$\sim$
Encoding:	Default (ISO-88	59-1)	~
?	ОК	Cance	el



#### Additional material - 1

- All the imported examples from Infineon come with a tutorial explaining the needed HW/SW setup, the code and how to run and test the example
- The tutorial is accessible from the AURIX<sup>™</sup> Development Studio by Ctrl + click on the link (1) in the Cpu0\_Main.c file

Cp	bu0_Main.c 🔀
22 23 24 25 26	* WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN * CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS * IN THE SOFTWARE.
27© 28 29 30 31	<pre>/*\title ADC background scan source * \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request. * \description The Background Scan mode of the Analog-to-Digital Converter (ADC) module is configured to measure the * analog signals applied to the channels 0 to 3 of the group 0. *</pre>
32 33	* \name ADC_Background_Scan_1_KIT_TC297_TFT * \version V1.0.0
34 35 36	<pre>* \board APPLICATION KIT TC2X7 V1.1, KIT_AURIX_TC297_TFT_BC-Step, TC29xTA/TX_BC-step * \keywords ADC_background scan_conversion_VADC_ADC_Background_Scan_1_AURIX_ * \documents https://www.infineon.com/aurix-expert-training/Infineon-AURIX_ADC_Background_Scan_1_KIT_TC297_TFT-TR-v01_00_00-EN.pdf</pre>
37 38 39	* \documents/www.infineon.com/aurix-expertertraining/ic230_icco_on_i_o.com * \lastUpdated 2020-02-11
41	<pre>#include "Ifx_Types.h" #include "IfxStuht" #include "IfxStuht"</pre>
44	#include "ADC_Background_Scan.h"
45 46	<pre>IfxCpu_syncEvent g_cpuSyncEvent = 0;</pre>
	int core0_main(void)
48 49 50	<pre>{     IfxCpu_enableInterrupts();</pre>
51⊖ 52 53	/* !!WATCHDOG0 AND SAFETY WATCHDOG ARE DISABLED HERE!! * Enable the <u>watchdogs</u> and service them periodically if it is required */
54 55 56	IfxScuWdt_disableCpuWatchdog(IfxScuWdt_getCpuWatchdogPassword()); IfxScuWdt_disableSafetyWatchdog(IfxScuWdt_getSafetyWatchdogPassword());
57 58	/* Wait for CPU sync event */ IfxCpu_emitEvent(&g_cpuSyncEvent);



#### Additional material - 2

From the same Cpu0\_Main.c file, it is possible to download the Infineon Low Level Drivers documentation (2) for the specific device used in the example

Cp	bu0_Main.c 🔀
22 23	* WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN
24	* CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS
25	* IN THE SOFTWARE.
26	
27⊝	/*\title ADC background scan source
28	* \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request.
29	* \description The Background Scan mode of the Analog-to-Digital Converter (ADC) module is configured to measure the
30	* analog signals applied to the channels 0 to 3 of the group 0.
31	
32	* \name ADC_Background_Scan_1_KIT_TC297_TFT
33	* \version V1.0.0
34	* \board APPLICATION KIT TC2X7 V1.1, KIT_AURIX_TC297_TFT_BC-Step, TC29xTA/TX_BC-step
35	* \keywords ADC, background scan, conversion, VADC, ADC_Background_Scan_1, AURIX
36	* \documents_https://www.infineon.com/aurix-expert-training/Infineon-AURIX_ADC_Background_ScrT_TC297_TFT-TR-v01_00_00-EN.pdf
37	* \documents https://www.infineon.com/aurix-expert-training/TC29B_iLLD_UM_1_0_1_11_0.chm 2
38	* \lastUpdatter 2020 02 11
39	
	<pre>#include "Ifx_Types.h" #include "Ifx_Cpu.h"</pre>
	#include IfxScuddt.h"
	Hinclude "Hosciwatch"
44	#Include Abc_background_scan.n
	<pre>IfxCpu syncEvent g cpuSyncEvent = 0;</pre>
46	
	int core0_main(void)
48	
49	IfxCpu_enableInterrupts();
50	
510	
52	* Enable the <u>watchdogs</u> and service them periodically if it is required */
53 54	
54	IfxScuWdt_disableCpuWatchdog(IfxScuWdt_getCpuWatchdogPassword()); IfxScuWdt disableSafetyWatchdog(IfxScuWdt getSafetyWatchdogPassword());
56	11x5cumut_u1sablesaretywatchuog(11x5cumut_BetsaretywatchuogPassworu());
57	/* Wait for CPU sync event */
58	/ walf for the syncevent /; Ifxcpu emittevent(& cpusyncevent);



### Additional material - 3

 Hint: both the example's tutorial and the iLLD documentation can be opened by Right clicking on the project name and pressing the "Open documentation" utility (3)

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ADC_Background_Scan_1_KIT_TC297_TFT [ Active - Debug ]													
		Binaries			New					>			
	_	Includes Configurat Debug Libraries	itions		Go Into								
	_				Open in	New W	/indow						
	=		kground_Sca		Index					>			
	_		kground_Sca		Build Co	onfigura	ations			>			
	_	Cpu0_Mai Cpu1_Mai			Build Ta	rgets				>			
	_	Cpu2_Mai			Build Pr	oject							
		-	kground_Sca		Clean P	roject							
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					Rename								
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				4	Export								
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					Close Pr	roject							
				0	Run As					>			
				*	Debug A	As				>			
					Profile A	١s				>			
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				dig .	Edit Pro	ject Me	tadata		_			-	
			L	afi	Open de	ocumer	ntation					3	
				*	Run C/C	C++ Co	de Analy	sis					
					Properti	es			Alt+Ent	er			

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Document reference Getting Started with AURIX<sup>™</sup> Development Studio V1.0.15

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