

# ASCLIN\_UART\_1 for KIT\_AURIX\_TC397\_TFT

UART Communication via ASCLIN module

AURIX™ TC3xx Microcontroller Training  
V1.0.0



[Please read the Important Notice and Warnings at the end of this document](#)

## Scope of work

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**An ASCLIN module configured for UART communication sends "Hello World!" and receives the string via the internal loopback.**

The string "Hello World!" is sent and received via UART through one pin due to the internal loopback. The data can be visualized using an oscilloscope.

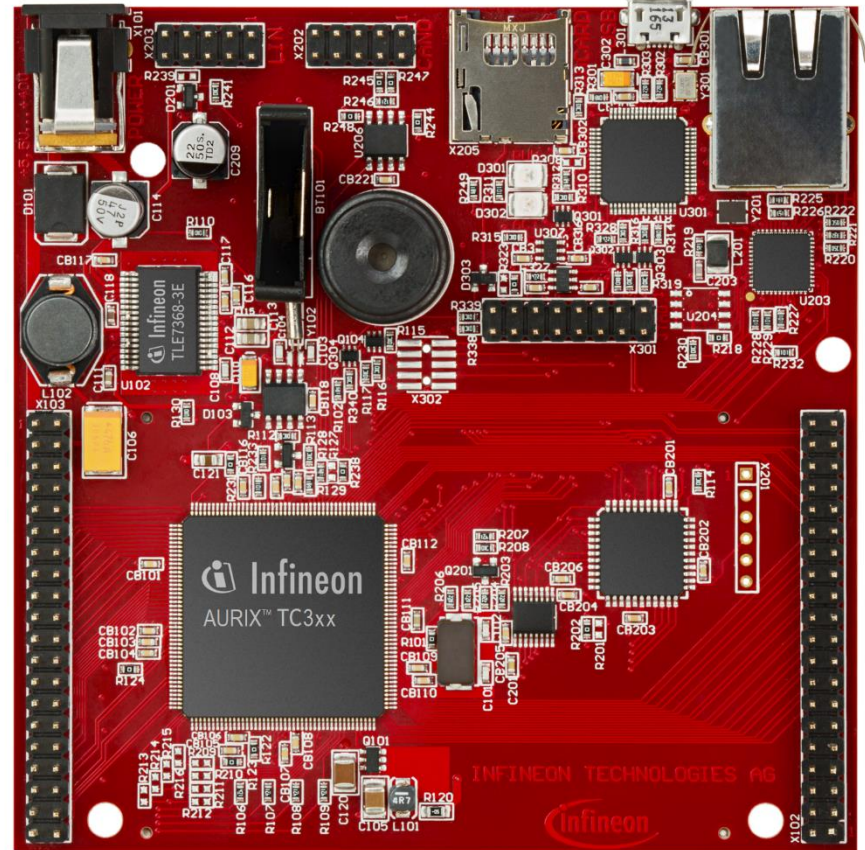
# Introduction

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- › The Asynchronous/Synchronous Interface (ASCLIN) module enables asynchronous/synchronous serial communication with external devices. Among others, it supports asynchronous reception/transmission (UART) for communication.
- › For test purposes, the transmit pin (TX) and receive pin (RX) can be shorted internally on-chip (loopback mode).

# Hardware setup

This code example has been developed for the board KIT\_A2G\_TC397\_5V\_TFT.



# Implementation

## Configuration of the ASCLIN module:

Configuration of the ASCLIN module for UART communication is done in the setup phase by initializing an instance of the ***IfxAsclin\_Asc\_Config*** structure with the following parameters:

- › ***baudrate*** – structure to set the actual communication speed in bit/s
- › ***interrupt*** – structure to set:
  - transmit and receive interrupt priorities (***txPriority***, ***rxPriority***)
  - ***typeOfService*** – defines which service provider is responsible for handling the interrupt, which can be any of the available CPUs, or the DMA
- › ***pins*** – structure to set which GPIO port pins are used for the communication
- › ***rxBuffer***, ***rxBufferSize***, ***txBuffer***, ***txBufferSize*** – to configure the buffers that will hold the incoming/outgoing data

The function ***IfxAsclin\_Asc\_initModuleConfig()*** fills the configuration structure with default values and ***IfxAsclin\_Asc\_initModule()*** initializes the module with the user configuration.

All the above functions can be found in the iLLD header ***IfxAsclin\_Asc.h***.

# Implementation

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## The UART send/receive function:

- › Sending the string “Hello World!” is implemented inside the function ***send\_receive\_ASCLIN\_UART\_message()*** which is called once after initialization of the ASCLIN module.
- › This function calls ***lfxAsclin\_Asc\_write()*** and ***lfxAsclin\_Asc\_read()*** which are provided by the iLLD header ***lfxAsclin\_Asc.h***.

# Implementation

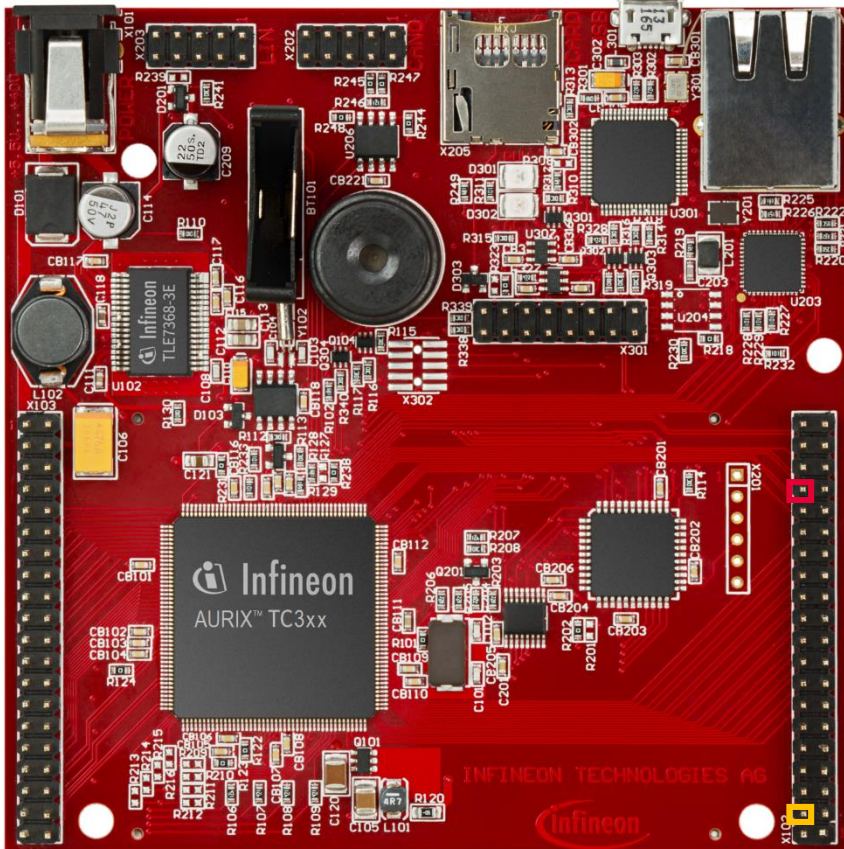
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## The UART send/receive function:

- › The UART frame configured for 115200/8-N-1 consists of different parts:
  - One start bit which is “0”
  - Eight bits of data
  - One stop bit which is “1”
  
- › Each time when the last byte is taken out of the transmit FIFO (size is 16-bytes), the Transmit FIFO Level (TFL) flag is set and the interrupt service routine ***asclin0TxISR()*** is entered. The ISR calls ***IfxAsclin\_Asc\_isrTransmit()*** which refills the FIFO with the remaining bytes to be transmitted and clears the interrupt flag.
  
- › Each time when an UART byte is received, the Receive FIFO Level (RFL) flag is set and the interrupt service routine ***asclin0RxISR()*** is entered. The ISR calls ***IfxAsclin\_Asc\_isrReceive()*** which moves the received byte to the global array ***g\_ascRxBuffer*** and clears the interrupt flag.

# Run and Test

An oscilloscope probe must be connected to the UART TX/RX pin (P15.5) to observe the UART signal.



TX/RX

Ground

|        | X102 |    |        |
|--------|------|----|--------|
| P14.5  | 40   | 39 | P14.4  |
| P33.10 | 38   | 37 | P20.9  |
| P15.7  | 36   | 35 | P15.6  |
| P15.5  | 34   | 33 | P15.4  |
| P15.3  | 32   | 31 | P15.2  |
| P22.3  | 30   | 29 | P22.2  |
| P22.1  | 28   | 27 | P22.0  |
| P33.11 | 26   | 25 | P23.4  |
| P23.3  | 24   | 23 | P23.2  |
| P23.1  | 22   | 21 | P23.0  |
| P33.6  | 20   | 19 | P33.8  |
| P33.12 | 18   | 17 | P33.1  |
| P33.2  | 16   | 15 | P33.3  |
| P33.4  | 14   | 13 | P33.5  |
| AN0    | 12   | 11 | AN8    |
| AN2    | 10   | 9  | AN3    |
| AN11   | 8    | 7  | AN13   |
| AN20   | 6    | 5  | AN21   |
| GND    | 4    | 3  | GND    |
| V_UC   | 2    | 1  | VCC_IN |



# Run and Test

After code compilation and flashing the device, perform the following steps:

- > Connect the oscilloscope probe to the TX/RX pin (P15.5)
- > Reset and run the program by pressing the PORST push button
- > Check the oscilloscope for the UART signal:



→ "Hello World!"

# Run and Test

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An additional test without using an oscilloscope can be performed with the debugger.

- › Before transmission, the buffer ***g\_txData*** is filled with the message "Hello World!" and the buffer ***g\_rxData*** is empty.
  
- › After transmission, both buffers should hold the same message:
  - By using the debugger, you can watch the content of both buffers before and after transmission by setting a breakpoint to ***send\_receive\_ASCLIN\_UART\_message()***.
  - When reaching this breakpoint, check the content of both buffers (it should be different).
  - After stepping over this function, the content of the buffers must be equal.

# References



- › AURIX™ Development Studio is available online:
- › <https://www.infineon.com/aurixdevelopmentstudio>
- › Use the „*Import...*“ function to get access to more code examples.



- › More code examples can be found on the GIT repository:
- › [https://github.com/Infineon/AURIX\\_code\\_examples](https://github.com/Infineon/AURIX_code_examples)



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**Email: [erratum@infineon.com](mailto:erratum@infineon.com)**

**Document reference**

**ASCLIN\_UART\_1\_KIT\_TC397\_TFT**

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