

SP5T Diversity Antenna Switch with GPIO Interface

BGS15GA14

About this document

Scope and purpose

This application note describes Infineon's SP5T Diversity Antenna Switch with GPIO Interface: BGS15GA14 as switch for Mobile phones in different RF FE applications such as diversity switch.

1. This application note documents the behavior of BGS15GA14 for different LTE bands (Band 1, 2, 5, 7 and Band 13).
2. The BGS15GA14 is used in this document.
3. General purpose wideband RF switch for diversity application.
4. Key Parameters:
 - 5 high-linearity TRx paths with power handling capability of up to 30 dBm
 - Low insertion loss
 - Low harmonic generation
 - High port-to-port-isolation
 - Suitable for Edge / CDMA2000 / LTE / WCDMA applications

BGS15GA14 is part of the family BGS1xGA14:

- BGS13GA14: Single Pole Three Throw
- BGS14GA14: Single Pole Four Throw
- BGS15GA14: Single Pole Five Throw
- BGS16GA14: Single Pole Six Throw
- BGS17GA14: Single Pole Seven Throw
- BGS18GA14: Single Pole Eight Throw

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1) The graphs are generated with the simulation program AWR Microwave Office®.

Introduction

1 Introduction

Infineon's RF CMOS switches are the first on the market to be based purely on standard industrial CMOS processes that offer low insertion loss, high isolation and low harmonics generation for high-volume production. They are widely used for band selection/switching or diversity switching at the antenna or different RF paths within the RF Front-End (FE).

The BGS15GA14 RF MOS switch is specifically designed for cell phone and mobile applications. Any of the 3 ports can be used as termination of the diversity antenna handling up to 30 dBm.

This SP5T offers low insertion loss and high robustness against interferer signals at the antenna port and low harmonic generation in termination mode. The on-chip controller integrates CMOS logic and level shifters, driven by control inputs from 1.35 V to VDD. The BGS15GA14 RF Switch is manufactured in Infineon's patented MOS technology, offering the performance of GaAs with the economy and integration of conventional CMOS including the inherent higher ESD robustness. The device has a very small size of only 2x2 mm² and a maximum height of 0.60 mm.

The recent trend of smartphone and tablet users to download more and more data anytime and anywhere increases the demand for more bandwidth and for an additional receiver channel called the diversity path. To select the right receive band, a diversity switch with low insertion loss and excellent RF performance is one method of choice. Nowadays, diversity switches covering up to 7 or more different UMTS/LTE bands are becoming more and more popular in smartphones and tablets (Overview LTE Bands).

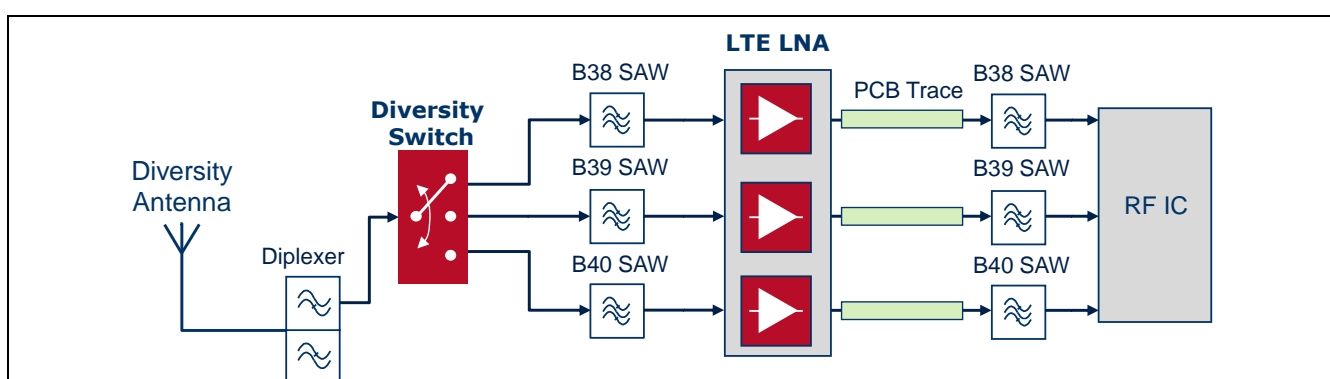


Figure 1 Example of TD-LTE band for diversity path

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Introduction

Table 1 Overview LTE Bands

Band No.	Band Definition	Uplink Frequency Range	Downlink Frequency Range	FDD/TDD System	Comment
1	Mid-Band	1920-1980 MHz	2110-2170 MHz	FDD	
2	Mid-Band	1850-1910 MHz	1930-1990 MHz	FDD	
3	Mid-Band	1710-1785 MHz	1805-1880 MHz	FDD	
4	Mid-Band	1710-1755 MHz	2110-2155 MHz	FDD	
5	Low-Band	824-849 MHz	869-894 MHz	FDD	
6	Low-Band	830-840 MHz	875-885 MHz	FDD	
7	High-Band	2500-2570 MHz	2620-2690 MHz	FDD	
8	Low-Band	880-915 MHz	925-960 MHz	FDD	
9	Mid-Band	1749.9-1784.9 MHz	1844.9-1879.9 MHz	FDD	
10	Mid-Band	1710-1770 MHz	2110-2170 MHz	FDD	
11	Mid-Band	1427.9-1452.9 MHz	1475.9-1500.9 MHz	FDD	
12	Low-Band	698-716 MHz	728-746 MHz	FDD	
13	Low-Band	777-787 MHz	746-756 MHz	FDD	
14	Low-Band	788-798 MHz	758-768 MHz	FDD	
15		reserved	reserved	FDD	
16		reserved	Reserved	FDD	
17	Low-Band	704-716 MHz	734-746 MHz	FDD	
18	Low-Band	815-830 MHz	860-875 MHz	FDD	
19	Low-Band	830-845 MHz	875-890 MHz	FDD	
20	Low-Band	832-862 MHz	791-821 MHz	FDD	
21	Mid-Band	1447.9-1462.9 MHz	1495.9-1510.9 MHz	FDD	
22	High-Band	3410-3500 MHz	3510-3600 MHz	FDD	
23	Mid-Band	2000-2020 MHz	2180-2200 MHz	FDD	
24	Mid-Band	1626.5-1660.5 MHz	1525-1559 MHz	FDD	
25	Mid-Band	1850-1915 MHz	1930-1995 MHz	FDD	
26	Low-Band	814-849 MHz	859-894 MHz	FDD	
27	Low-Band	807-824 MHz	852-869 MHz	FDD	
28	Low-Band	703-748 MHz	758-803 MHz	FDD	
29	Low-Band	N/A	716-728 MHz	FDD	
30	High-Band	2305-2315 MHz	2350-2360 MHz	FDD	
31	Low-Band	452.5-457.5 MHz	462.5-467.5MHz	FDD	
32	Mid-Band	N/A	1452-1496 MHz	FDD	
33	Mid-Band	1900-1920 MHz		TDD	
34	Mid-Band	2010-2025 MHz		TDD	
35	Mid-Band	1850-1910 MHz		TDD	
36	Mid-Band	1930-1990 MHz		TDD	
37	Mid-Band	1910-1930 MHz		TDD	

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Introduction

Table 1 **Overview LTE Bands**

38	High-Band	2570-2620 MHz	TDD	
39	Mid-Band	1880-1920 MHz	TDD	
40	High-Band	2300-2400 MHz	TDD	
41	High-Band	2496-2690 MHz	TDD	
42	High-Band	3400-3600 MHz	TDD	
43	High-Band	3600-3800 MHz	TDD	
44	Low-Band	703-803 MHz	TDD	
46	High-Band	5150-5925 MHz	TDD	

Note: FDD: Frequency Division Duplexing; TDD: Time Division Duplexing

2 BGS15GA14 Features

2.1 Main Features

- 5 high-linearity, interchangeable RX ports
- Low insertion loss
- Low harmonic generation
- High port-to-port-isolation
- Suitable for Edge / C2K / LTE / WCDMA Applications
- 0.1 to 6 GHz coverage
- No decoupling capacitors required if no DC applied on RF lines
- On chip control logic including ESD protection
- General Purpose Input-Output (GPIO) Interface
- Small form factor 2.0mm x 2.0mm
- No power supply blocking required
- High EMI robustness
- RoHS and WEEE compliant package

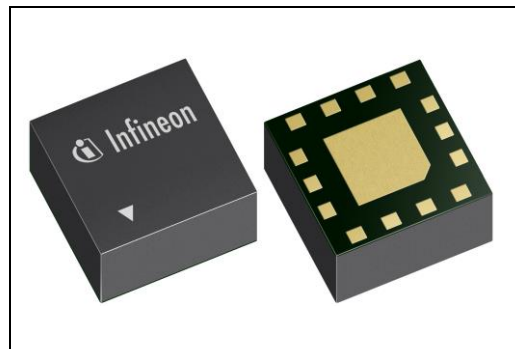


Figure 2 BGS15GA14

2.2 Functional Diagram

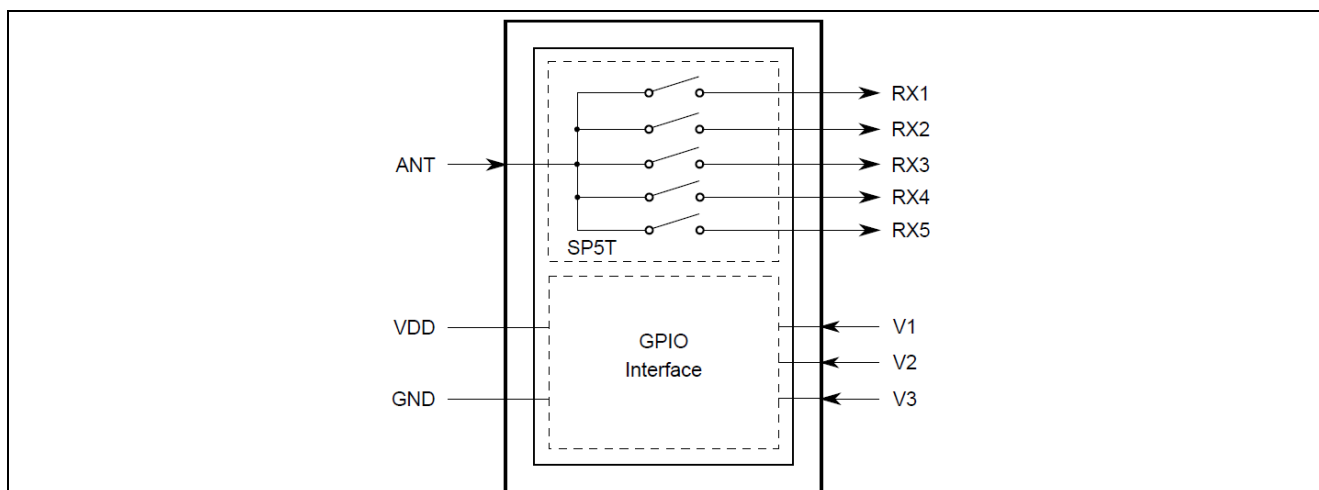


Figure 3 Equivalent Circuit Block diagram of BGS15GA14

2.3 Signal Description

Table 2 Pin Configuration of BGS15GA14

Pin No.	Name	Pin Type	Function
0	GND	GND	RF Ground; die pad
1	RX5	I/O	RX port 5
2	RX3	I/O	RX port 3
3	RX1	I/O	RX port 1
4	VDD	PWR	V _{DD} supply
5	V3	I	GPIO control pin
6	V2	I	GPIO control pin
7	V1	I	GPIO control pin
8	NC		Not connected
9	RX2	I/O	RX port 2
10	RX2	I/O	RX port 4
11	NC		Not connected
12	NC		Not connected
13	ANT	I/O	Antenna Port
14	NC		Not connected

Table 3 Modes of Operation: Truth Table of BGS15GA14

		Control Inputs		
State	Mode	V1	V2	V3
1	RX1-ANT	0	0	0
2	RX2-ANT	0	0	1
3	RX3-ANT	0	1	0
4	RX4-ANT	0	1	1
5	RX5-ANT	1	0	0
6	50Ω	1	0	1
7	RF3/RF5-ANT	1	1	0
8	Shutdown	1	1	1

SP5T Diversity Antenna Switch with GPIO Interface

BGS15GA14

BGS15GA14 Features

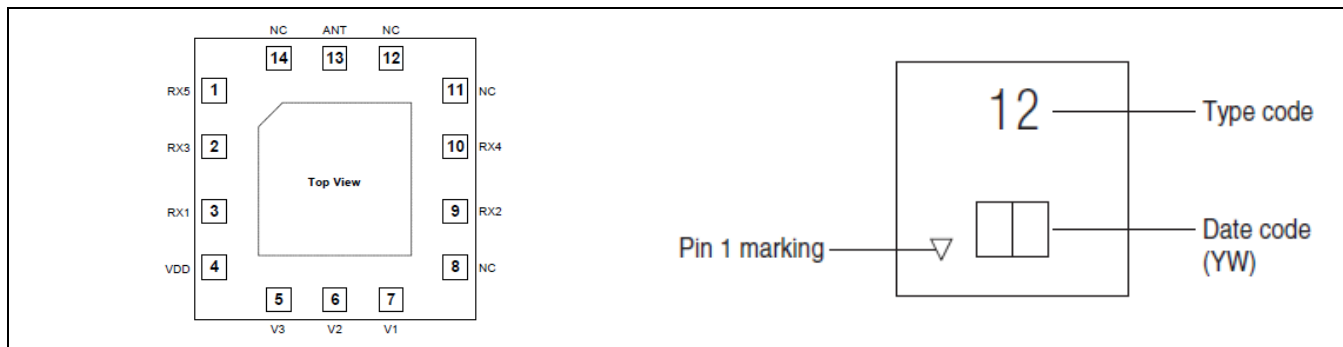


Figure 4 Package and pin connections (top view) of BGS15GA14

3 Application Circuit and Performance Overview

In this chapter the performance of the application circuit, the schematic and bill-on-materials are presented.

Device:	BGS15GA14
Application:	SP5T Diversity Antenna Switch with GPIO Interface
PCB Marking:	BGS1xGA14

3.1 Summary of Measurement Results

All measurement results of this application note are measured with a typical device of the BGS15GA14 on an application board. The measurement procedure is shown in chapter 4, 0 and 5.3, including the needed de-embedding for s-Parameter measurements.

The small signal characteristics are measured at 25 °C, -5 dBm P_{in} , 2.8V V_{dd} , 1.8V V_{crlt} up to 4GHz with a Network Analyzer connected to an automatic multiport switch box in single ended mode.

In the following tables and graphs the most important RF parameter of the BGS15GA14 are shown. The markers are set to the most important frequencies of the WCDMA and LTE systems.

3.2 Insertion Loss

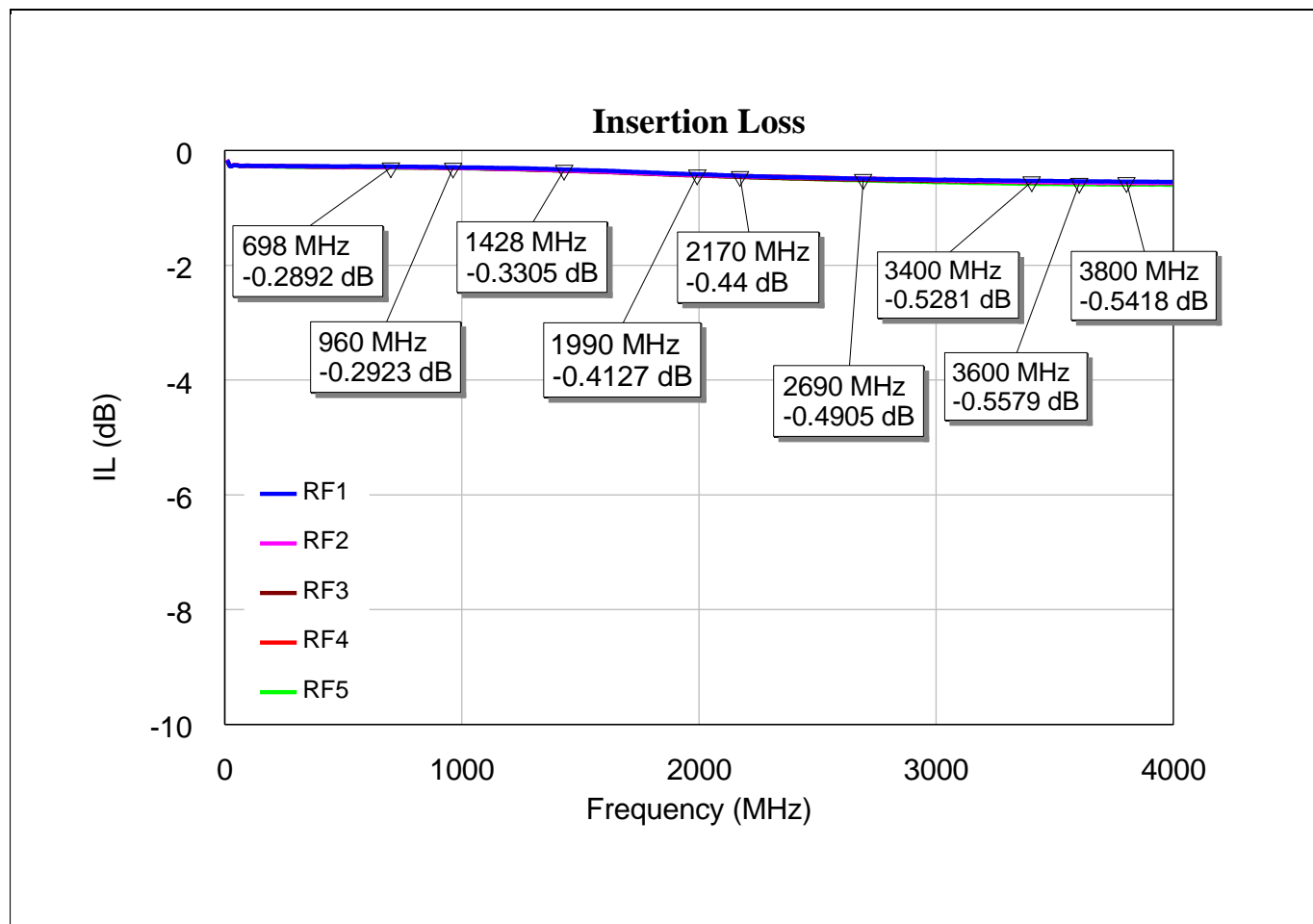


Figure 5 Insertion Loss in dB up to 4GHz

Table 4 Forward Transmission in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF1	0.28	0.29	0.33	0.41	0.44	0.49	0.53	0.54	0.54
RF2	0.29	0.30	0.35	0.43	0.45	0.49	0.55	0.56	0.56
RF3	0.29	0.30	0.35	0.44	0.46	0.51	0.55	0.56	0.57
RF4	0.29	0.30	0.35	0.42	0.44	0.49	0.55	0.55	0.55
RF5	0.30	0.31	0.35	0.43	0.46	0.52	0.58	0.59	0.59

3.3 Antenna Return Loss

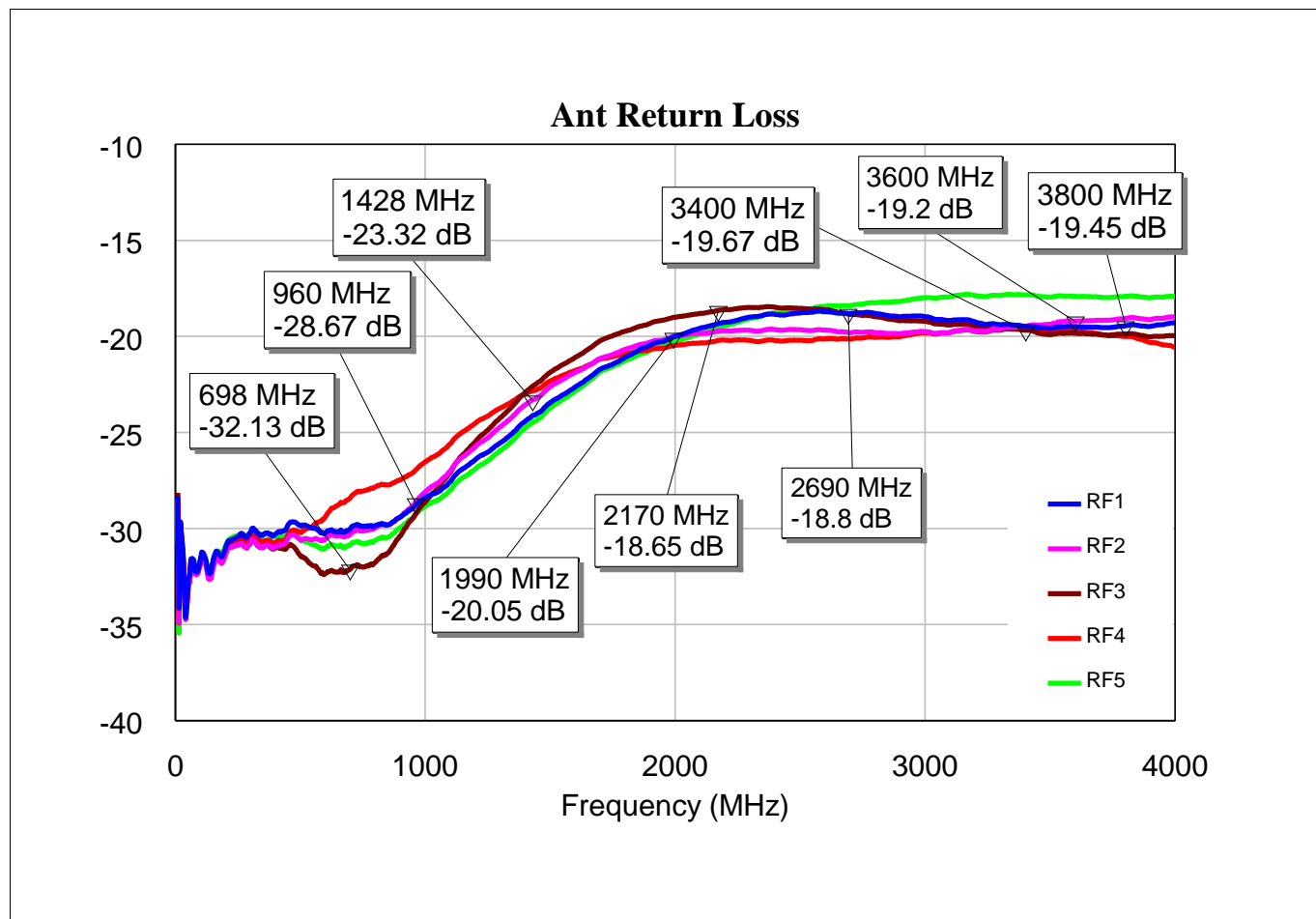


Figure 6 RF matching @ Ant Port in dB

Table 5 Insertion Loss in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF1	30.04	28.82	24.15	20.05	19.34	18.80	19.48	19.49	19.45
RF2	30.33	28.68	23.32	20.01	19.73	19.79	19.48	19.20	19.06
RF3	32.13	29.29	22.61	19.02	18.65	18.83	19.68	19.83	19.87
RF4	28.42	26.98	22.84	20.48	20.22	20.14	19.61	19.71	20.00
RF5	30.86	29.30	24.50	20.30	19.54	18.40	17.84	17.91	17.91

3.4 Port Return Loss

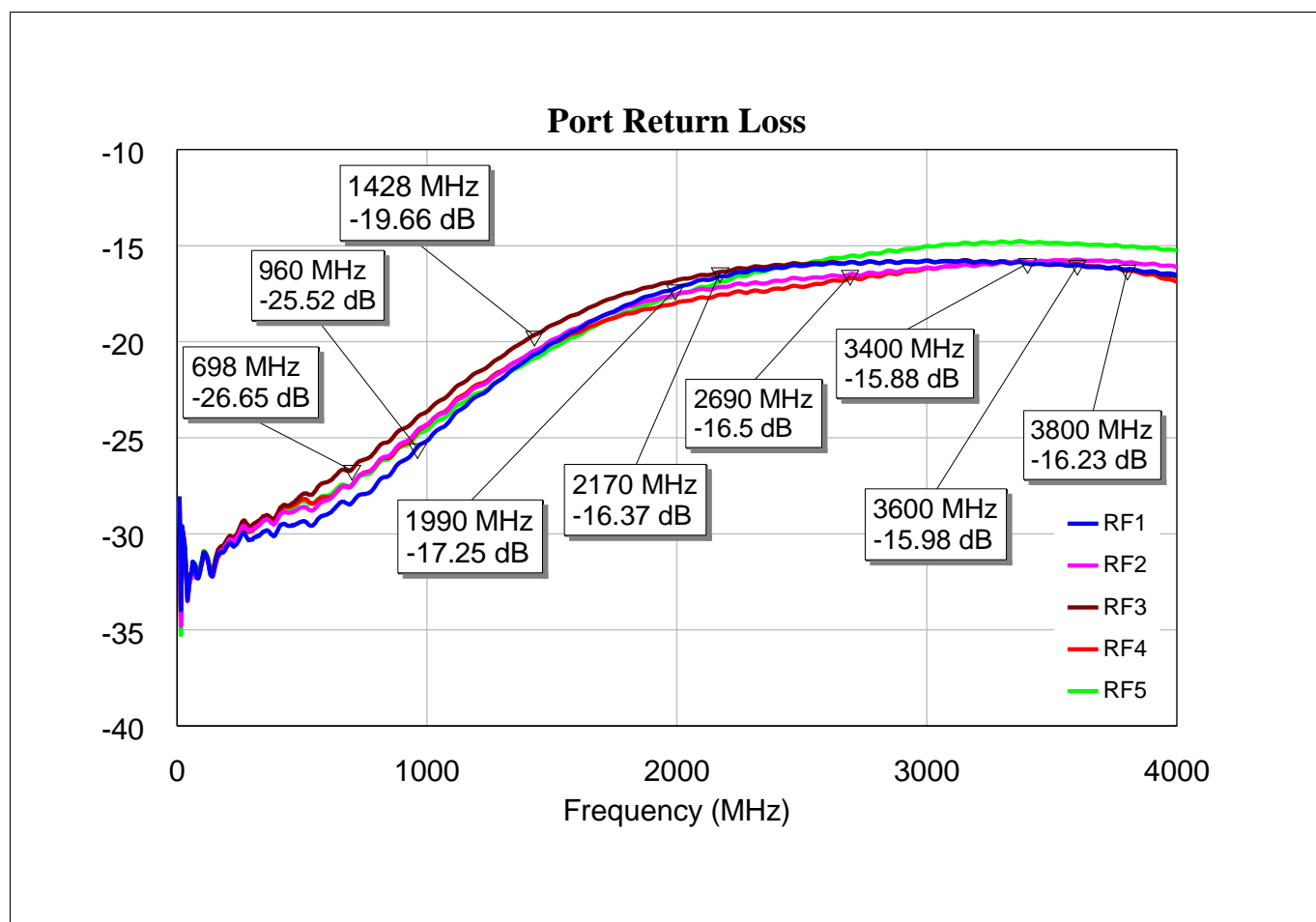


Figure 7 RF matching @ RFx Ports in dB

Table 6 Port Return Loss in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF1	28.40	25.52	20.68	17.25	16.61	15.87	15.93	15.98	16.23
RF2	27.48	24.64	20.47	17.57	17.15	16.50	15.82	15.72	15.89
RF3	26.66	23.95	19.66	16.84	16.37	15.87	15.88	16.01	16.28
RF4	27.51	24.73	20.59	18.02	17.55	16.71	15.87	15.94	16.31
RF5	27.45	24.88	20.88	17.61	16.89	15.54	14.82	14.88	15.06

3.5 Isolation Antenna to Port

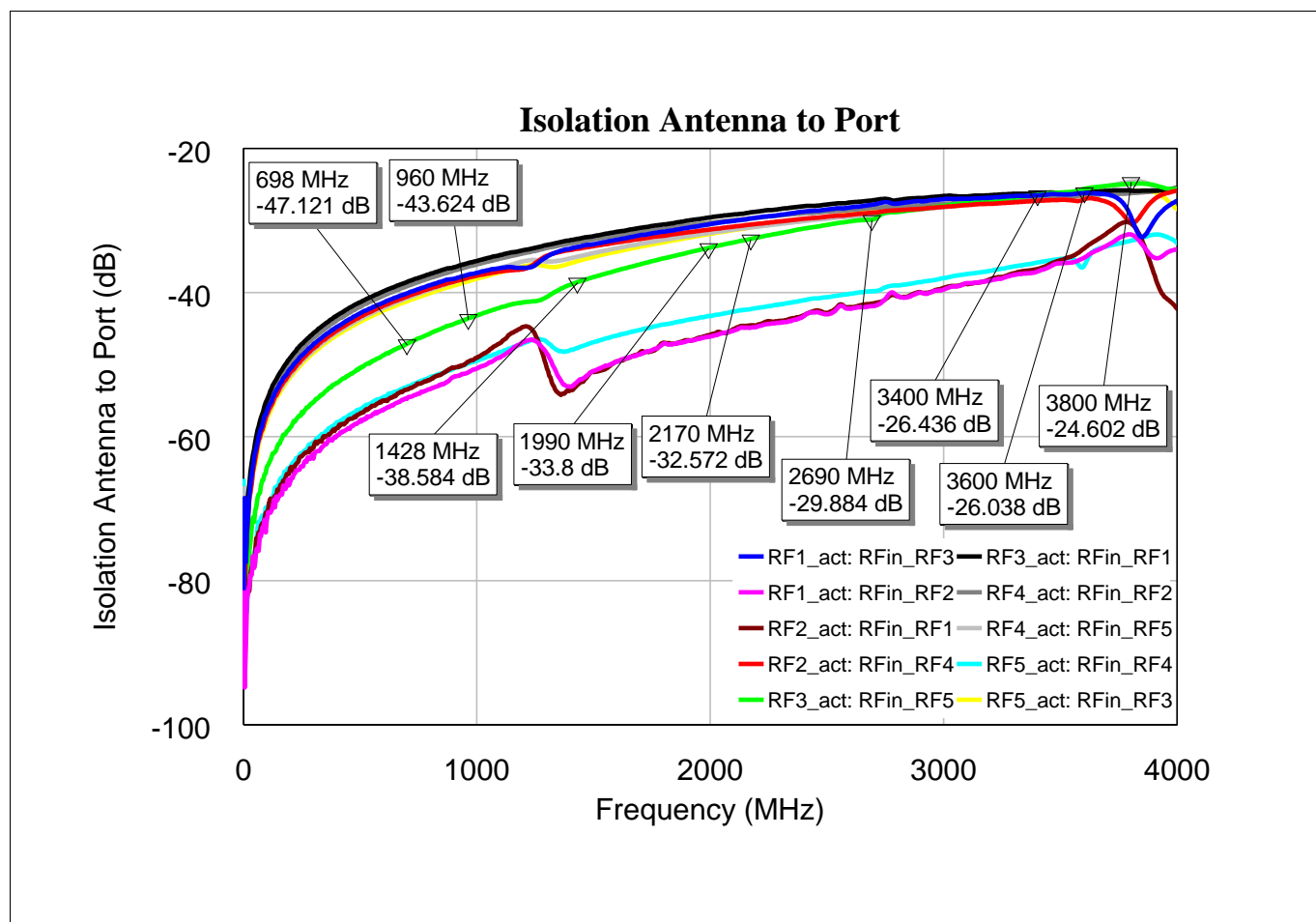


Figure 8 Isolation Antenna to Port in dB

Table 7 Isolation Antenna to Port in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF1_act: RFin_RF3	40.13	37.54	33.68	30.55	29.73	27.82	26.49	26.19	29.26
RF1_act: RFin_RF2	54.74	51.13	52.45	46.15	44.90	41.89	37.12	35.20	31.92
RF2_act: RFin_RF1	53.49	49.63	52.91	45.93	44.64	41.54	36.74	33.92	30.34
RF2_act: RFin_RF4	40.58	37.96	33.92	31.28	30.56	28.91	27.26	27.08	30.18
RF3_act: RFin_RF5	47.12	43.62	38.58	33.80	32.57	29.88	26.44	25.62	24.89
RF3_act: RFin_RF1	38.56	35.93	32.55	29.61	28.87	27.21	26.24	26.04	25.82
RF4_act: RFin_RF2	39.15	36.42	33.02	30.34	29.69	28.25	26.98	26.40	26.22
RF4_act: RFin_RF5	40.78	37.92	35.03	31.74	30.97	29.60	26.75	26.00	24.60
RF5_act: RFin_RF4	53.12	49.84	47.71	43.24	42.32	39.81	35.83	36.41	32.82
RF5_act: RFin_RF3	41.23	38.47	35.67	31.91	30.98	28.78	26.91	26.46	25.90

3.6 Isolation Port to Port

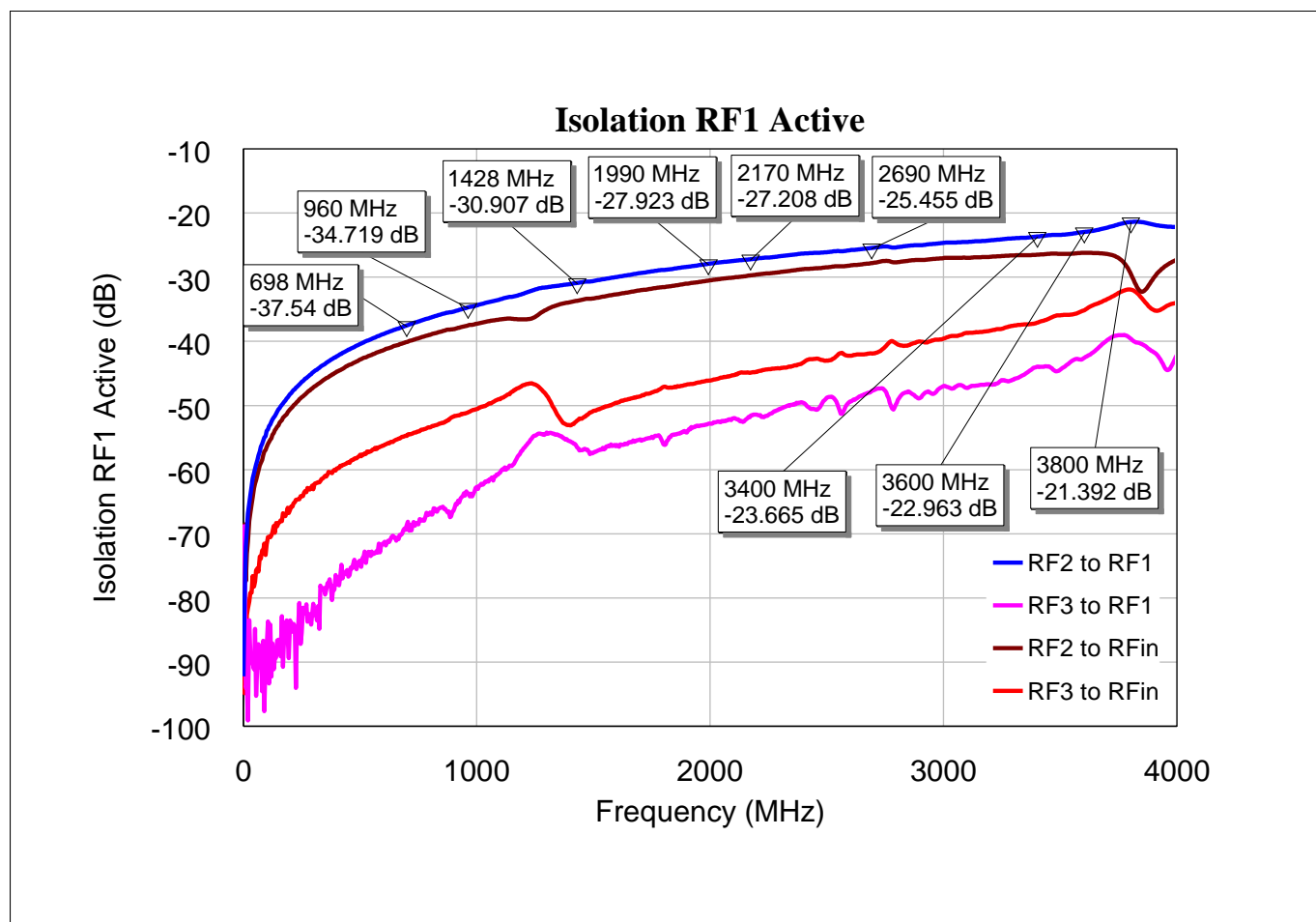


Figure 9 Isolation Port to Port with RF1 active in dB

Table 8 Isolation Port to Port with RF1 active in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF2 to RF1	37.54	34.72	30.91	27.92	27.21	25.46	23.67	22.96	21.39
RF3 to RF1	68.95	63.76	56.38	52.92	51.82	47.84	43.97	42.66	39.51
RF2 to RFin	40.13	37.54	33.68	30.55	29.73	27.82	26.49	26.19	29.26
RF3 to RFin	54.74	51.13	52.45	46.15	44.90	41.89	37.12	35.20	31.92

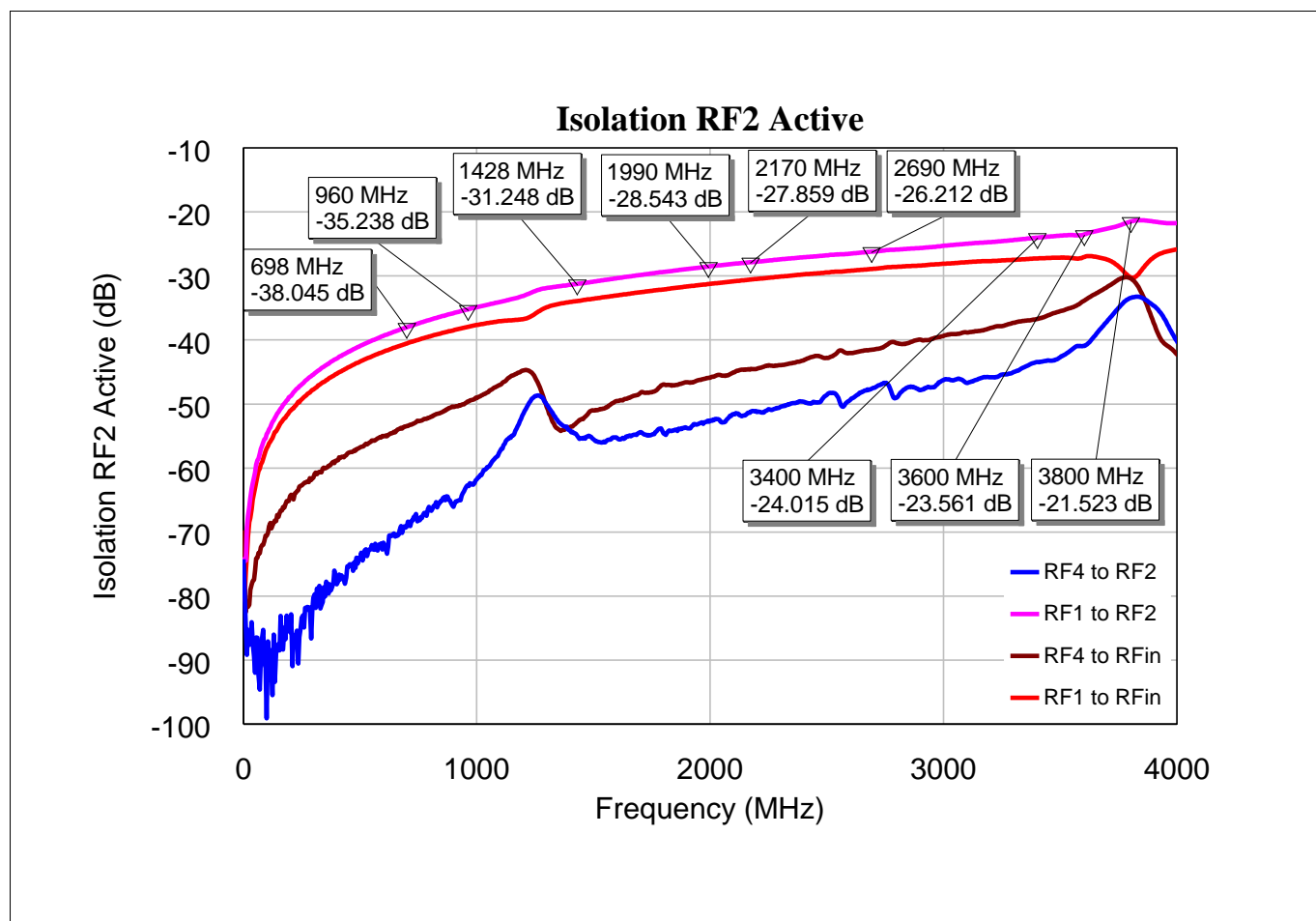


Figure 10 Isolation Port to Port with RF2 active in dB

Table 9 Isolation Port to Port with RF2 active in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF4 to RF2	69.23	62.69	54.80	52.62	51.37	47.67	43.43	40.95	33.45
RF1 to RF2	38.05	35.24	31.25	28.54	27.86	26.21	24.02	23.56	21.52
RF4 to RFin	53.49	49.63	52.91	45.93	44.64	41.54	36.74	33.92	30.34
RF1 to RFin	40.58	37.96	33.92	31.28	30.56	28.91	27.26	27.08	30.18

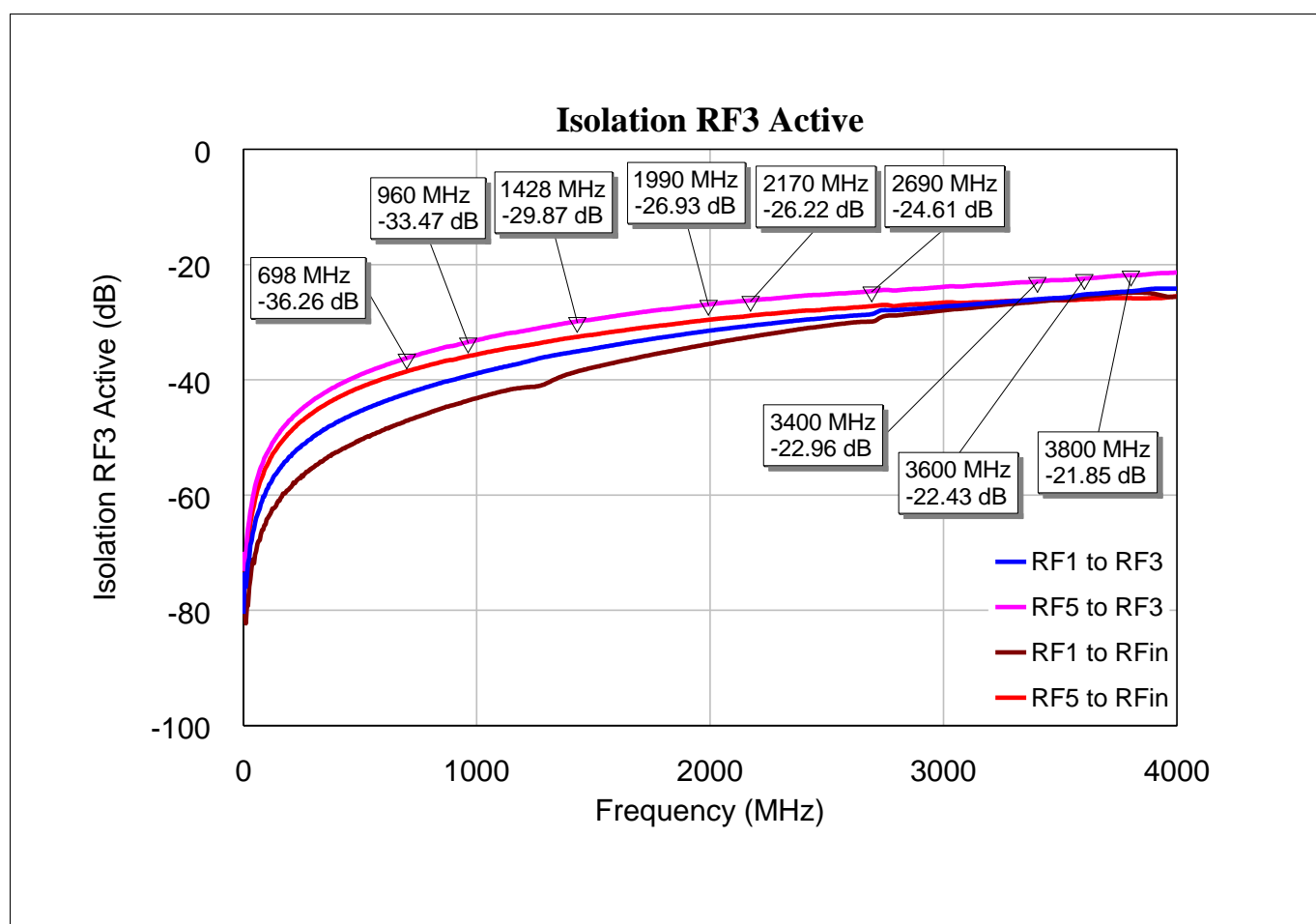


Figure 11 Isolation Port to Port with RF3 active in dB

Table 10 Isolation Port to Port with RF3 active in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF1 to RF3	42.41	39.32	35.08	31.50	30.63	28.63	26.03	25.21	24.63
RF5 to RF3	36.26	33.47	29.87	26.93	26.23	24.61	22.96	22.43	21.85
RF1 to RFin	47.12	43.62	38.58	33.80	32.57	29.88	26.44	25.62	24.89
RF5 to RFin	38.56	35.93	32.55	29.61	28.87	27.21	26.24	26.04	25.82

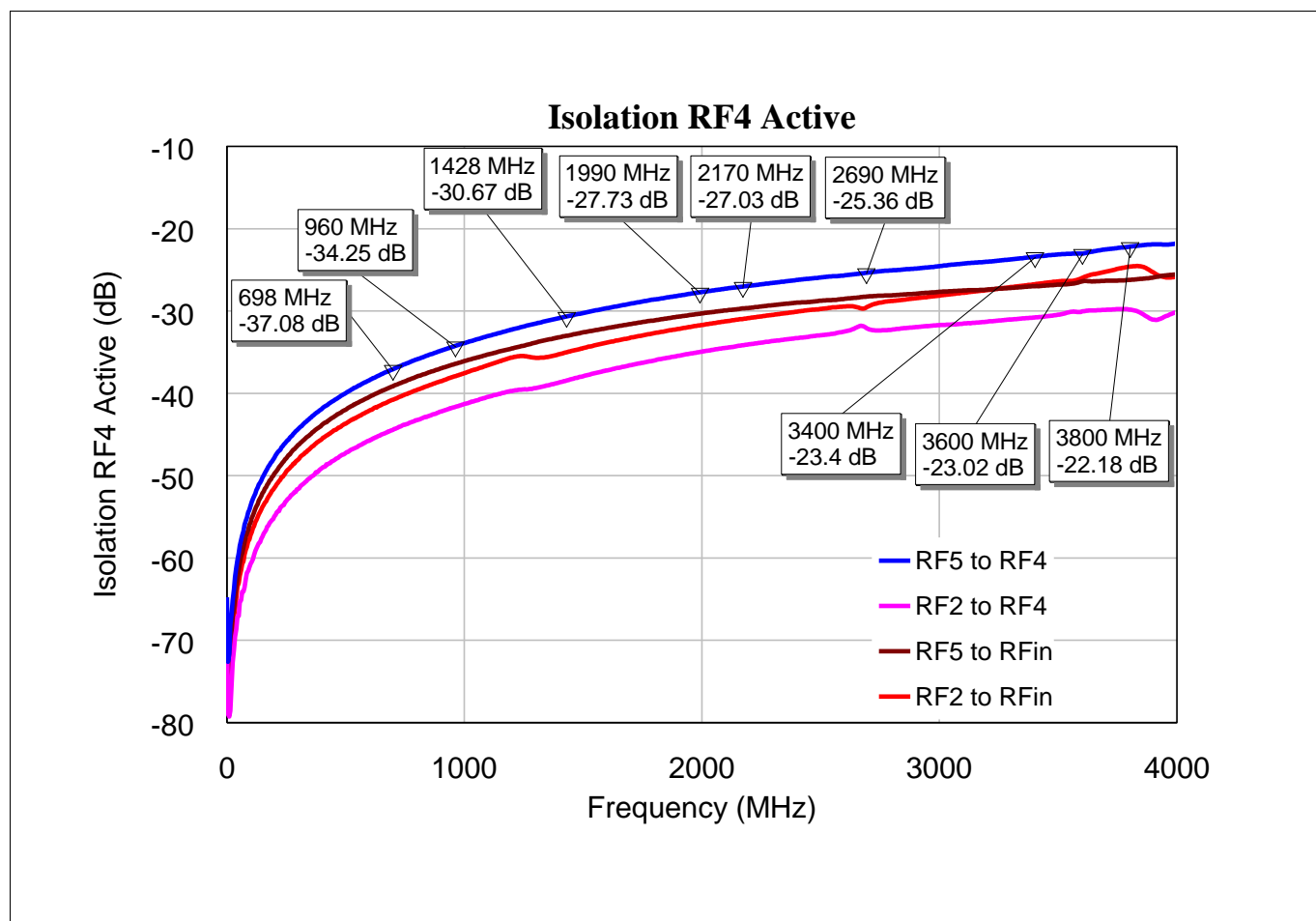


Figure 12 Isolation Port to Port with RF4 active in dB

Table 11 Isolation Port to Port with RF4 active in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF5 to RF4	37.08	34.25	30.67	27.73	27.03	25.36	23.40	23.02	22.18
RF2 to RF4	44.45	41.66	38.46	34.99	34.16	32.00	30.79	30.06	29.82
RF5 to RFin	39.15	36.42	33.02	30.34	29.69	28.25	26.98	26.40	26.22
RF2 to RFin	40.78	37.92	35.03	31.74	30.97	29.60	26.75	26.00	24.60

SP5T Diversity Antenna Switch with GPIO Interface

BGS15GA14



Application Circuit and Performance Overview

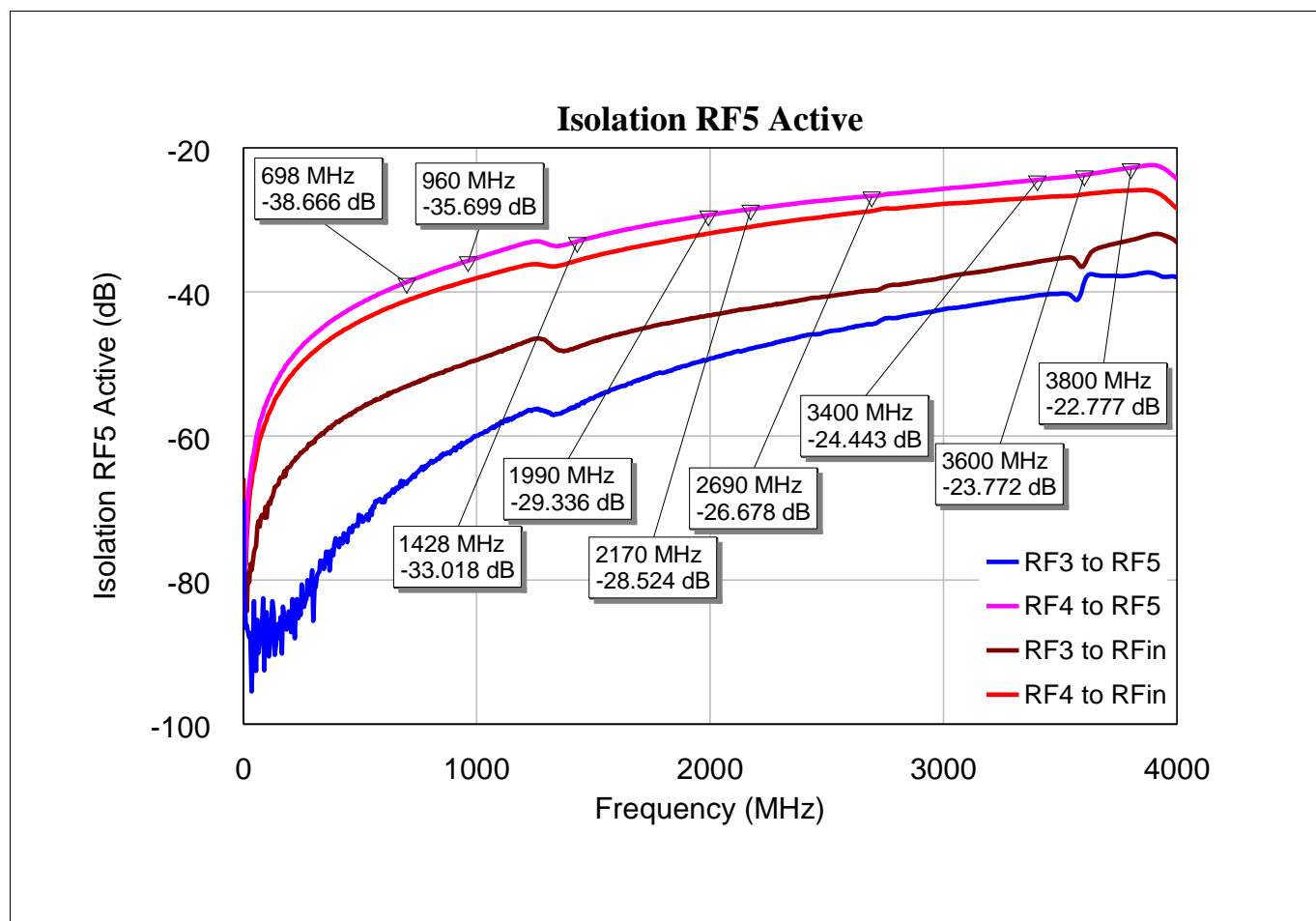


Figure 13 Isolation Port to Port with RF5 active in dB

Table 12 Isolation Port to Port with RF5 active in dB

Frequency (MHz)	698	960	1428	1990	2170	2690	3400	3600	3800
RF3 to RF5	66.51	60.70	55.75	49.41	47.85	44.51	40.49	38.96	37.70
RF4 to RF5	38.67	35.70	33.02	29.34	28.52	26.68	24.44	23.77	22.78
RF3 to RFin	53.12	49.84	47.71	43.24	42.32	39.81	35.83	36.41	32.82
RF4 to RFin	41.23	38.47	35.67	31.91	30.98	28.78	26.91	26.46	25.90

Switching time

4 Switching time

4.1 Measurement Specifications

Switching On Time: 50% Trigger signal to 90% RF Signal

Switching Off Time: 50% Trigger signal to 10% RF Signal

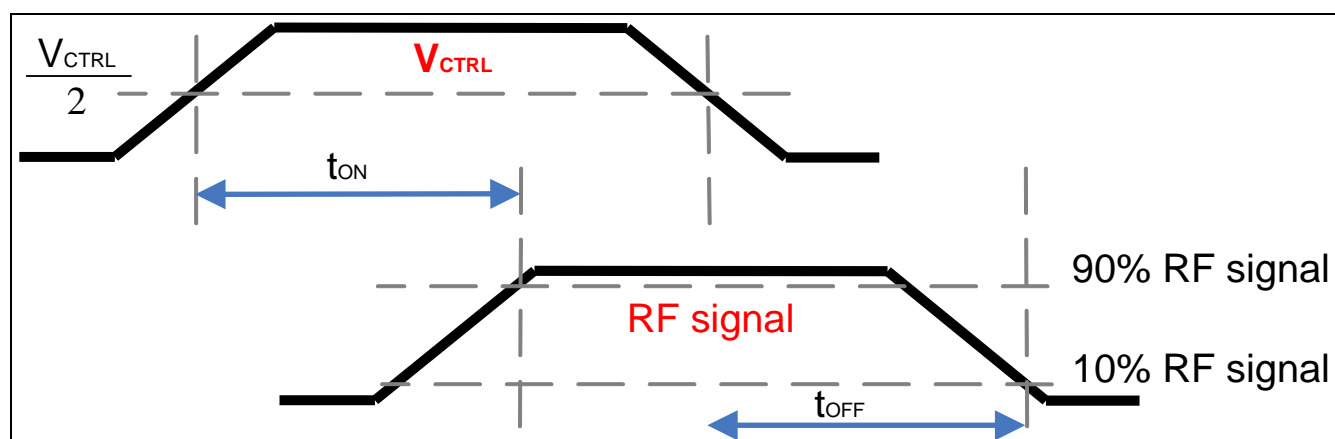


Figure 14 Switching Time

Rise time: 10% to 90% RF Signal

Fall time: 90% to 10% RF Signal

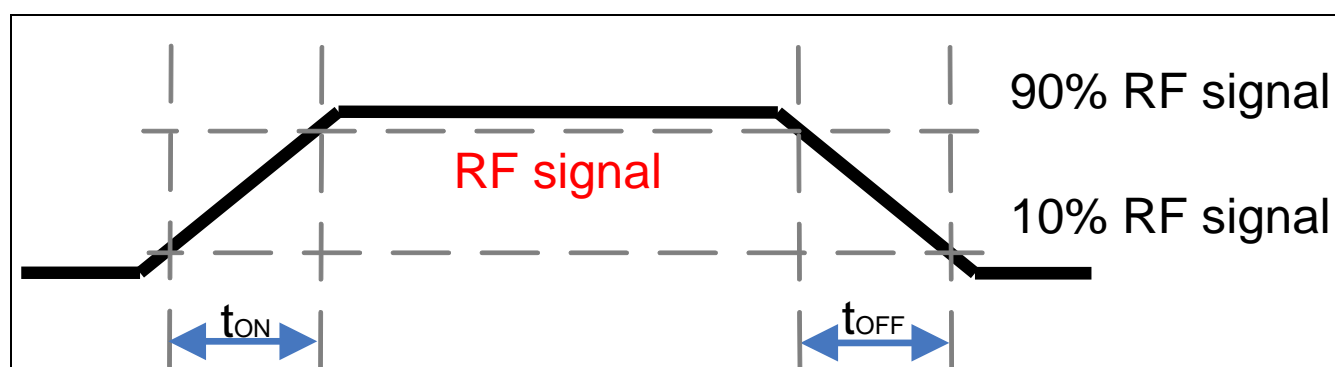


Figure 15 Rise/Fall Time

4.2 Measurement Setup

The setup on below is representing switching time measurement setup. In the Figure 16 the setup is configured for a SPDT switch, where the trigger signal is a one kHz signal with the amplitude of device-Vdd/Vctrl. The setup properties (RFin and trigger signal pulse) could be changed for measuring other devices like amplifier.

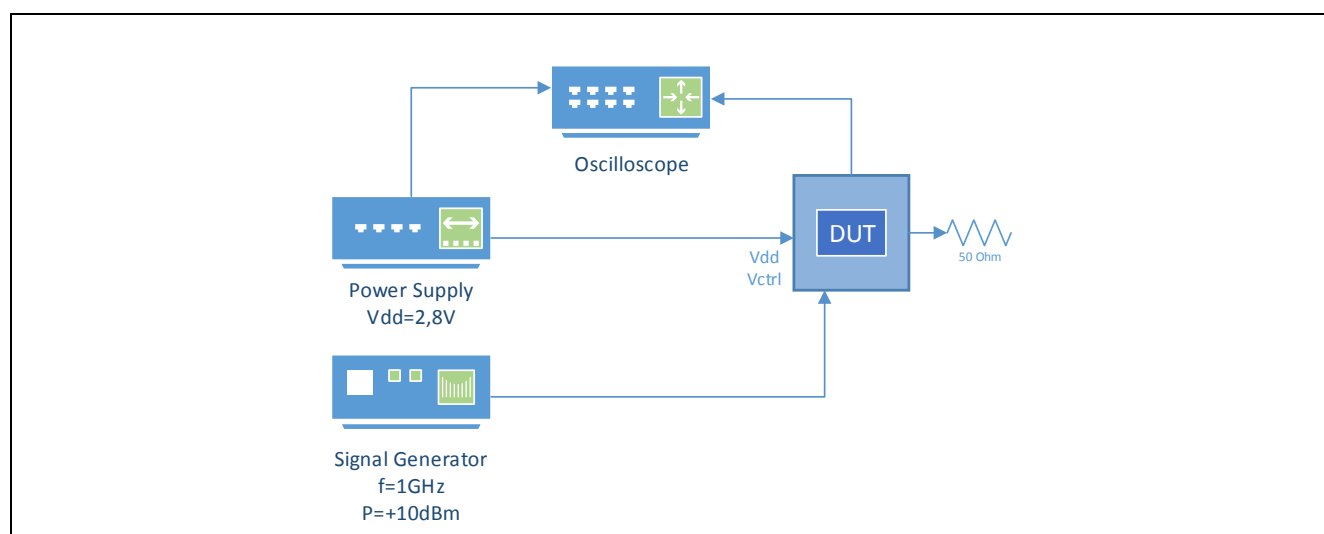


Figure 16 Measurement setup

4.3 Measurement results

The switching Time measurement setup consist of one pulse generator which generates a square wave with 50% duty cycle and an amplitude of 1.8 Volts, an oscilloscope which can detect the 1 GHz signal and the 1 kHz signal and one Signal generator which is set to an output signal of 1GHz with a power level 10 dBm.

If the oscilloscope can not detect the 1 GHz signal of the RF path, due to small bandwidth, it is possible to use a crystal oscillator in front of the oscilloscope (such a device detects any RF signal present at input and commutate that one) that the RF signal can be detected.

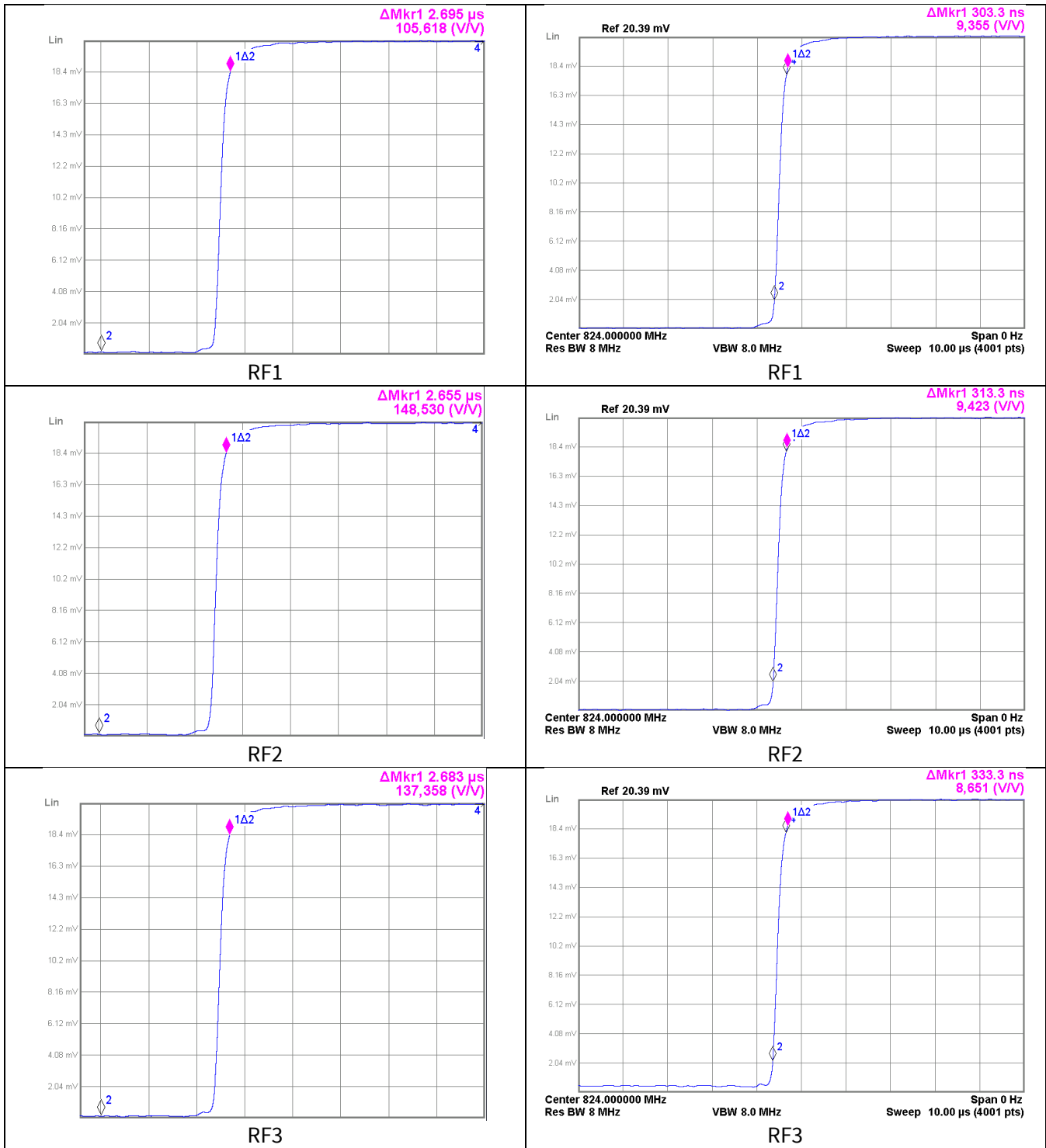
VDD= 2.7V
Vctrl= 0/1.8V Pulsed with 600Hz 50%duty cycle
RFin= 300MHz @ 0dBm

	Vctrl to RF	RF rise Time
Spec	2 - 4 μ s	2 μ s
RF1	2.695 μ s	303.3 ns
RF2	2.655 μ s	313.3 ns
RF3	2.683 μ s	333.3 ns
RF4	2.703 μ s	308.3 ns
RF5	2.655 μ s	313.3 ns

SP5T Diversity Antenna Switch with GPIO Interface

BGS15GA14

Switching time



SP5T Diversity Antenna Switch with GPIO Interface

BGS15GA14

Switching time

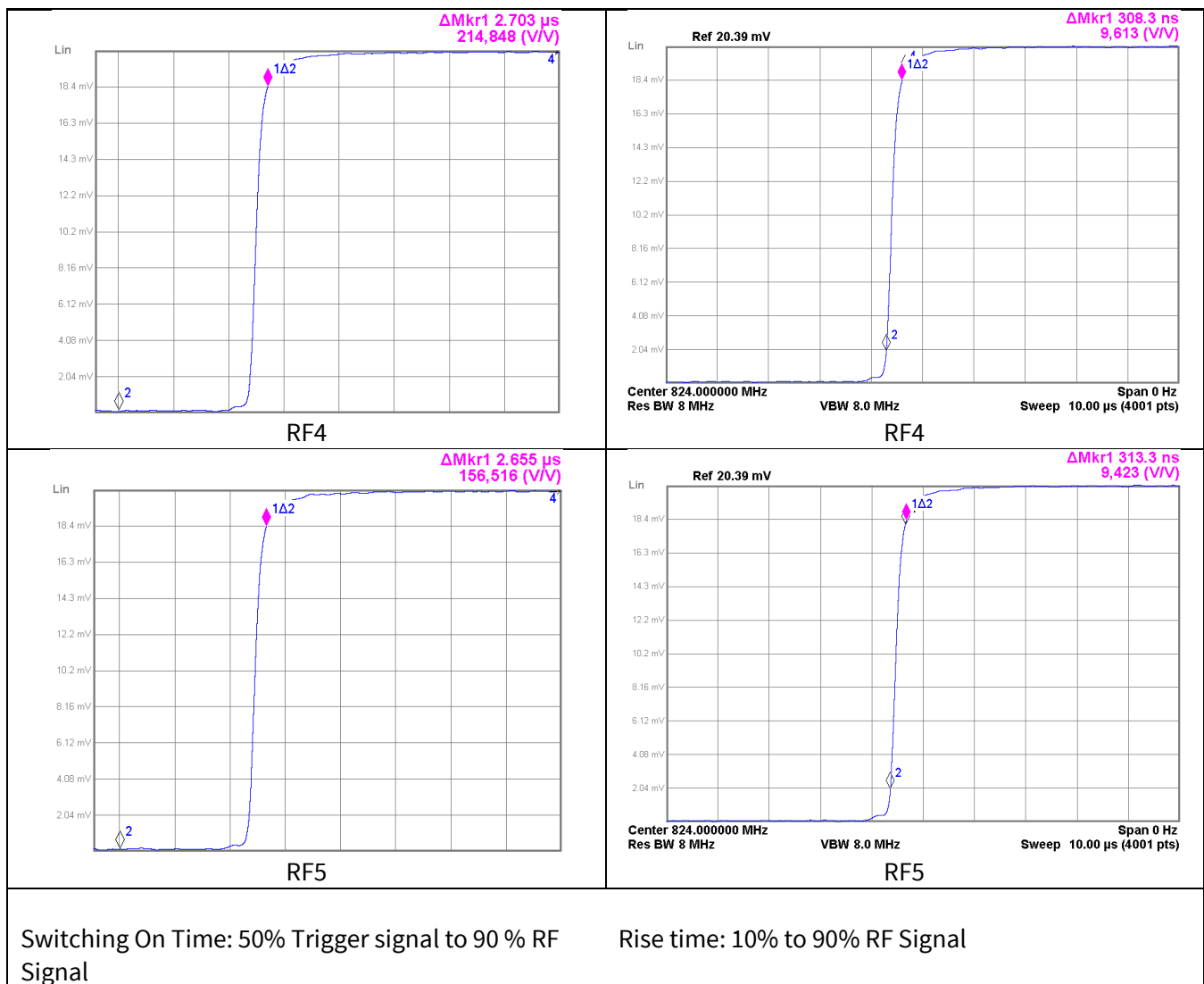


Figure 17 Screenshots of switching times

5 Intermodulation

Intermodulation distortion is characterized by the appearance in the output of frequencies equal to the sums and differences of integral multiples of the two or more component frequencies present in the input waveform.

Defined by the following expressions:

Table 13 IMD Mathematical definitions

Second Order IMD	$f_{IMD2low} = f_{Rx} - f_{Tx}$		$f_{IMD2high} = f_{Rx} + f_{Tx}$
Third Order IMD	$f_{IMD3l} = 2f_{Tx} - f_{Rx}$	$f_{IMD3m} = 2f_{Rx} + f_{Tx}$	$f_{IMD3h} = f_{Rx} + 2f_{Tx}$

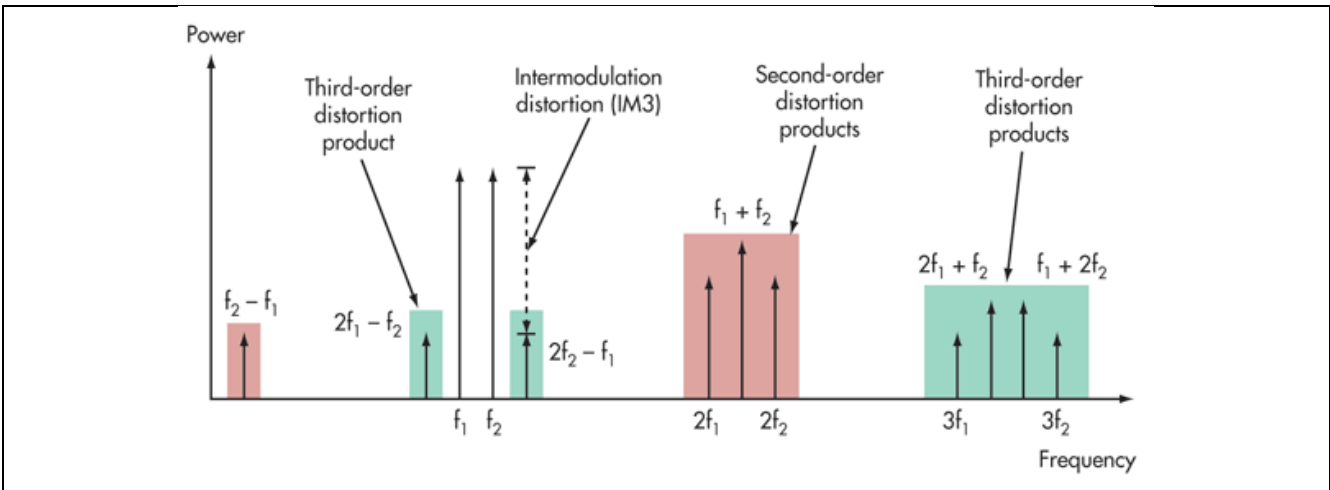


Figure 18 Representation of IMD products

5.1 Test conditions

Developing the same mathematical expressions we can see that external signals matching IMDs frequencies can interfere over f_{Rx}

Table 14 IMD Mathematical definitions extended

Second Order IMD	$f_{IMD2low} = f_{Rx} - f_{Tx} \rightarrow$ $f_{Rx} = f_{IMD2low} + f_{Tx}$		$f_{IMD2high} = f_{Rx} + f_{Tx} \rightarrow$ $f_{Rx} = f_{IMD2high} - f_{Tx}$
Third Order IMD	$f_{IMD3l} = 2f_{Tx} - f_{Rx} \rightarrow$ $f_{Rx} = 2f_{Tx} - f_{IMD3l}$	$f_{IMD3m} = 2f_{Rx} + f_{Tx} \rightarrow$ $f_{Rx} = (f_{Tx} - f_{IMD3m})/2$	$f_{IMD3h} = f_{Rx} + 2f_{Tx} \rightarrow$ $f_{Rx} = f_{IMD3h} - 2f_{Tx}$

One of the possible intermodulation scenarios is shown in Figure 19. The transmission (Tx) signal from the main antenna is coupled into the diversity antenna with high power. This signal (21 dBm or 10 dBm depending the case) and a received Jammer signal (-15 dBm) are entering the switch. Thanks to the specified application for the BGS15GA14 in between the filters and the Transceiver, the Tx signal from the main antenna loose until arriving at the switch input mostly 5 to 10 or more dB, depending of the filter and PCB structure of the RF frontend. The IMD products are measured with a Tx of 21dBm or 10dBm, which is corresponding to the IMD spec of a main antenna diversity switch like Infineon BGS15GA14. Therefore, the measured IMD products will be extremely better in the specified application circuit within the filters and transceiver as showed in the measurement results below.

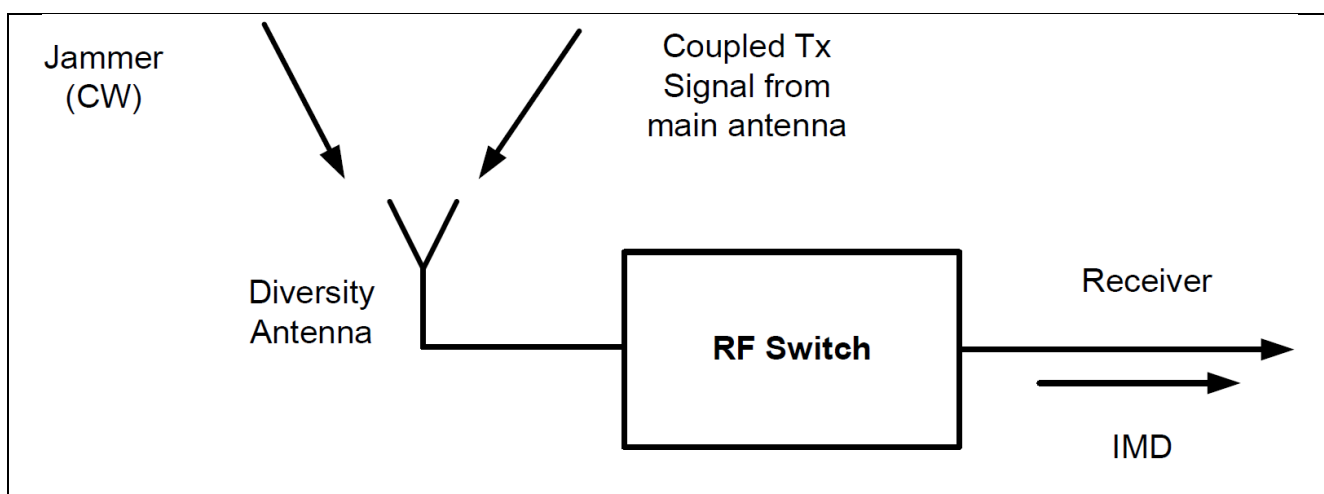


Figure 19 Block diagram of RF Switch intermodulation

Intermodulation

Special combinations of TX and Jammer signal are producing intermodulation products 2nd and 3rd order, which fall in the RX band and disturb the wanted RX signal.

5.2 Measurement Setup

The test setup for the IMD measurements has to provide a very high isolation between RX and TX signals. As an example the test set-up and the results for the high band are shown (Figure 20 and Table 11).

For the RX / TX separation a professional duplexer with 80 dB isolation is used.

For each distortion scenario there is a min and a max value given. This variation is caused by a phase shifter connected between switch and duplexer. In the test set-up the phase shifter represents a no ideal matching of the switch to 50 Ohm.

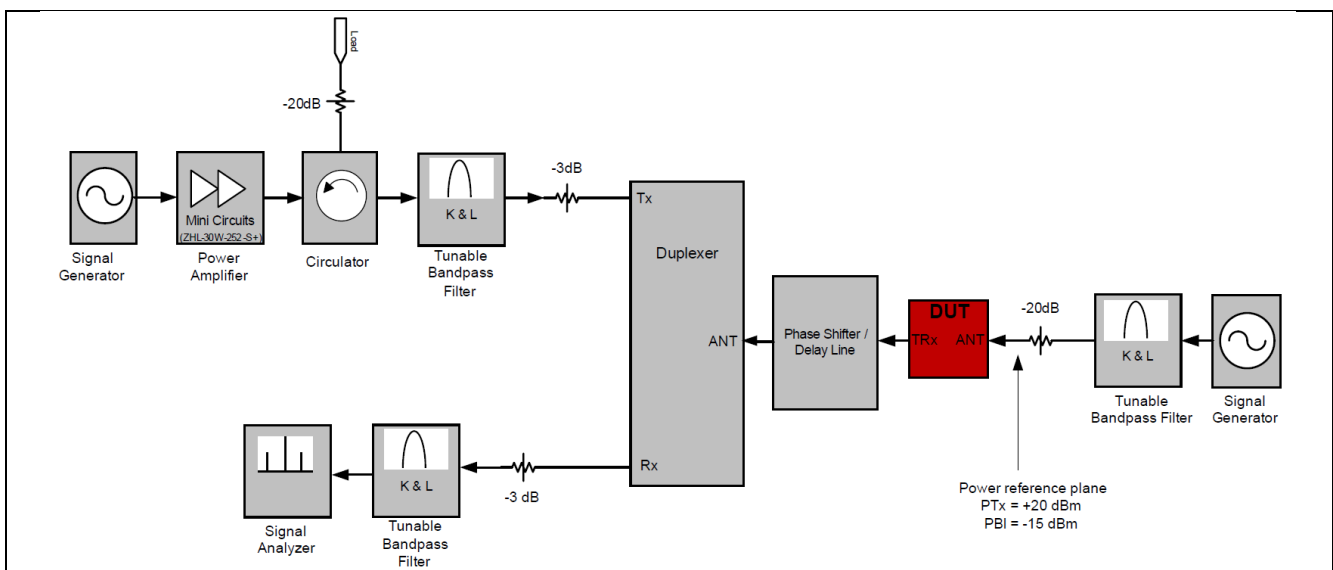


Figure 20 Block diagram of RF Switch intermodulation

5.3 Measurement Results

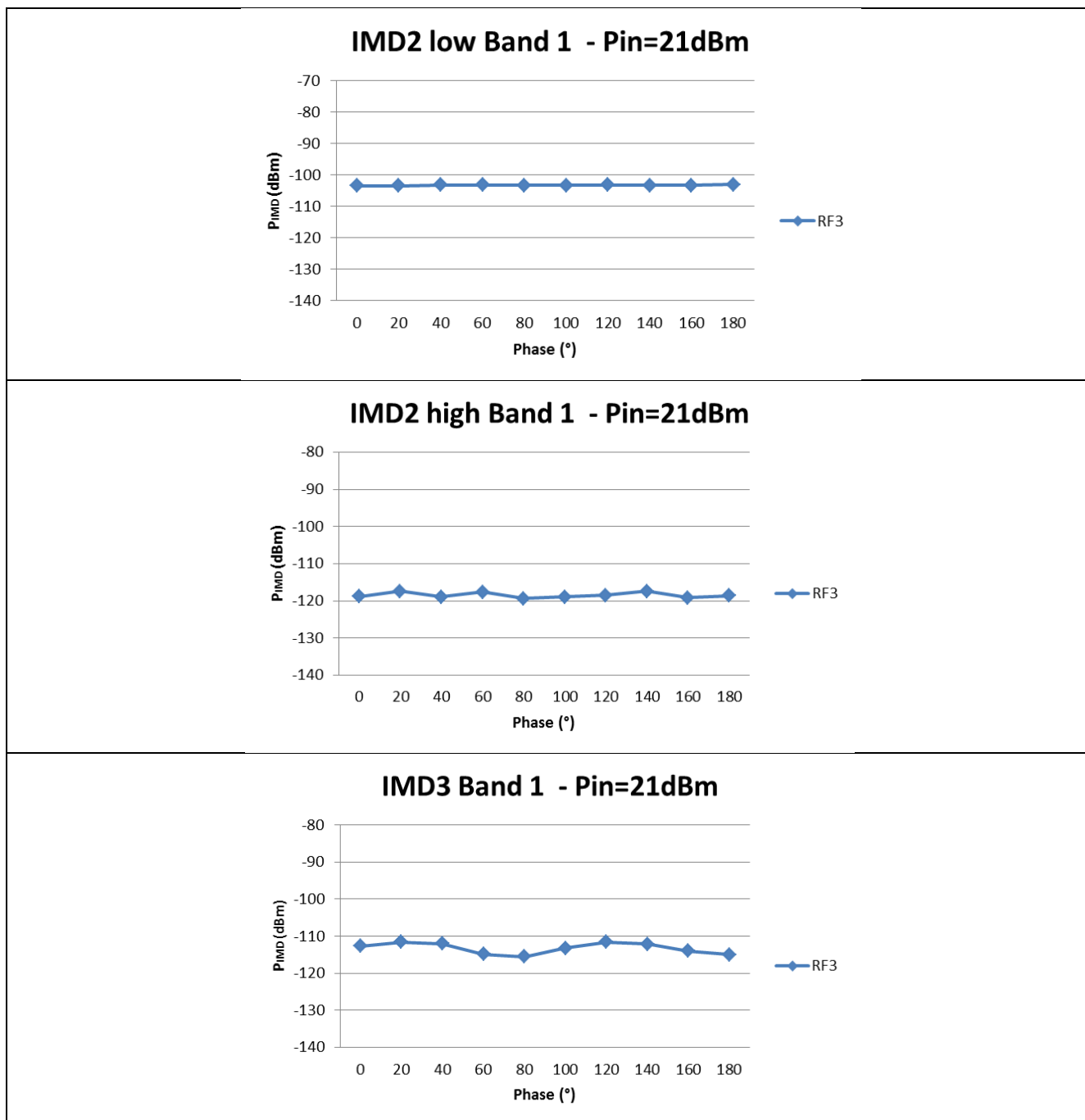


Figure 21 IMD products of Band 1 LTE and Pin=21dBm

Table 15 IMD products of Band 1 LTE

IMD Band 1 P _{Tx} =21dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =190MHz)	-103.47	-103.1
IMD2High (f _{blocker} =4090MHz)	-119.33	-117.34
IMD3 (f _{blocker} =1760MHz)	-115.51	-111.53

Intermodulation

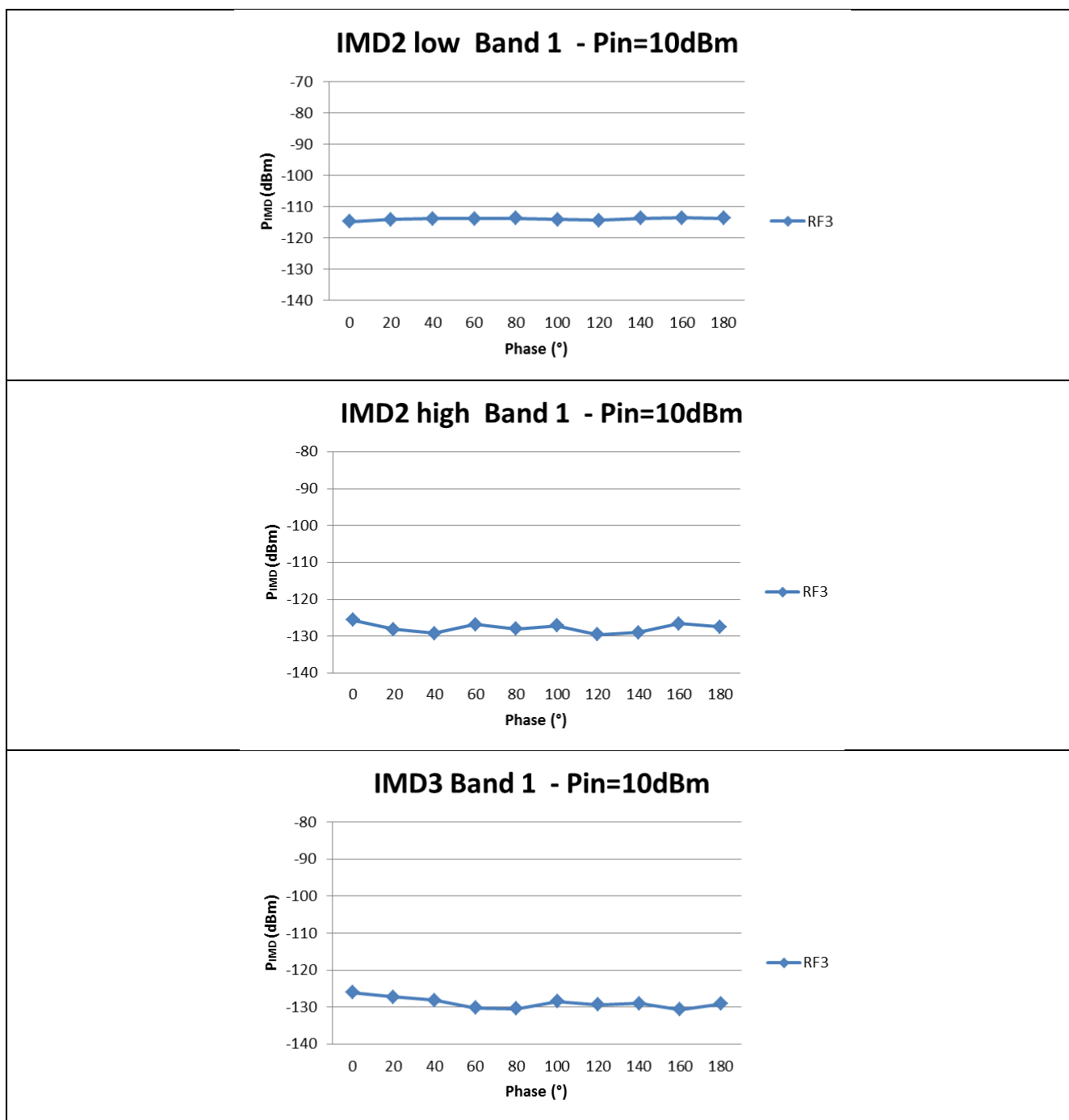


Figure 22 IMD products of Band 1 LTE and Pin=10dBm

Table 16 IMD products of Band 1 LTE

IMD Band 1 P _{Tx} =10dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =190MHz)	-114.82	-113.64
IMD2High (f _{blocker} =4090MHz)	-129.51	-125.59
IMD3 (f _{blocker} =1760MHz)	-130.69	-125.99

Intermodulation

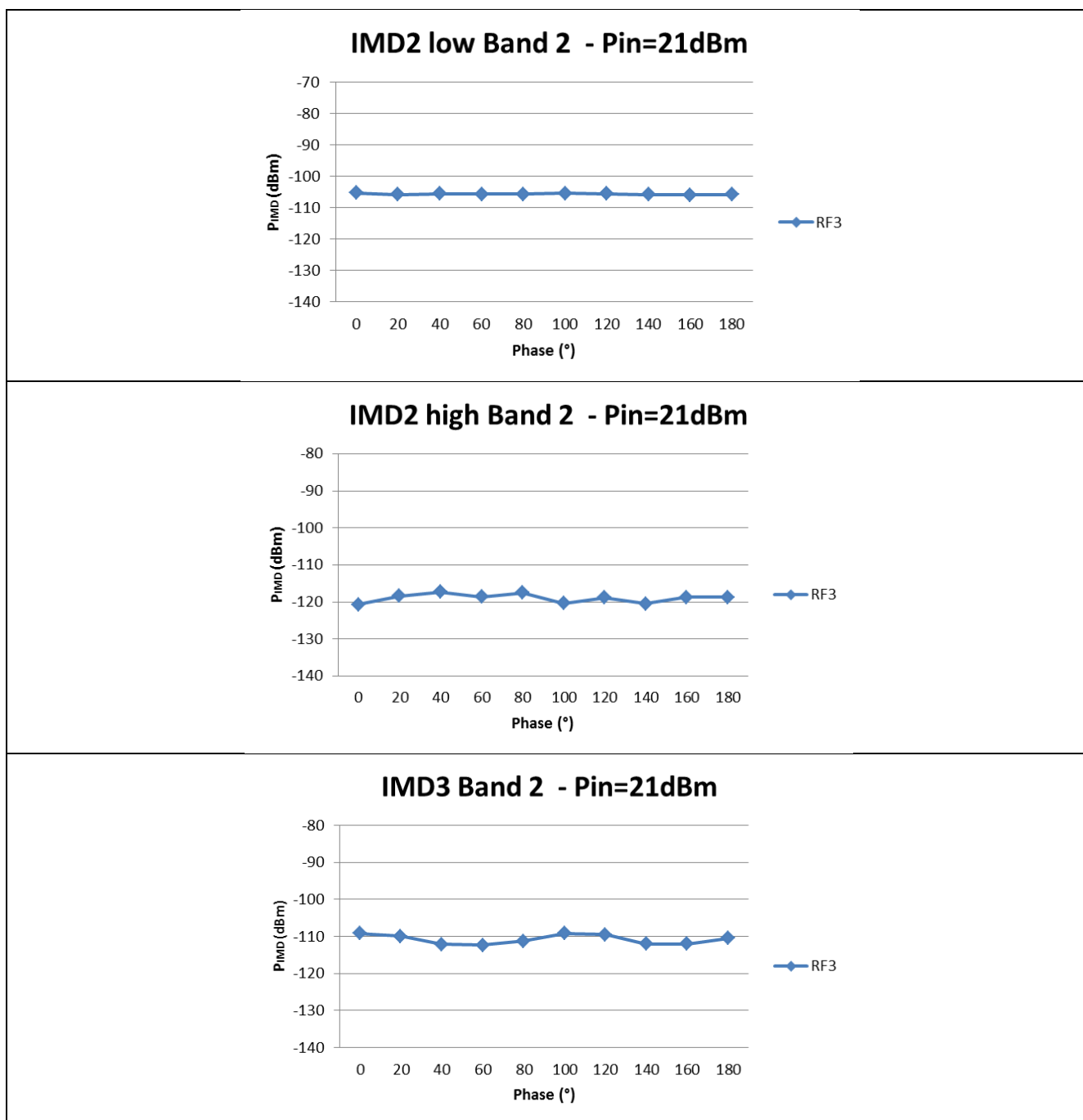


Figure 23 IMD products of Band 2 LTE and Pin=21dBm

Table 17 IMD products of Band 2 LTE

IMD Band 2 P _{Tx} =21dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =80MHz)	-105.95	-105.35
IMD2High (f _{blocker} =3840MHz)	-120.72	-117.28
IMD3 (f _{blocker} =1800MHz)	-112.36	-109.22

Intermodulation

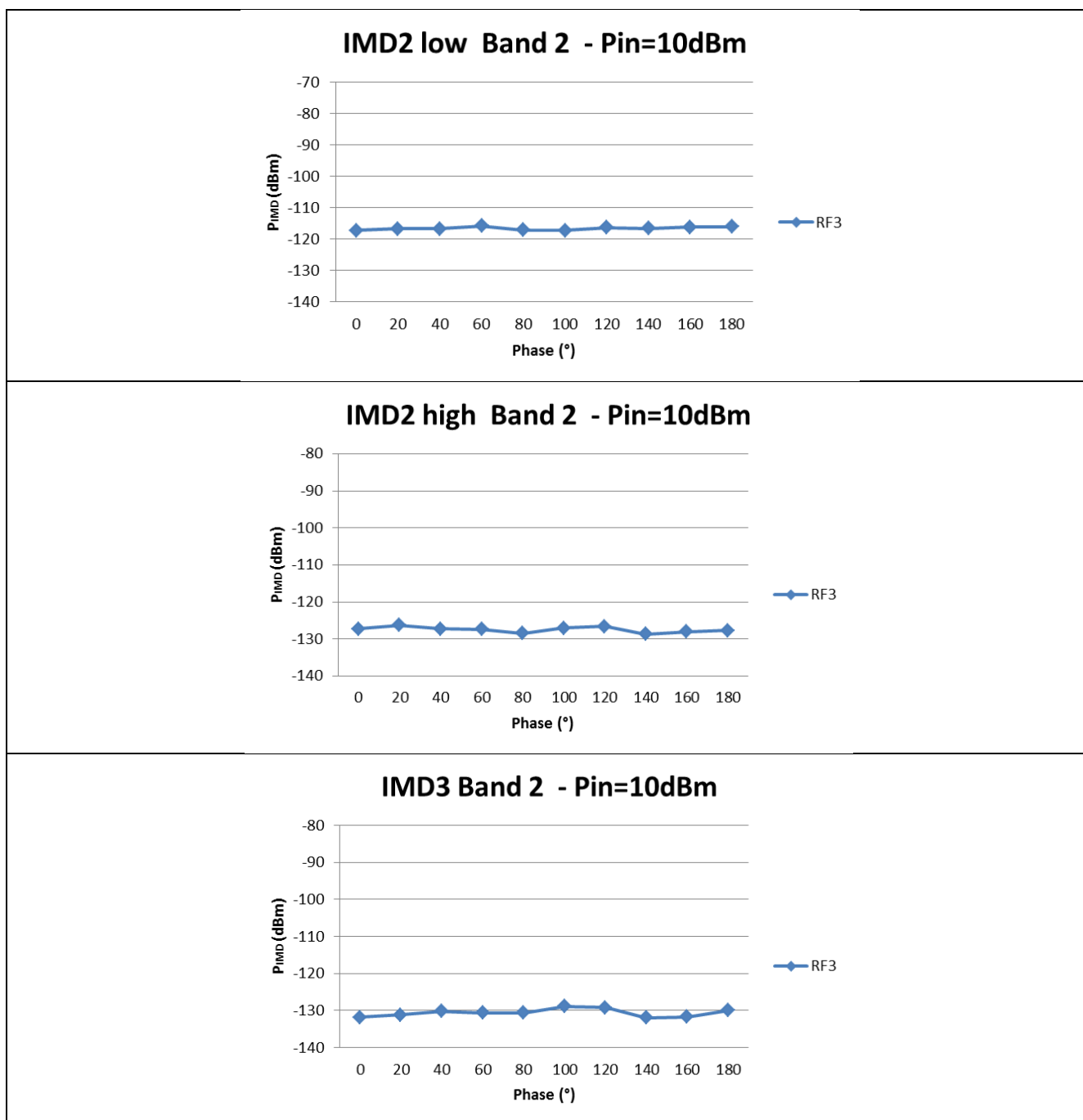


Figure 24 IMD products of Band 2 LTE and Pin=10dBm

Table 18 IMD products of Band 2 LTE

IMD Band 2 P _{Tx} =10dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =80MHz)	-117.32	-115.88
IMD2High (f _{blocker} =3840MHz)	-128.67	-126.21
IMD3 (f _{blocker} =1800MHz)	-131.98	-128.89

Intermodulation

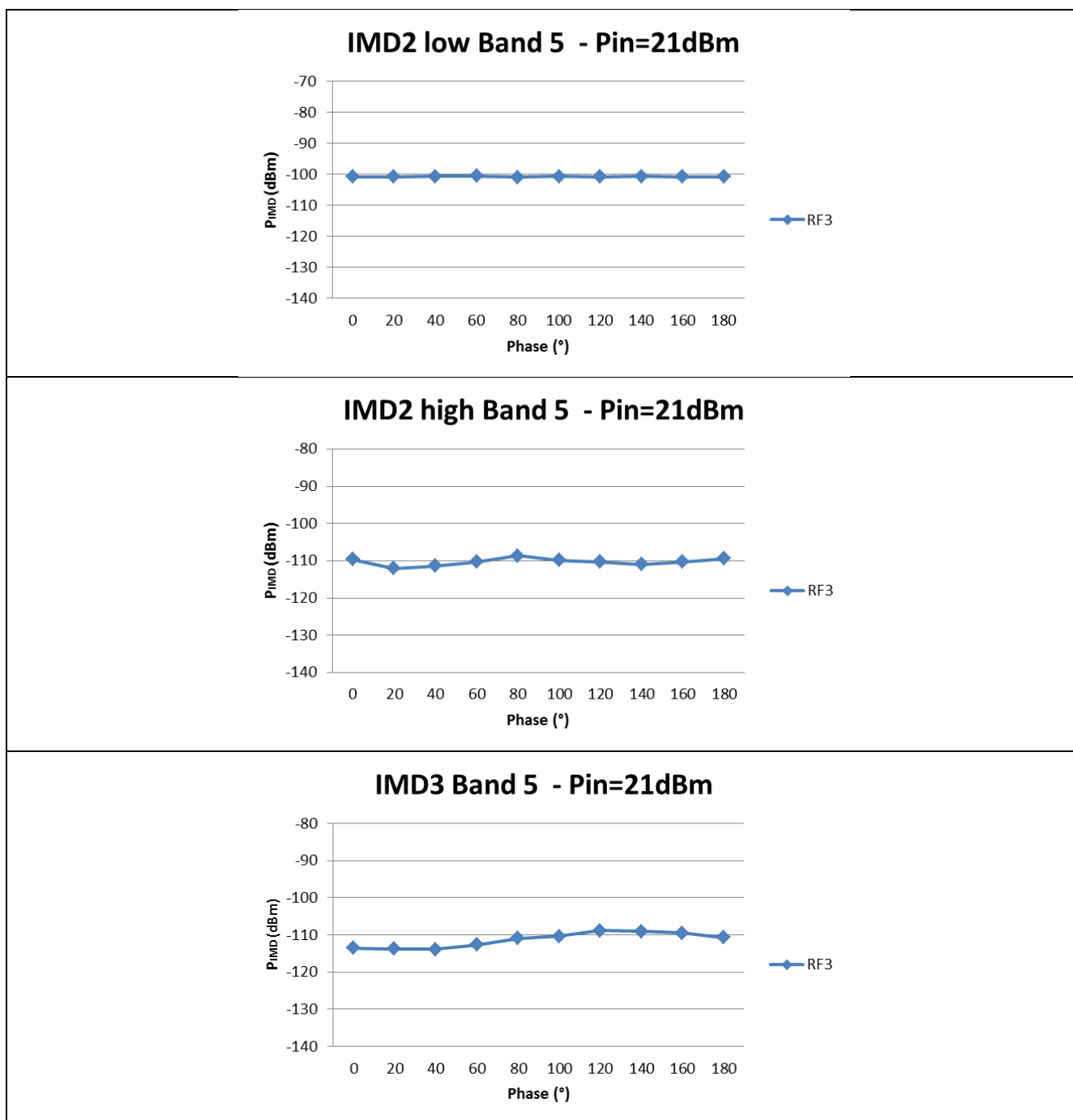


Figure 25 IMD products of Band 5 LTE and Pin=21dBm

Table 19 IMD products of Band 5 LTE

IMD Band 5 P _{Tx} =21dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =45MHz)	-100.91	-100.58
IMD2High (f _{blocker} =1718MHz)	-112.06	-108.60
IMD3 (f _{blocker} =791,5MHz)	-113.85	-108.79

Intermodulation

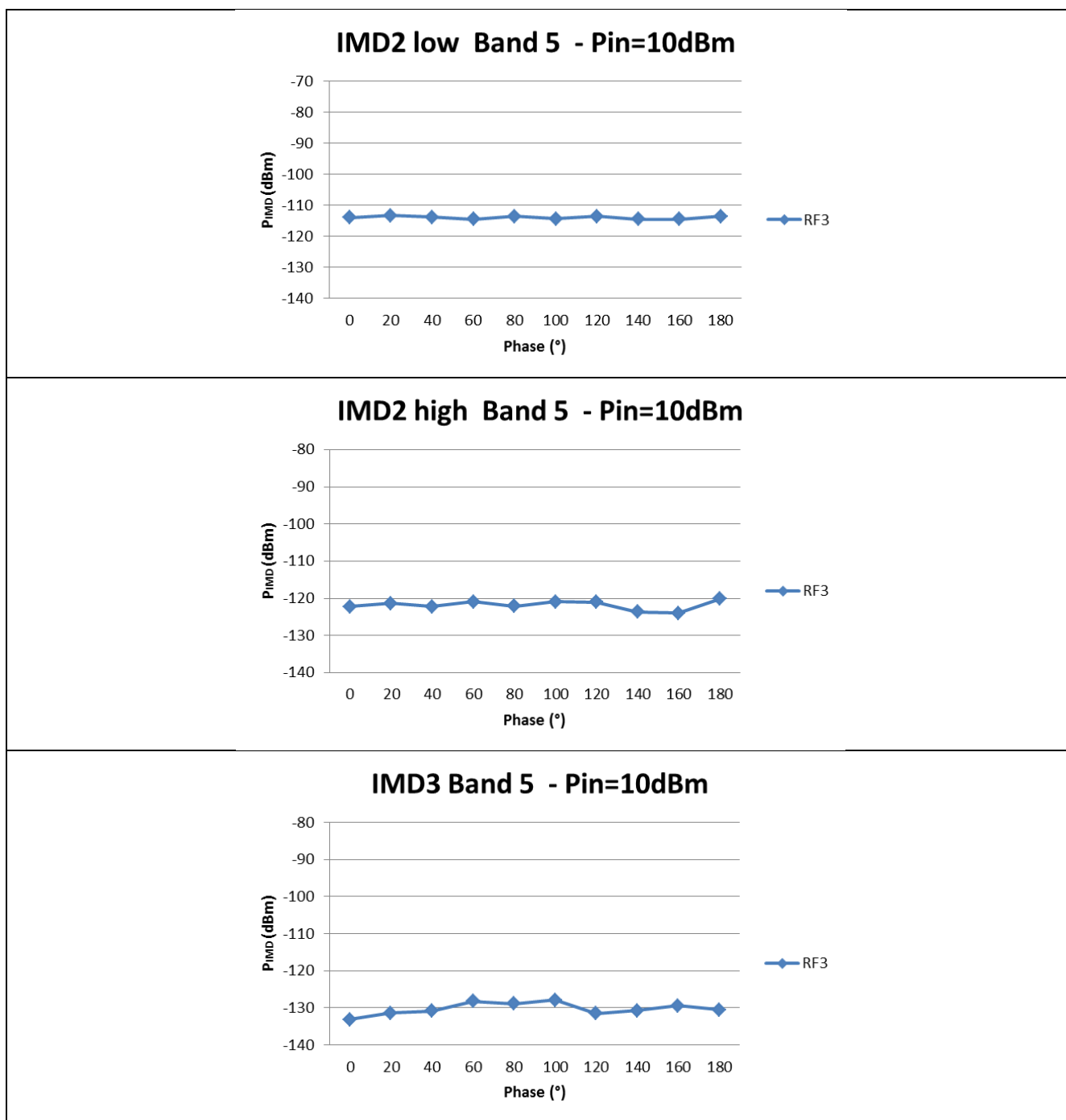


Figure 26 IMD products of Band 5 LTE and Pin=10dBm

Table 20 IMD products of Band 5 LTE

IMD Band 5 P _{Tx} =10dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =45MHz)	-114.52	-113.36
IMD2High (f _{blocker} =1718MHz)	-123.96	-120.12
IMD3 (f _{blocker} =791,5MHz)	-133.15	-127.95

Intermodulation

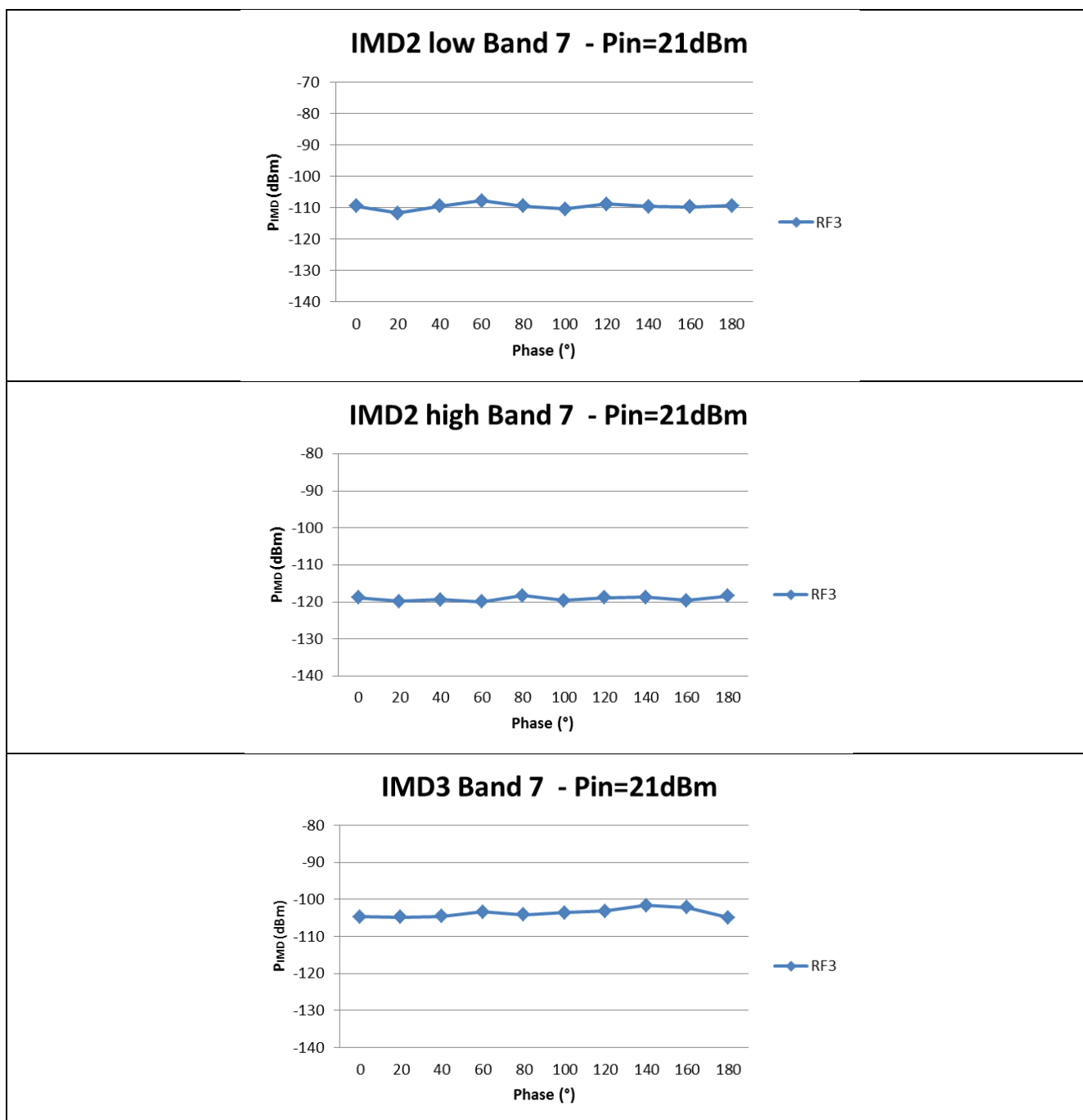


Figure 27 IMD products of Band 7 LTE and Pin=21dBm

Table 21 IMD products of Band 7 LTE

IMD Band 7 P _{TX} =21dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =120MHz)	-111.7	-107.79
IMD2High (f _{blocker} =5190MHz)	-119.87	-118.23
IMD3 (f _{blocker} =2415MHz)	-104.84	-101.56

Intermodulation

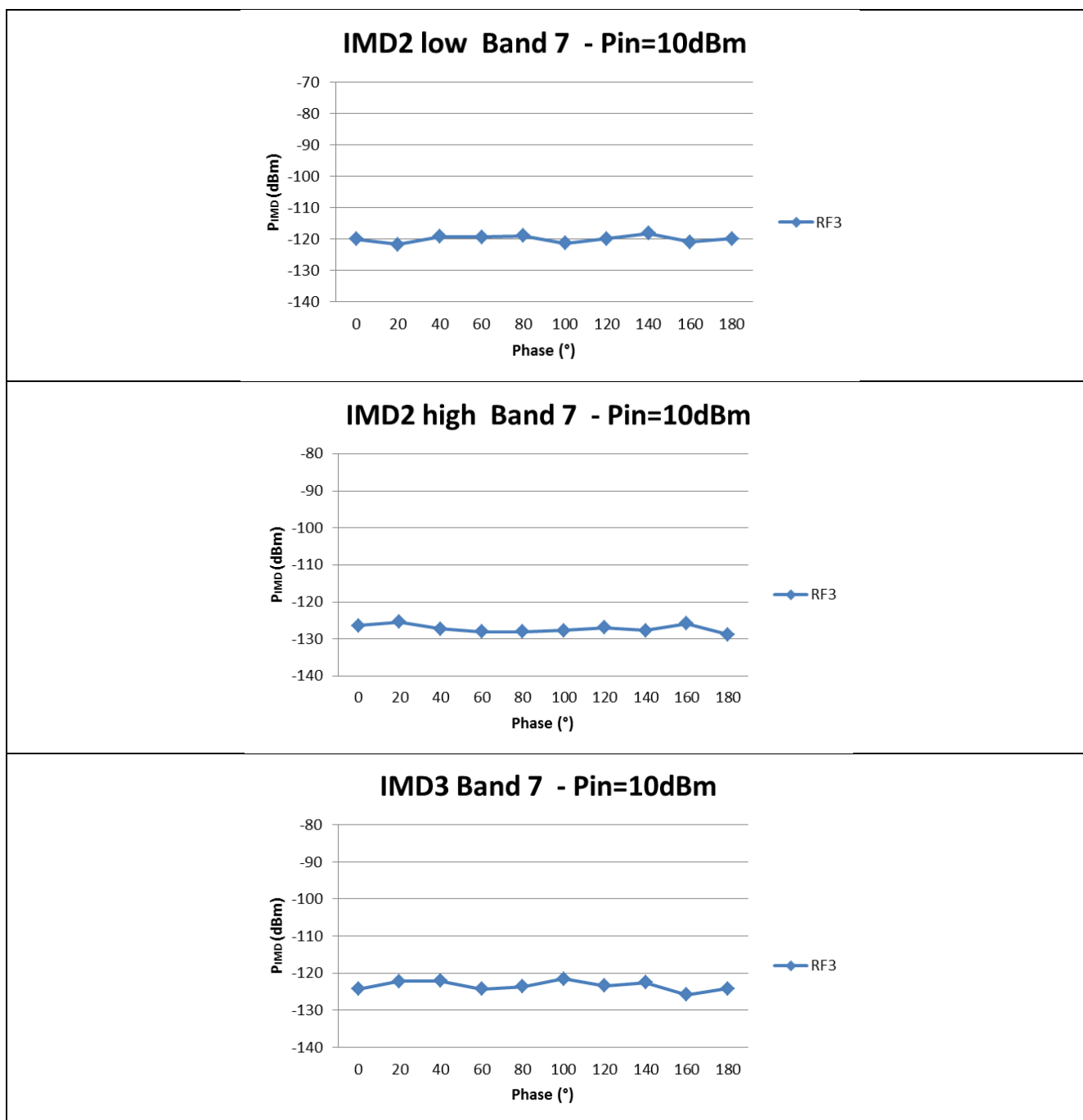


Figure 28 IMD products of Band 7 LTE and Pin=10dBm

Table 22 IMD products of Band 7 LTE

IMD Band 7 P _{Tx} =10dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =120MHz)	-121.71	-118.24
IMD2High (f _{blocker} =5190MHz)	-128.78	-125.33
IMD3 (f _{blocker} =2415MHz)	-125.83	-121.57

Intermodulation

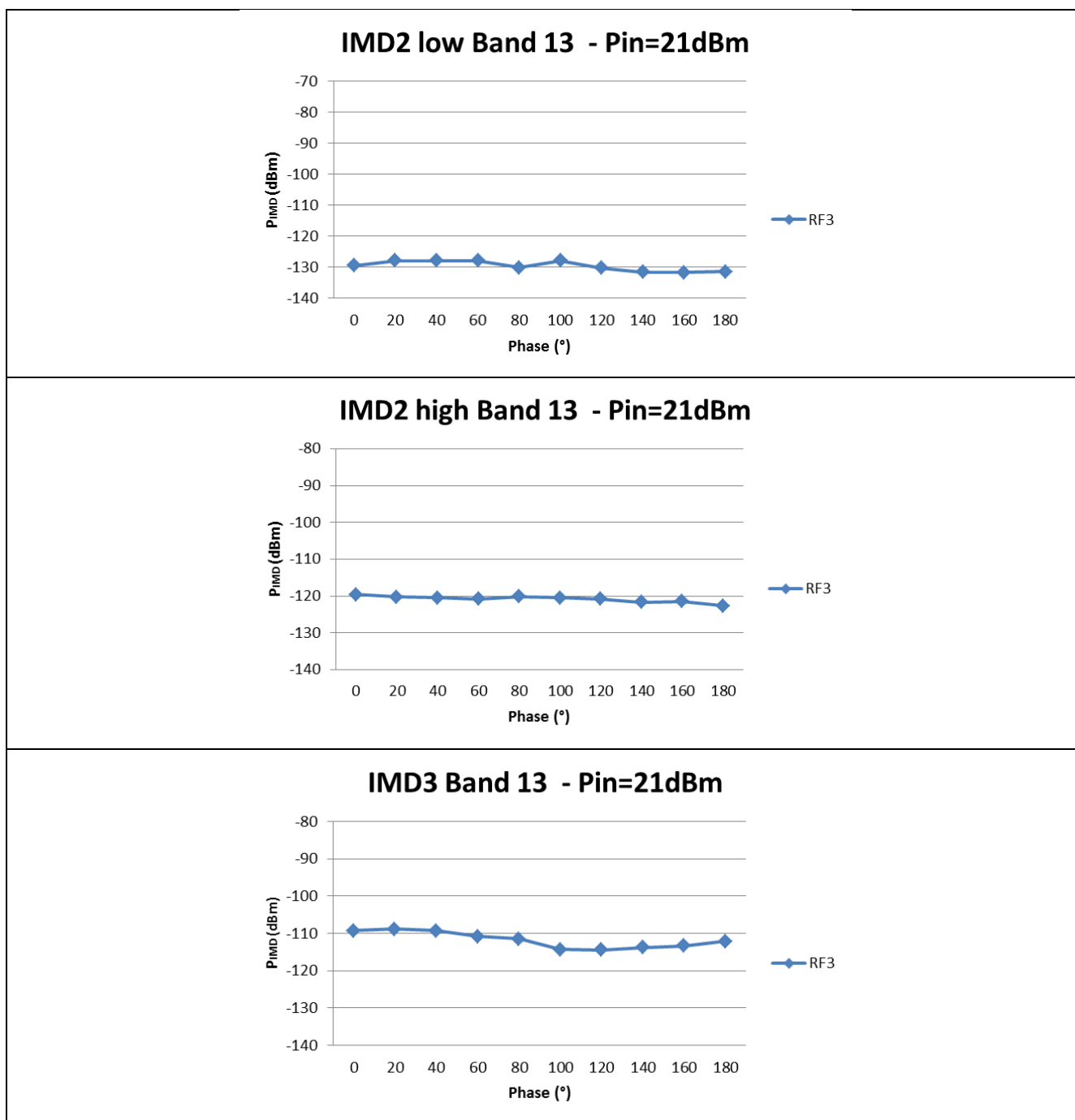


Figure 29 IMD products of Band 13 LTE and Pin=21dBm

Table 23 IMD products of Band 13 LTE

IMD Band 13 P _{Tx} =21dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =31MHz)	-131.68	-127.86
IMD2High (f _{blocker} =1533MHz)	-122.61	-119.58
IMD3 (f _{blocker} =813MHz)	-114.38	-108.85

Intermodulation

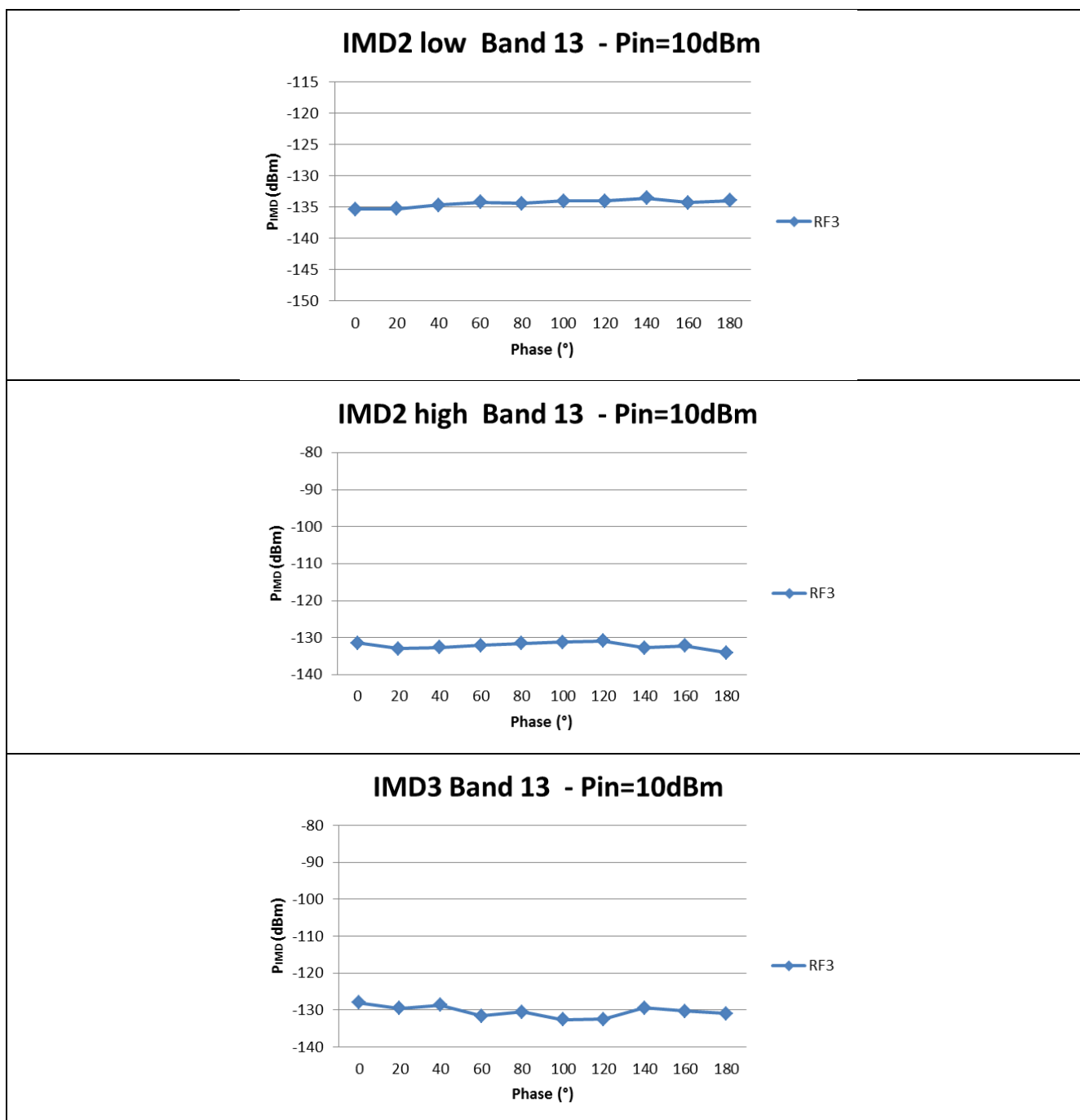


Figure 30 IMD products of Band 13 LTE and Pin=10dBm

Table 24 IMD products of Band 13 LTE

IMD Band 13 P _{Tx} =10dBm	RF3	
	Min	Max
IMD2Low (f _{blocker} =31MHz)	-138.37	-132.03
IMD2High (f _{blocker} =1533MHz)	-134.01	-130.84
IMD3 (f _{blocker} =813MHz)	-132.58	-128.06

6 Harmonic Generation

Harmonic generation is another important parameter for the characterization of a RF switch. RF switches have in such a Differential Band select Switching application to deal with high RF levels, up to 24 dBm. With this high RF power at the input of the switch harmonics are generated. This harmonics (2nd and 3rd) can disturb the other reception bands or cause distortion in other RF applications (GPS, WLAN) within the mobile phone.

6.1 Measurement Setup

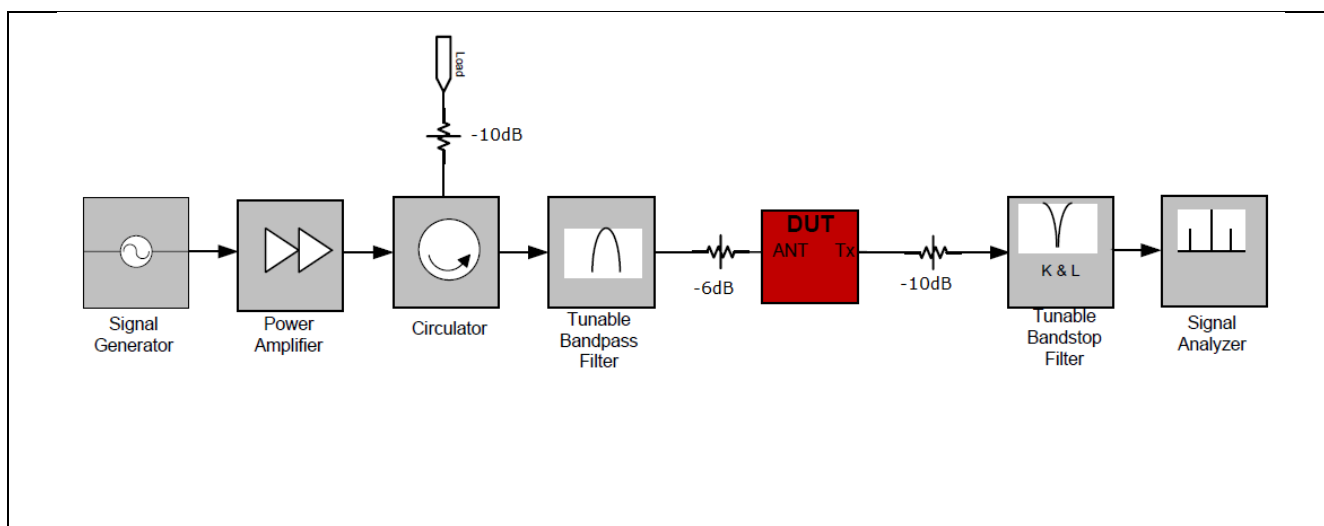


Figure 31 Setup for harmonics measurement

The results for the 2nd and 3rd order harmonic generation at different Bands are shown from for all RF ports on the following points.

The x-axis show the input power and the y-axis show the generated harmonics in dBm.

6.2 Measurement results

6.2.1 Harmonics for Band 1

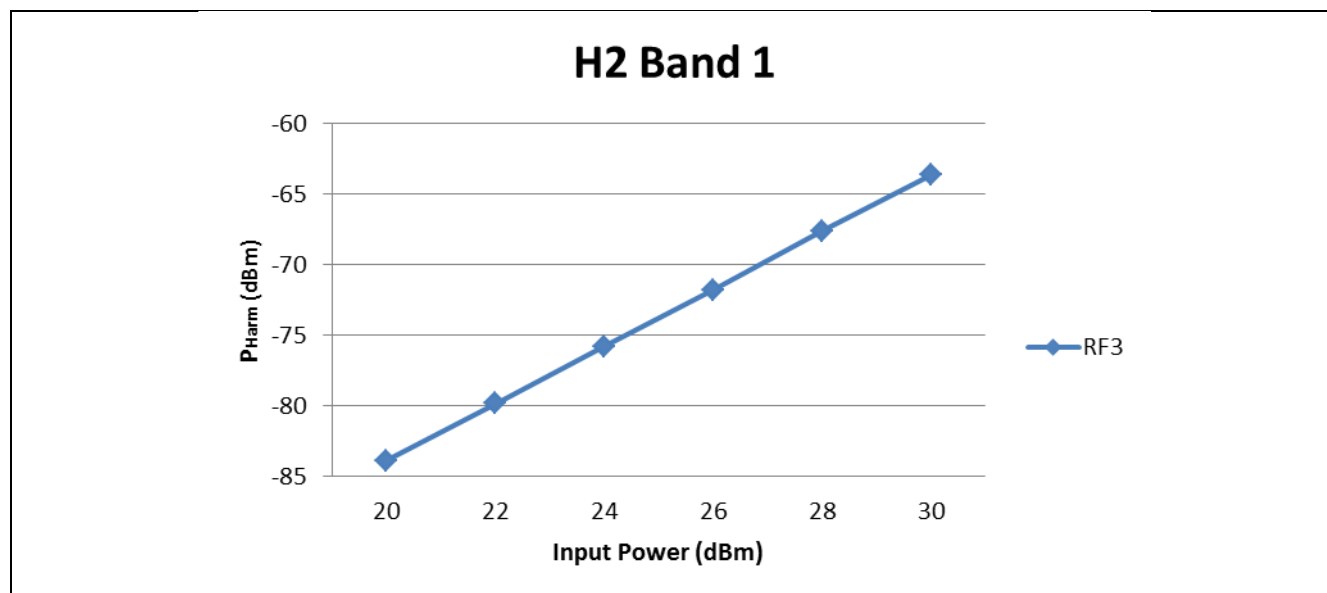


Figure 32 2nd harmonics at $f_c=1950\text{MHz}$

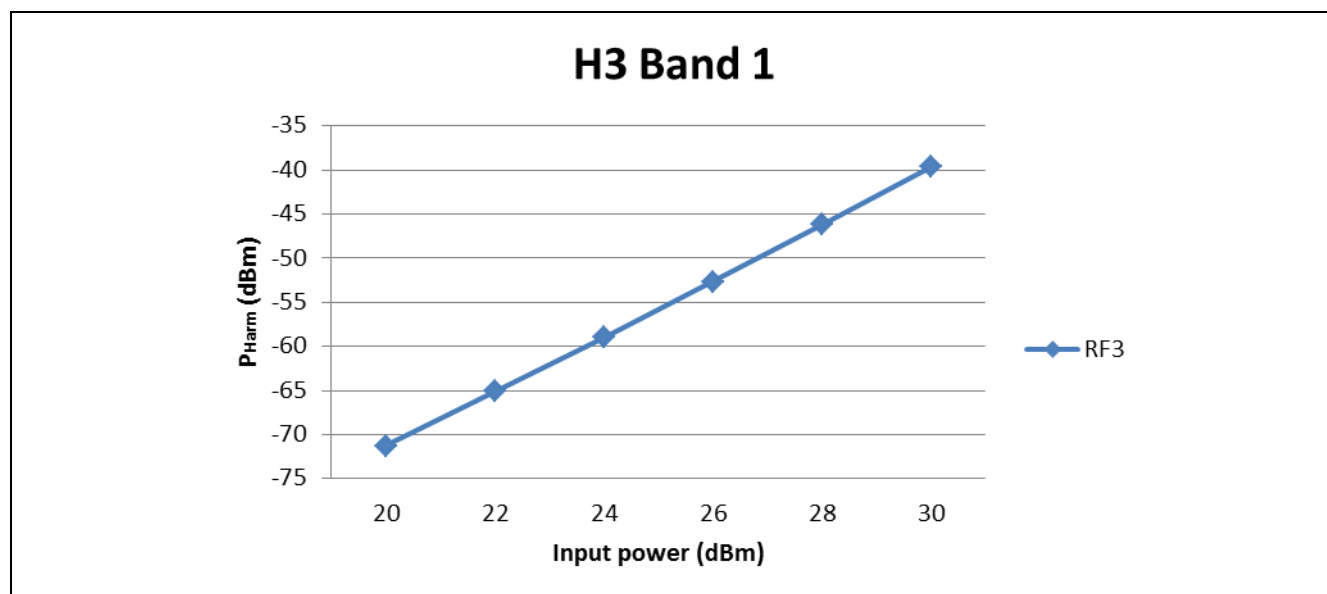


Figure 33 3rd harmonics at $f_c=1950\text{MHz}$

$f_c=1950\text{MHz}$		H2	H3
		RF3 (dBm)	RF3 (dBm)
RFin (dBm)	20	-83.86	-71.29
	22	-79.83	-65.13
	24	-75.79	-58.97
	26	-71.76	-52.68
	28	-67.61	-46.18
	30	-63.64	-39.65

6.2.2 Harmonics for Band 2

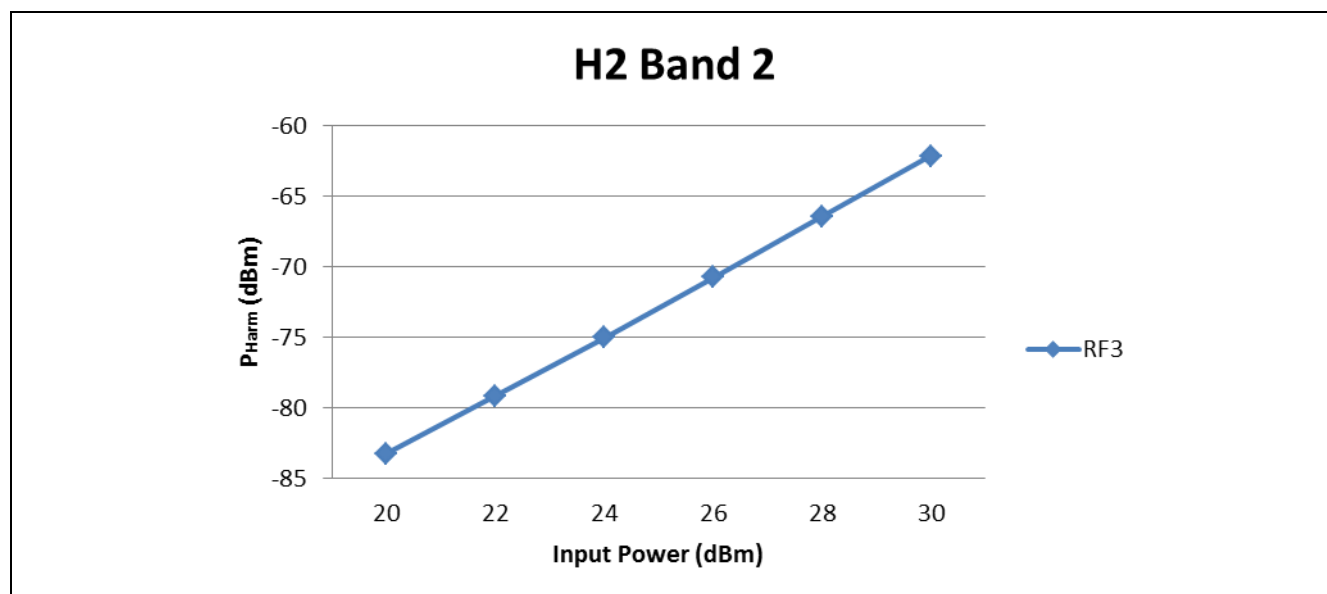


Figure 34 2nd harmonics at fc=1880MHz

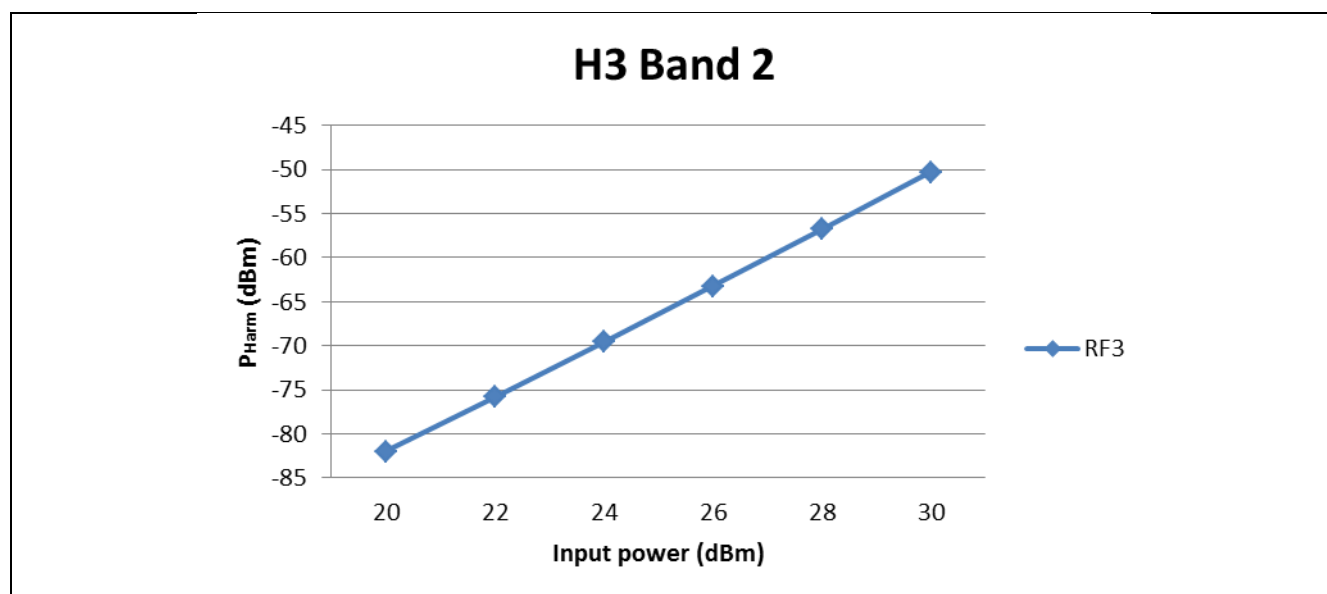


Figure 35 3rd harmonics at fc=1880MHz

fc=1880MHz		H2	H3
		RF3 (dBm)	RF3 (dBm)
RFin (dBm)	20	-83.21	-81.99
	22	-79.12	-75.83
	24	-75.02	-69.58
	26	-70.75	-63.28
	28	-66.41	-56.77
	30	-62.16	-50.31

6.2.3 Harmonics for Band 5

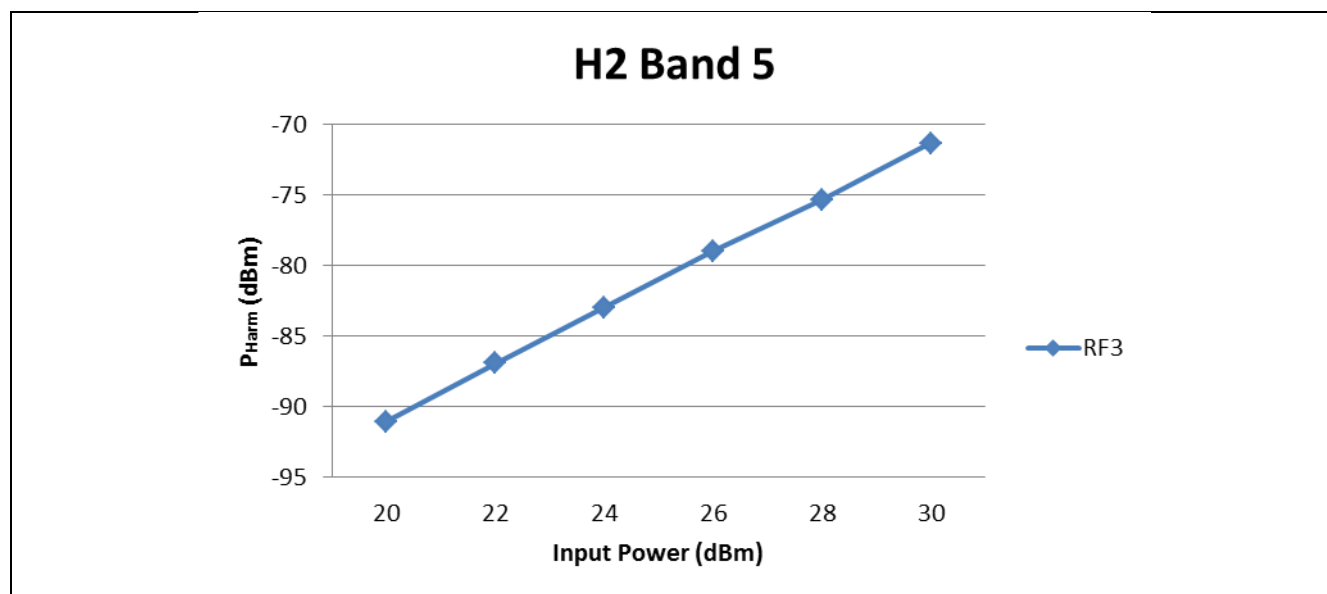


Figure 36 2nd harmonics at $f_c=836,5\text{MHz}$

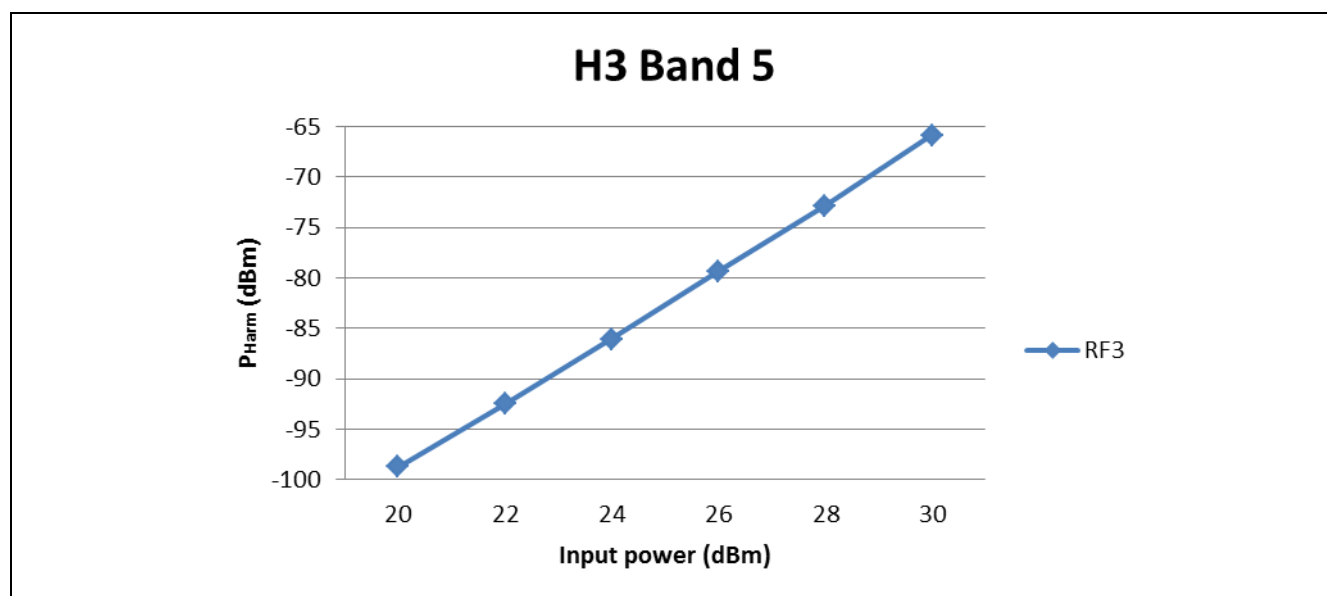


Figure 37 3rd harmonics at $f_c=836,5\text{MHz}$

$f_c=836,5\text{MHz}$		H2	H3
		RF3 (dBm)	RF3 (dBm)
RFin (dBm)	20	-91.03	-98.72
	22	-86.91	-92.46
	24	-82.97	-86.07
	26	-78.98	-79.33
	28	-75.33	-72.83
	30	-71.33	-65.86

6.2.4 Harmonics for Band 7

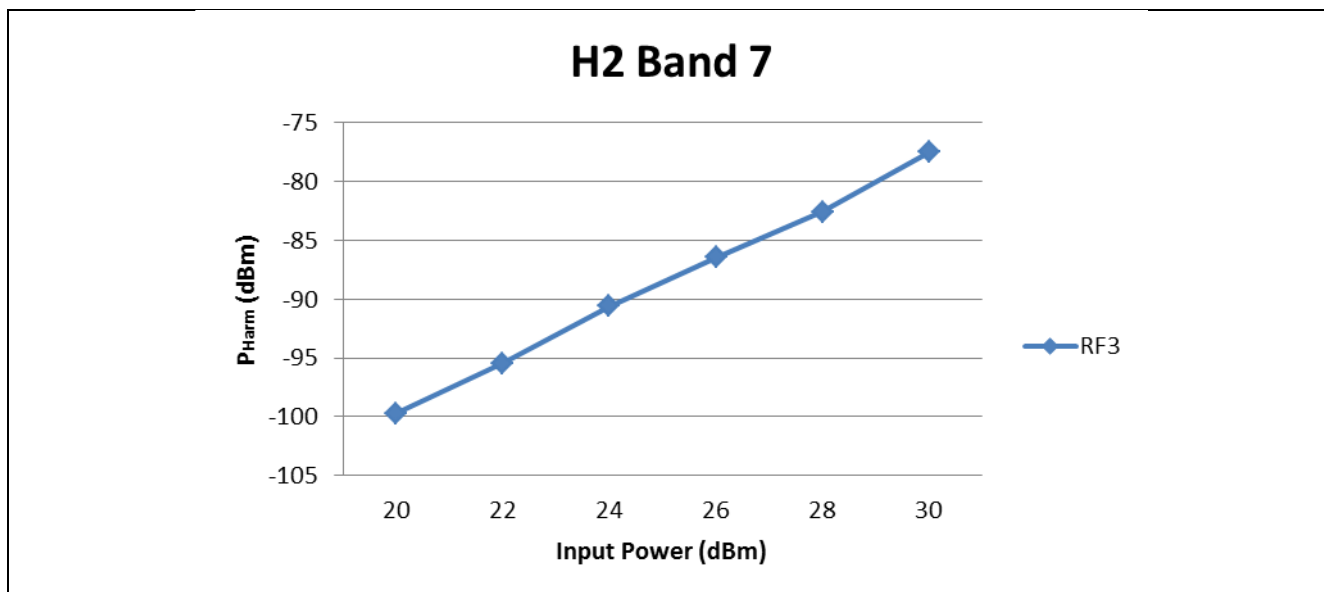


Figure 38 2nd harmonics at $f_c=2535\text{MHz}$

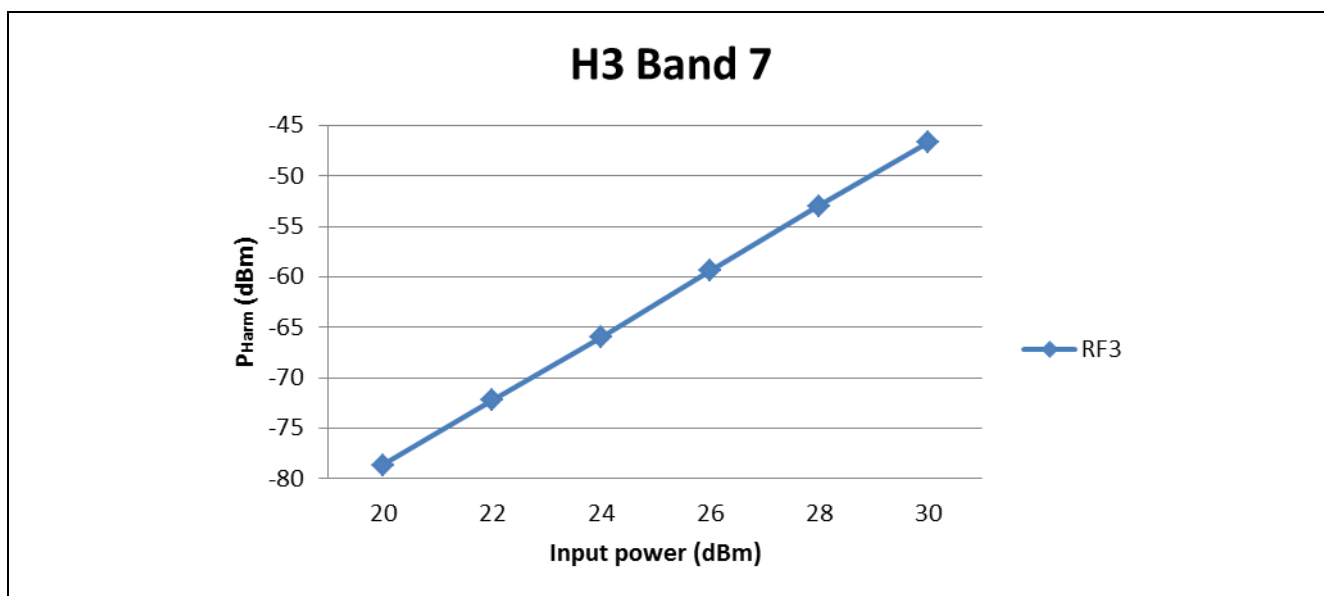


Figure 39 3rd harmonics at $f_c=2535\text{MHz}$

$f_c=2535\text{MHz}$		H2	H3
		RF3 (dBm)	RF3 (dBm)
RFin (dBm)	20	-99.71	-78.64
	22	-95.43	-72.24
	24	-90.58	-66.03
	26	-86.44	-59.36
	28	-82.58	-52.99
	30	-77.54	-46.65

6.2.5 Harmonics for Band 13

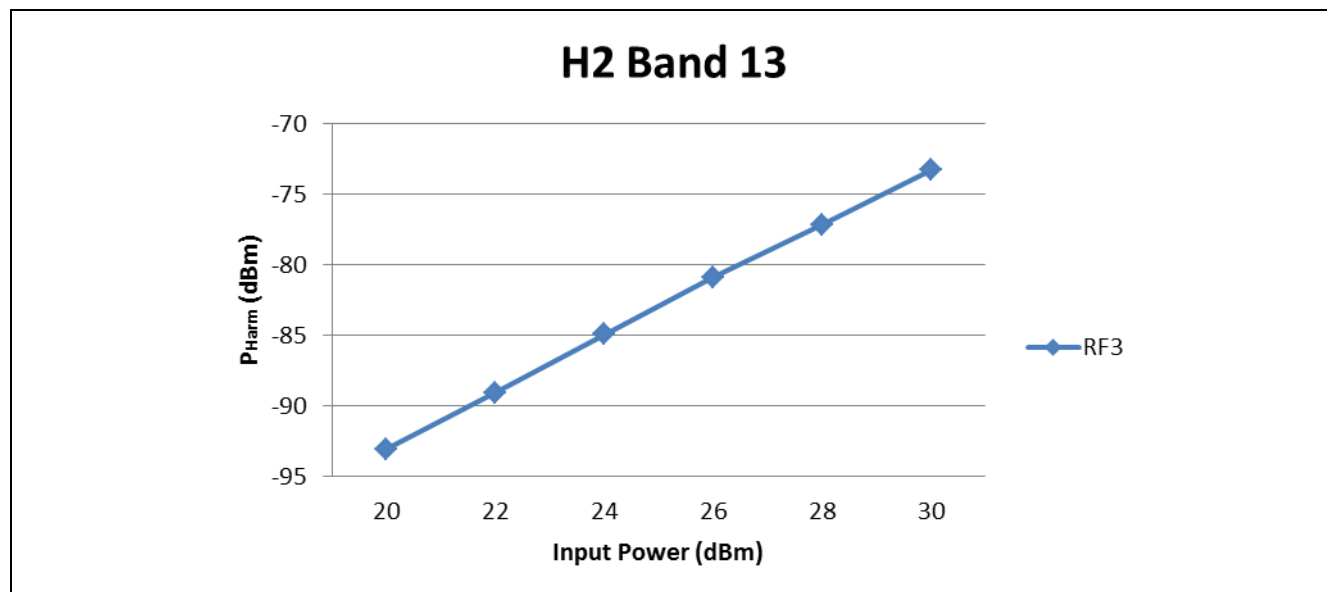


Figure 40 2nd harmonics at fc=782MHz

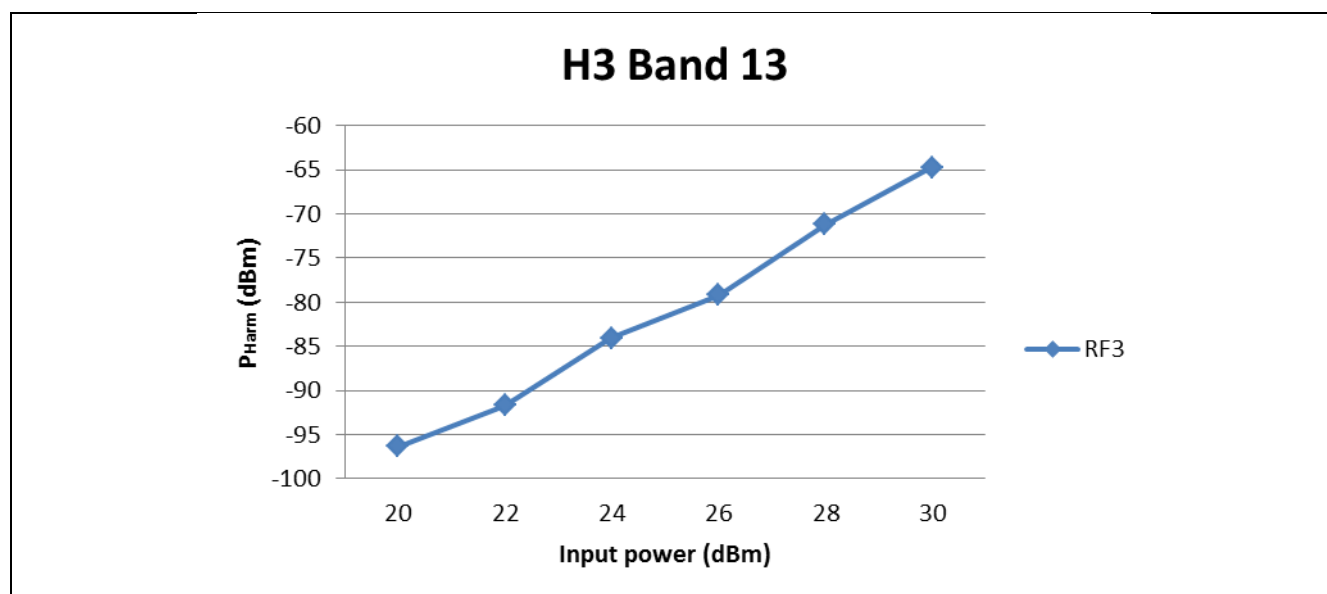


Figure 41 3rd harmonics at fc=782MHz

fc=782MHz		H2	H3
		RF3 (dBm)	RF3 (dBm)
RFin (dBm)	20	-93.04	-96.32
	22	-89.03	-91.72
	24	-84.92	-84.08
	26	-80.88	-79.21
	28	-77.14	-71.24
	30	-73.29	-64.77

7 Evaluation Board and Layout Information

7.1 Evaluation Board

In this application note, the following PCB is used:

PCB Marking: **BGS1xGA14**

PCB material: **Rogers**

ϵ_r of PCB material: **3.55**

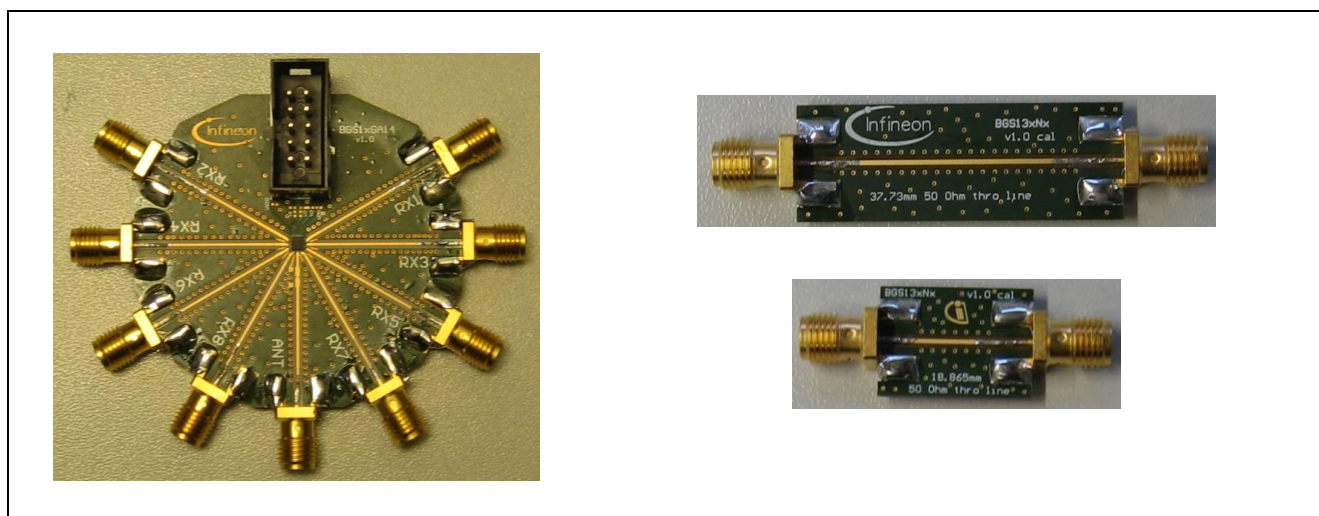


Figure 42 BGS15GA14 Application Board and deembedding kit

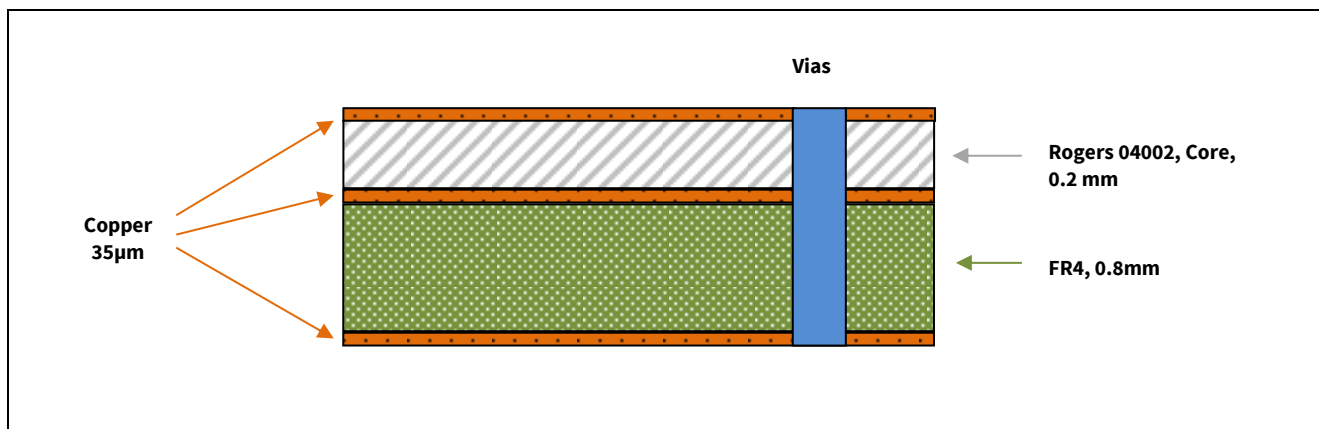


Figure 43 PCB cross-section of the evaluation board for BGS15GA14

7.2 Measurement description and deembedding

Below is a picture of the evaluation board used for the measurements (Figure 44). The board is designed in the way that all connecting 50 Ohm lines have the same length.

To get correct called “device level” measurement values for the insertion loss of the BGS15GA14 all influences and losses of the evaluation board, lines and connectors have to be eliminated. Therefore a separate de-embedding board, representing the line length is necessary.

After full port calibration of the network analyzer (NWA) a deembedding has to be done in several steps:

- Use an SMA connector whose inner conductor has been removed to tune out one of the SMA to PCB transitions using the port extension on one port (Figure 44). Turn port extensions on.
- Measure S21 of the half-thru structure (Figure 42, smallest board) with port extension enabled. The result is the de-embedding of S21 including only one SMA connector and the transmission line to the chip. Store this as S-parameter (s2p) file.
- Turn all port extension off.
- Load the stored s-parameter file as de-embedding on all used NWA ports
- Check insertion loss with the de-embedding through board (Figure 42 right upper board)

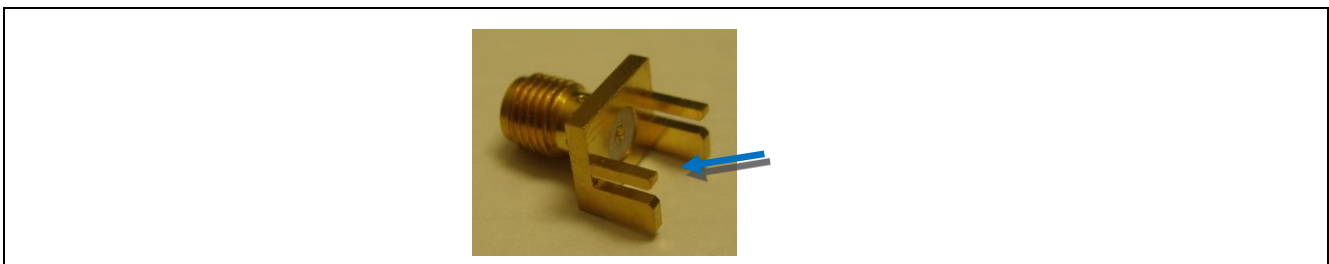


Figure 44 SMA connector for deembedding procedure

If the check of the deembedding shows an insertion loss of the through about ± 0.04 dB (depending on the measurement setup accuracy, e.g. NWA) then the Device itself can be measured.

8 Authors

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André Dewai, Senior Application Engineer of the Business Unit “RF and Protection Devices”

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