

Migrating from FM25C160 to FM25C160B

About this document

Scope and purpose

AN315 discusses the key differences that need to be considered when migrating from FM25C160 to FM25C160B. FM25C160 is now obsolete and this application note explains how FM25C160B is a replacement for FM25C160.

Associated part family

FM25C160, FM25C160B

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1 Introduction

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FM25C160B, a 16-Kbit SPI F-RAM, is a replacement device for FM25C160, which is now obsolete. The two devices are identical in terms of pinouts and read/write functionality. In terms of speed, both operate up to 20 MHz and have the same timing specifications. This application note discusses the key differences between the two devices that need to be considered when migrating from FM25C160 to FM25C160B.

2 Drop-in replacement or not?

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For most designs, FM25C160B is a drop-in replacement for FM25C160. From a software point of view, the two devices are identical. From a hardware point of view the key difference is the lower active current in FM25C160B. Additionally, FM25C160B datasheet adds a power-up and power-down ramp rate specification of 30 $\mu\text{s} / \text{V}$ and a power-up to first-access specification of 1 ms.

[Table 1](#) shows the compatibility chart of FM25C160 and FM25C160B. For a detailed comparison, see [Table 3](#).

Table 1 Compatibility chart

FM25C160 feature or spec	Is FM25C160B compatible?
Package	Yes
Pinout	Yes
Temperature range	Yes
Operating voltage	Yes
Operating current	Yes
Standby current	Yes
Read / Write function	Yes
Timing / frequency	Yes
Data retention	Refer to Table 3
Endurance	Yes

3 Ordering part numbers

3 Ordering part numbers

Table 2 gives the recommended FM25C160B ordering part numbers that correspond to the now obsolete FM25C160 ordering part numbers.

Table 2 Recommended ordering part numbers for migration

FM25C160		FM25C160B		Comments
Ordering part number	Status	Ordering part number	Status	
FM25C160-G	Obsolete	FM25C160B-G	In production	No hardware or software change is required.
FM25C160-GTR		FM25C160B-GTR		

4 Comparison of FM25C160 and FM25C160B

4 Comparison of FM25C160 and FM25C160B

Table 3 gives a detailed comparison of the two devices.

Table 3 Detailed comparison

	FM25C160	FM25C160B	Comments
Package types	-G	-G	Identical “green” SOIC package
Package outlines	SOIC-8	SOIC-8	Identical outline and board footprint
Pinout	-	-	Identical
Temperature range	–40°C to +85°C	–40°C to +85°C	Identical
Operating voltage range	4.5 V to 5.5 V	4.5 V to 5.5 V	Identical
Active supply current	400 µA @ 1 MHz 8.0 mA @ 20 MHz	250 µA @ 1 MHz 4.0 mA @ 20 MHz	FM25C160B offers lower active current
Standby current	10 µA	10 µA	Identical
Read / write function	-	-	Identical 2-byte addressing, Identical op-codes
Clock frequency	20 MHz	20 MHz	Identical
Data retention	45 years (+85°C)	10 years (+85°C) 38 years (+75°C) 151 years (+65°C)	Data retention is lower
Endurance (Write/Read Cycles)	1E+12	1E+14	FM25C160B has better endurance
V _{DD} Power-Up Ramp Rate (t _{VR})	-	30 µs / V	Power-up ramp rate should be slower than 30 µs / V for FM25C160B
V _{DD} Power-Down Ramp Rate (t _{VF})	-	30 µs / V	Power-down ramp rate should be slower than 30 µs / V for FM25C160B
Power-Up to first access (t _{PU})	-	1 ms	After power-up, the first access of FM25C160B should be after 1 ms

5 Critical considerations

5 Critical considerations

You should consider all the parameter differences mentioned in [Table 3](#) during the migration to FM25C160B. This section discusses the critical differences. System designers should also review the [datasheet](#) when migrating to the new part.

6 V_{DD} Ramp Rate

6 V_{DD} Ramp Rate

V_{DD} power-up and power-down ramp rate specifications are added in FM25C160B device. Ensure that the power-up and power-down ramp rates are slower than 30 $\mu\text{s} / \text{V}$ in your system.

7 Power-up to first access

7 Power-up to first access

Power-up to first access specification is added in FM25C160B device. Ensure that the FM25C160B device is accessed only after 1 ms from power-up.

8 Summary

8 Summary

AN315 discussed the differences between FM25C160 and FM25C160B that need to be considered during migration to the FM25C160B.

Related documents

Related documents

Datasheet

[FM25C160B: 16-Kbit \(2 K × 8\) Serial \(SPI\) F-RAM datasheet](#)

Application note

[AN304 – SPI GUIDE FOR F-RAM](#)

Revision history

Revision history

Document version	Date of release	Description of changes
**	2013-03-26	New spec.
*A	2014-03-05	- Updated to Cypress Template. - Updated Endurance for FM25C160B from E+12 to E+14. - Updated “V_{DD} Power-Down Ramp Rate” for FM25C160B from 100 µs / V to 30 µs / V. - Updated “Power-Up to First Access” for FM25C160B from 10 ms to 1 ms
*B	2014-09-19	- Changed title from “Differences between FM25C160 and FM25C160B” to “Migrating from FM25C160 to FM25160B.” - Updated abstract. - Added “Ordering Part Numbers” section. - Added title for Table 3. - Added “Related Documents” section.
*C	2026-07-16	Template update; no content update

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