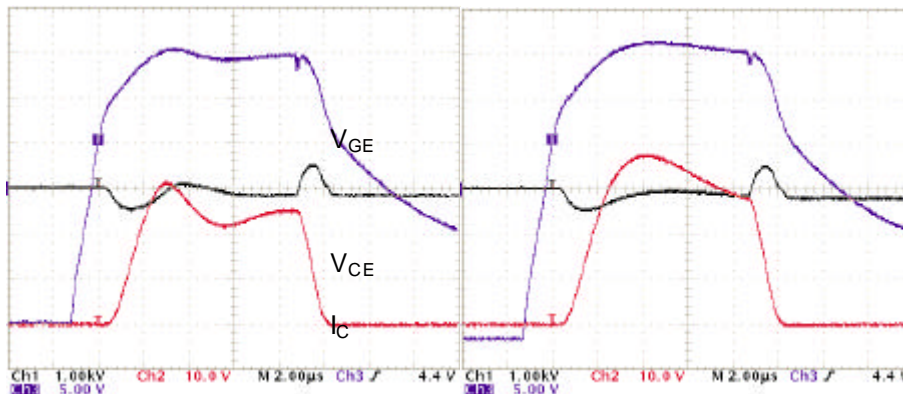


APPLICATION NOTE

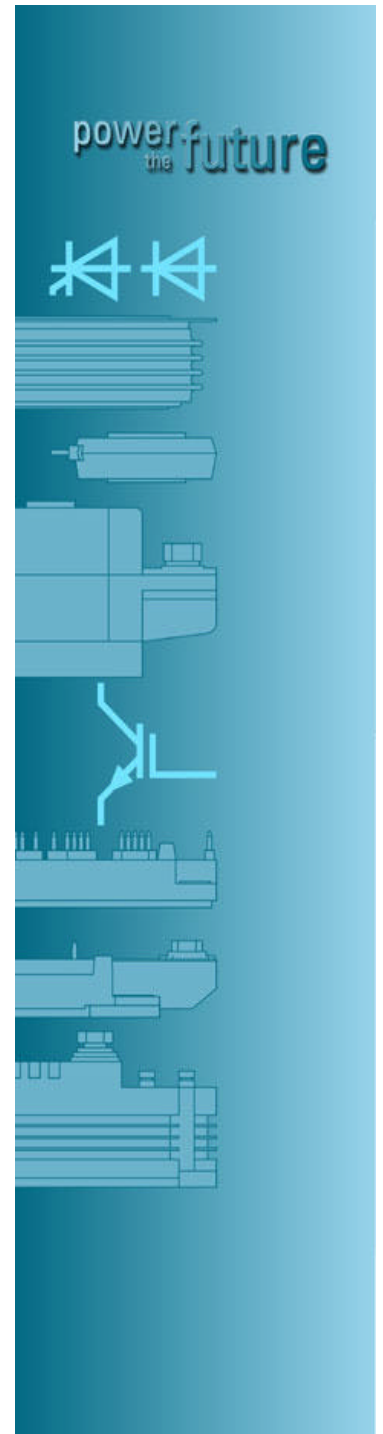
Short Circuit Operation of 6.5kV IGBTs

This application note presents aspects for driving IGBTs, which allow a safe short circuit operation of 6.5kV IGBTs. The following picture shows a short circuit turn-off of a FZ600R65KF1 with and without a clamping of the gate voltage under identical operation conditions (V_{ce} (black) 1kV/div, I_c (red) 1kA/div, V_{ge} (blue) 5V_{GE}/div). It is apparent that the gate voltage rate of rise on the left side is limited and no overshoot occurs: the max. gate voltage reaches 15.3V and the peak short circuit current is limited to approximately 3000A. In contrast, in the picture on the right, the gate voltage rises without clamping to 16.3V and the short circuit current reaches a peak value of 3800A.



The reason for this behavior is the positive feedback provided by the Miller capacity, which feeds back a positive current into the gate when the collector voltage is rising. This results in an increase in the gate voltage as well as the short circuit peak current. They may exceed the maximum admissible current level (for the FZ600R65KF1 e.g. $6 \times I_{nom} = 3600A$), which can lead to the thermal destruction of the module.

When considering the tolerances of the line voltages, common working voltages for 6.5kV IGBT are in the range of 3000...4400V DC. Due to a strong increase of the Miller capacity at low V_{cc} , possible oscillations at low voltage levels may occur. We strongly recommend not to perform short circuit tests at voltages below 2500V.

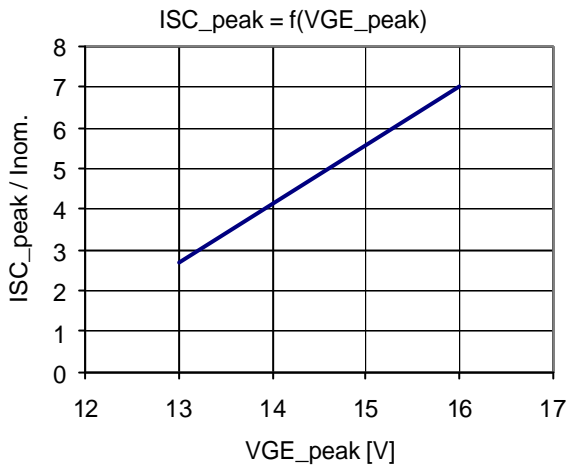


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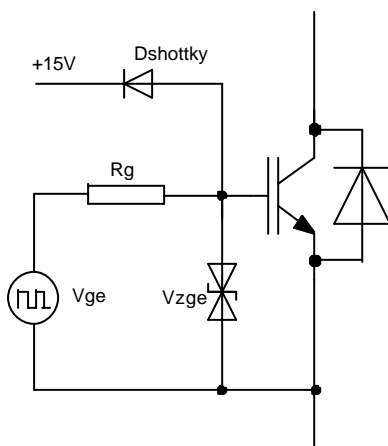
The dependency between peak short circuit current and gate voltage is given in the diagram on the left. To ensure the safe operation under short circuit conditions, an effective limitation of the SC peak current by means of clamping the gate voltage to values below 15.3 V has to be guaranteed.

Measures to limit the gate voltage and SC current respectively are:

- 1) Clamping of the gate voltage to a fixed potential

If the gate driver can supply a stable, internal 15V, the gate voltage should be clamped to this potential by a feedback diode with a low voltage drop (Schottky diode). The lower the inductive connection between the 15V supply and gate terminals, the more effective this measure will be.

- 2) Clamping of the gate voltage by gate-emitter Zener diodes



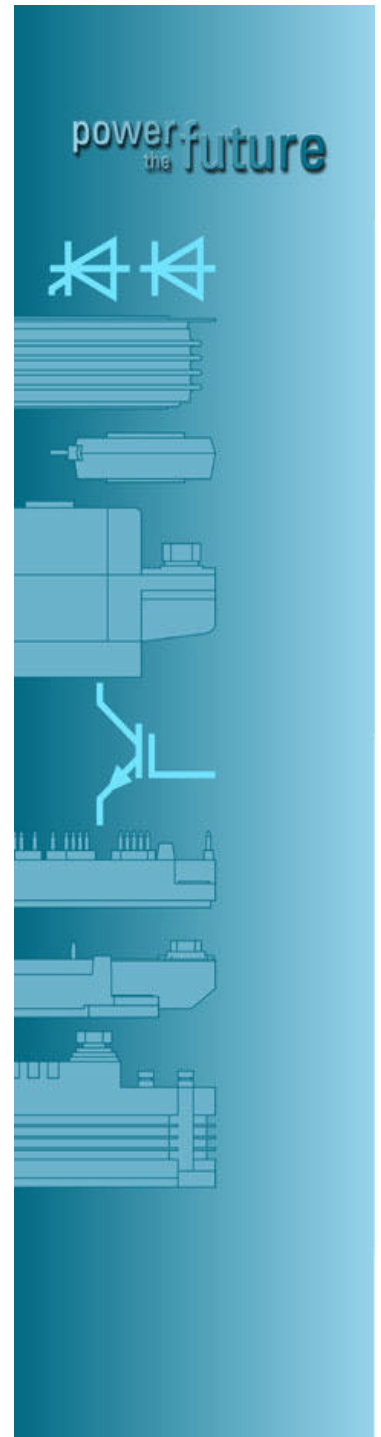
The closer the diodes are mounted to the auxiliary terminals of the module, the more effective this measure. Diodes with low temperature drift and tolerance (e.g. transient voltage suppressor diodes) should be used.

In accordance with the above explanations, the following condition has to be fulfilled at room temperature:

$$V_z \cdot (1 + a_T \cdot (\vartheta - 25^\circ\text{C})) \cdot (1 + \text{tol}) \leq 15.3\text{V} \text{ with}$$

V_z : nominal Zener diode voltage

a_T : temperature coefficient



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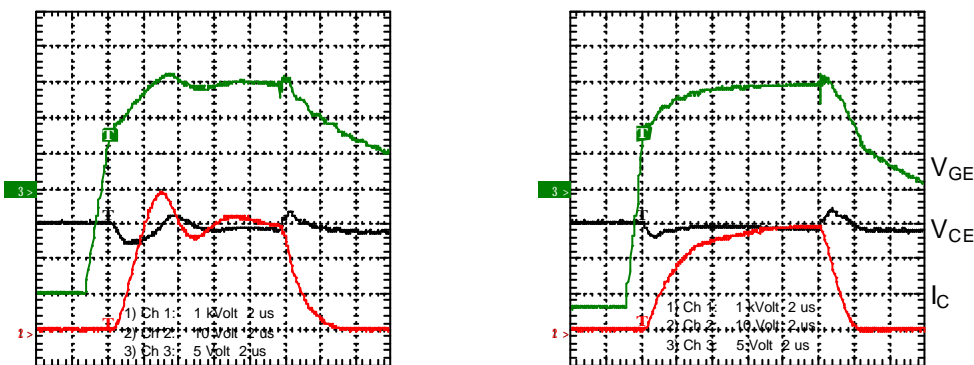
ϑ : max. operation temperature tol: tolerance of the Zener diode
 selected: 1.5KE6.8CA plus 1.5KE7.5CA (bi-directional) with
 α_T : $8 \cdot 10^{-4} \text{K}^{-1}$ (~12mV/K) ϑ : 50°C (assumption) tol: 5%

At a chosen gate voltage of 14.0V the turn-on resistor R_{gon} can be reduced to 2.7 Ohm. This can keep the switching losses at the value given in the data sheet for 15V. The reduction of the gate voltage has a negligible influence on the turn-off losses.

3) Introduction of a negative emitter feedback

For this measure the emitter of the gate-driver must not be – as is customary – connected to the auxiliary emitter, but to the main emitter terminals. The induced voltage drop inside the module is superimposed to the externally applied gate voltage. Due to the typically high short circuit di/dts , the negative feedback leads to an effective reduction of the gate voltage. In addition, an overshoot of the gate voltage can be avoided.

The impact of this measure is shown in the following picture:



This dynamic reduction of the gate voltage also lowers the di/dt of the regular turn-on process. By omitting the CGE which is given in the data sheet, but not required under this operation condition any longer, this measure does not result in an increase in the turn-on losses.

