

Effective factors for voltage sharing:

	static	dynamic
device characteristics	$\Delta I_{CES}, \Delta T_j$	$\Delta T_j, \Delta V_{ge_{th}}, \Delta t_{d_{on}}, \Delta t_{d_{off}}, \Delta Q_{rr}$
driver	-	$\Delta L_{wire}, \Delta t_{on}, \Delta t_{off}$

Recommendations:

use devices of one production lot (smallest parameter deviations guaranteed)

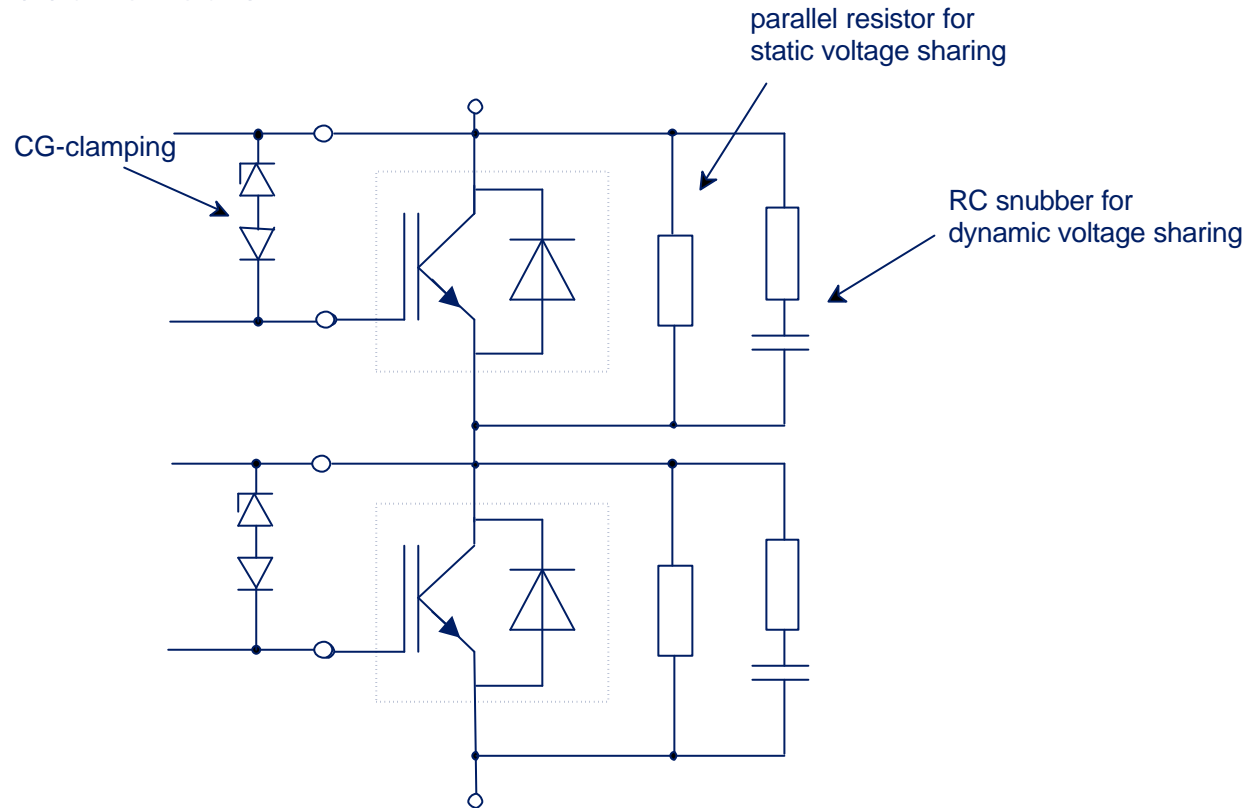
symmetrical design of gate driver (minimize deviations of delays)

symmetrical cooling conditions (identical heat-sink temperature and flow rate below the series connected devices)

use parallel resistors with $I_R \approx 5..10 \times I_{CESmax}$ to enforce static voltage sharing

use RC snubber to equalize voltage rate of rise

Snubber circuit:



Alternative: use of 3- or multi-level circuit configurations