



Please note that Cypress is an Infineon Technologies Company.

The document following this cover page is marked as “Cypress” document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

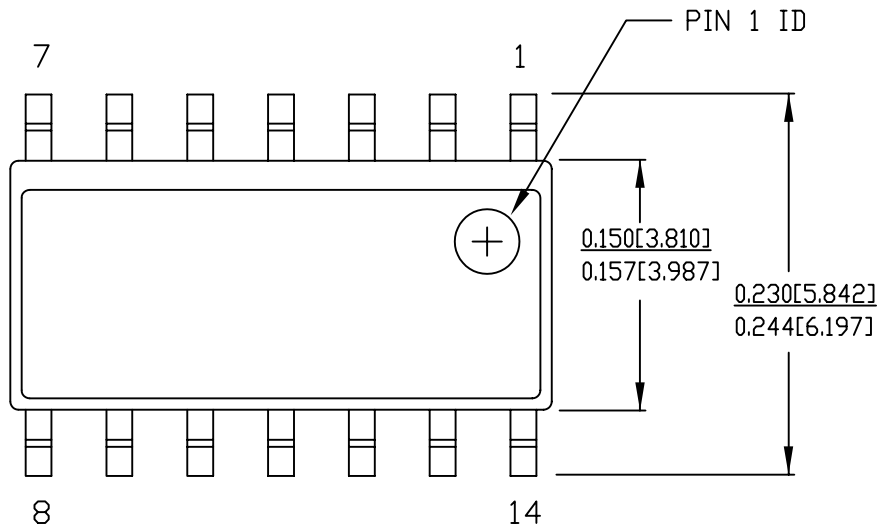
Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

Continuity of ordering part numbers

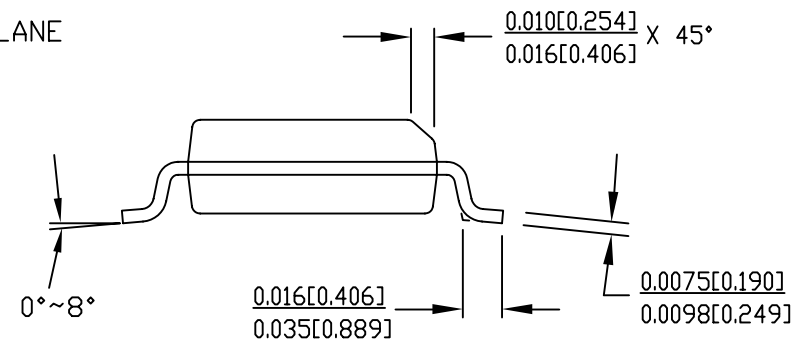
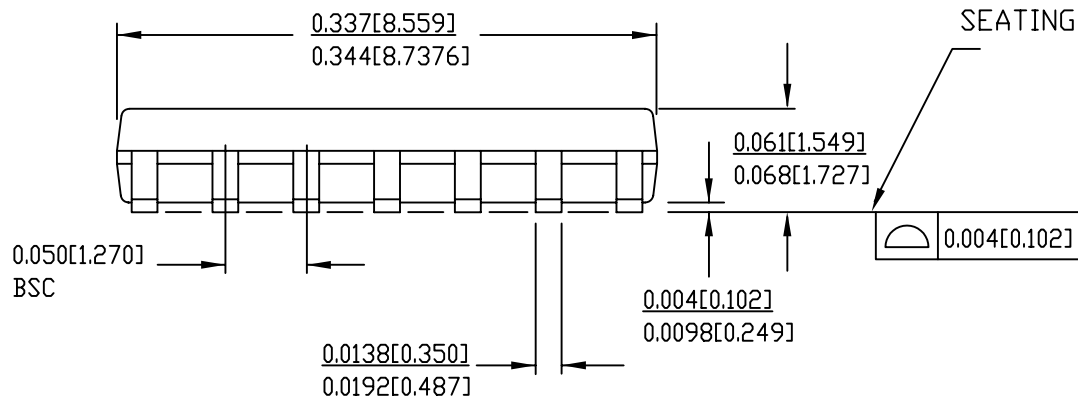
Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.


14 Lead (150 Mil) SOIC



DIMENSIONS IN INCHES[MM] MIN.
MAX.
REFERENCE JEDEC MS-012

PART #	
S14.15	STANDARD PKG.
SZ14.15	LEAD FREE PKG.

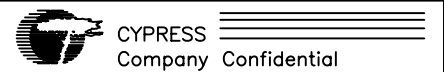


 CYPRESS Company Confidential	
TITLE PACKAGE OUTLINE, 14LD SOIC 150 MILS S14.15/SZ14.15	
SPEC NO. 51-85067	REV *E
SCALE	SHEET 1 OF 2

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PACKAGE CODE(S)

Rev	ECN No.	Orig. of change	Description of Change
**	1128	-	NEW RELEASE
*A	49415	-	CHG. TITLE
*B	128914	-	ADD MM DIM./ADD JEDEC #/ADD SZ PART #
*C	2835671	TZWTMP2	Changed template, & title from 14LD (150 MIL) SOIC PKG OUTLINE to PACKAGE OUTLINE, 14LD SOIC 150 MILS S14.15/SZ14.15.
*D	3252179	QADTMP1	NO CHANGE
*E	4613328	ROWI	SUNSET REVIEW, CHANGED DRAWING TEMPLATE.



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TITLE PACKAGE OUTLINE, 14LD SOIC 150 MILS S14.15/SZ14.15

SPEC NO. 51-85067

REV *E

SCALE

SHEET 2 OF 2

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PACKAGE CODE(S)

	1	2	3	4	5	6	
A							A
B							B
C							C
D							D
	1	2	3	4	5	6	