

16SCYQ060C

PD-93979C

Schottky Rectifier High Efficiency Series Thru-Hole (TO-257AA) 60V, 16A

Features

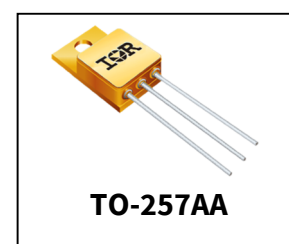
- Hermetically sealed
- Center tap
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Light weight
- ESD rating: Class 3B per MIL-STD-750, Method 1020

Product Summary

- V_{RRM} (per leg): 60V
- $I_{F(AV)}$: 16A
- V_F @ 16Apk, $T_J = 125^\circ\text{C}$ (per leg): 0.79V
- I_{FSM} @ $t_p = 8.3\text{ms}$ half-sine (per leg): 125A
- REF: MIL-PRF-19500/771

Potential Applications

- DC-DC converter
- Protection circuits
- Motor drives



Product Validation

Fully qualified according to MIL-PRF-19500 for space applications

Description

The 1N7059CCT3 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of HiRel environments. It is packaged in the hermetic isolated TO-257AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

Ordering Information

Table 1 Ordering options

Part number	Package	Screening Level
16SCYQ060C	TO-257AA	COTS
16SCYQ060CSCV	TO-257AA	JANTXV-equivalent
16SCYQ060CSCX	TO-257AA	JANTX-equivalent
JANS1N7059CCT3	TO-257AA	JANS

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Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_R	DC reverse voltage (per leg)	60	V
V_{RWM}	Working peak reverse voltage (per leg)	60	V
$I_{F(AV)}$	Max. average forward current (per package) ¹ - Refer to Fig. 5	16	A
I_{FSM}	Max. peak one cycle non-repetitive surge current (per leg) ²	125	A
T_J T_{STG}	Operating Junction and Storage Temperature Range	-65 to 150	°C
	Weight	4.3 (Typical)	g

¹ 50% duty cycle @ $T_c = 128^\circ\text{C}$, square waveform² $t_p = 8.3$ ms half-sine

Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics

Table 3 Electrical Characteristics

Symbol	Parameter	Max.	Unit	Test Conditions	
V _F	Forward Voltage Drop (Per Leg) See Fig. 1 ¹	0.63	V	@ 8.0A	T _J = -55°C
		0.79	V	@ 16A	
		0.59	V	@ 8.0A	T _J = 25°C
		0.81	V	@ 16A	
		0.57	V	@ 8.0A	T _J = 125°C
		0.79	V	@ 16A	
I _R	Reverse Leakage Current (Per Leg) See Fig. 2 ³	0.68	mA	T _J = 25°C	V _R = rated V _R
		150	mA	T _J = 125°C	
C _J	Junction Capacitance (Per Leg)	700	pF	V _R = 5V _{DC} (1MHz, 25°C)	
L _S	Series Inductance (Per Leg)	6.9 (Typical)	nH	Measured from anode lead to cathode lead 6mm (0.25 in.) from package	

2.2 Thermal-Mechanical Specifications

Table 4 Thermal-Mechanical Specifications

Symbol	Parameter	Max.	Unit	Test Conditions
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Per Leg)	2.0	$^\circ\text{C}/\text{W}$	DC operation See Fig. 4
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Per Package)	1.0	$^\circ\text{C}/\text{W}$	DC operation
	Die Size (Typical)	112 x 112	mils	

¹ Pulse Width < 300 μs , Duty Cycle < 2%

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Electrical Characteristics Curves

3 Electrical Characteristics Curves

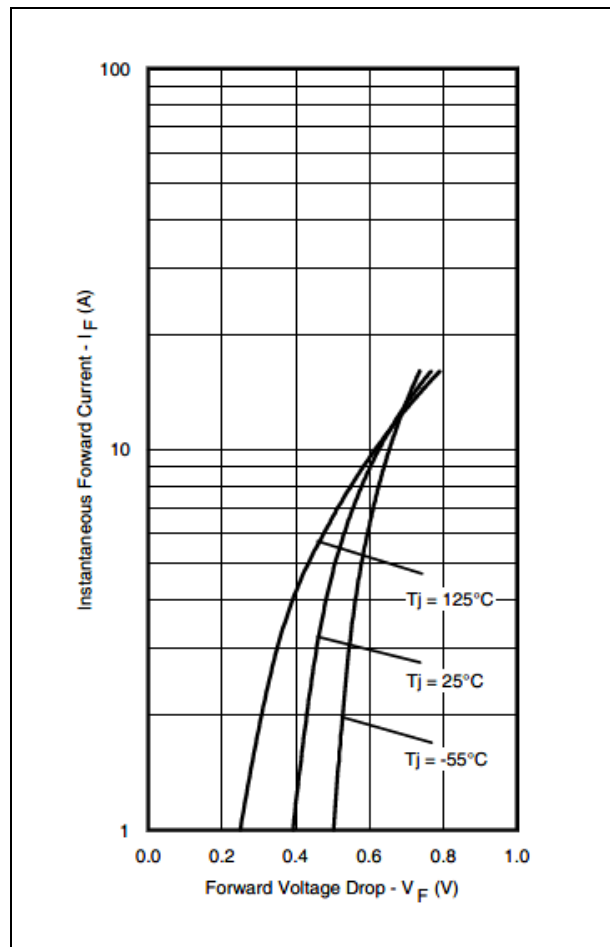


Figure 1 Maximum Forward Voltage Drop Characteristics (Per Leg)

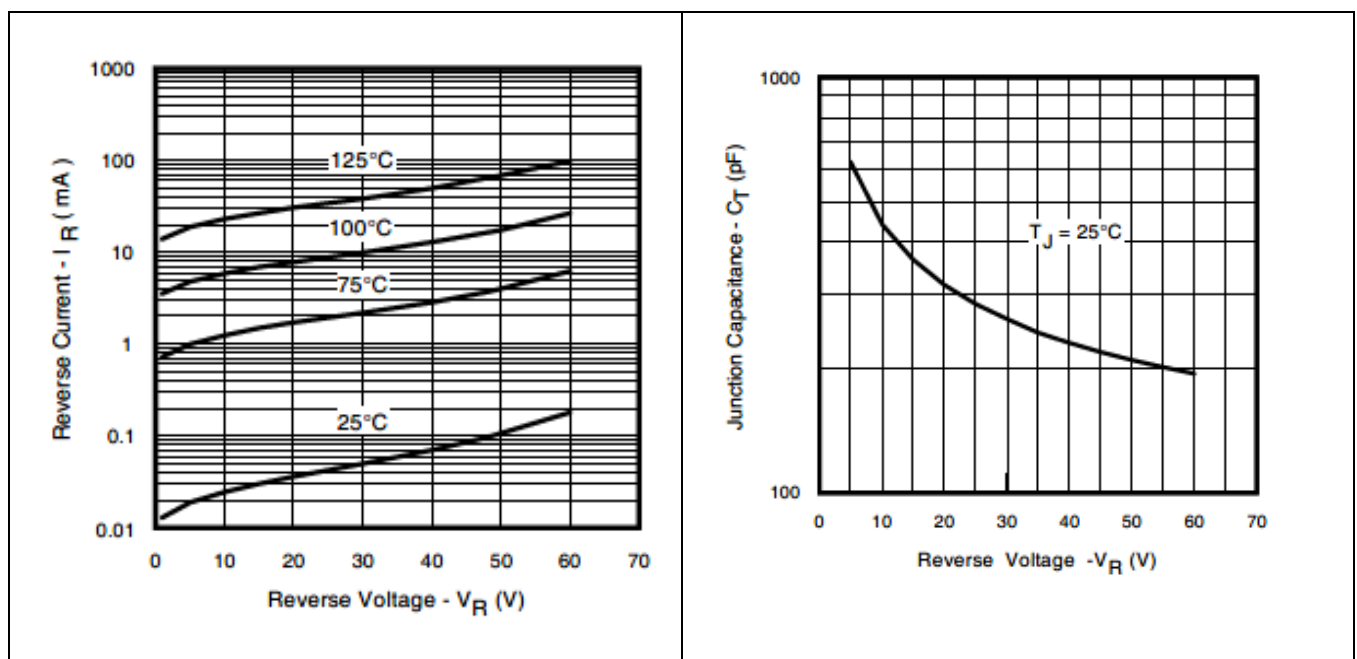


Figure 2 Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

Figure 3 Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

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Electrical Characteristics Curves

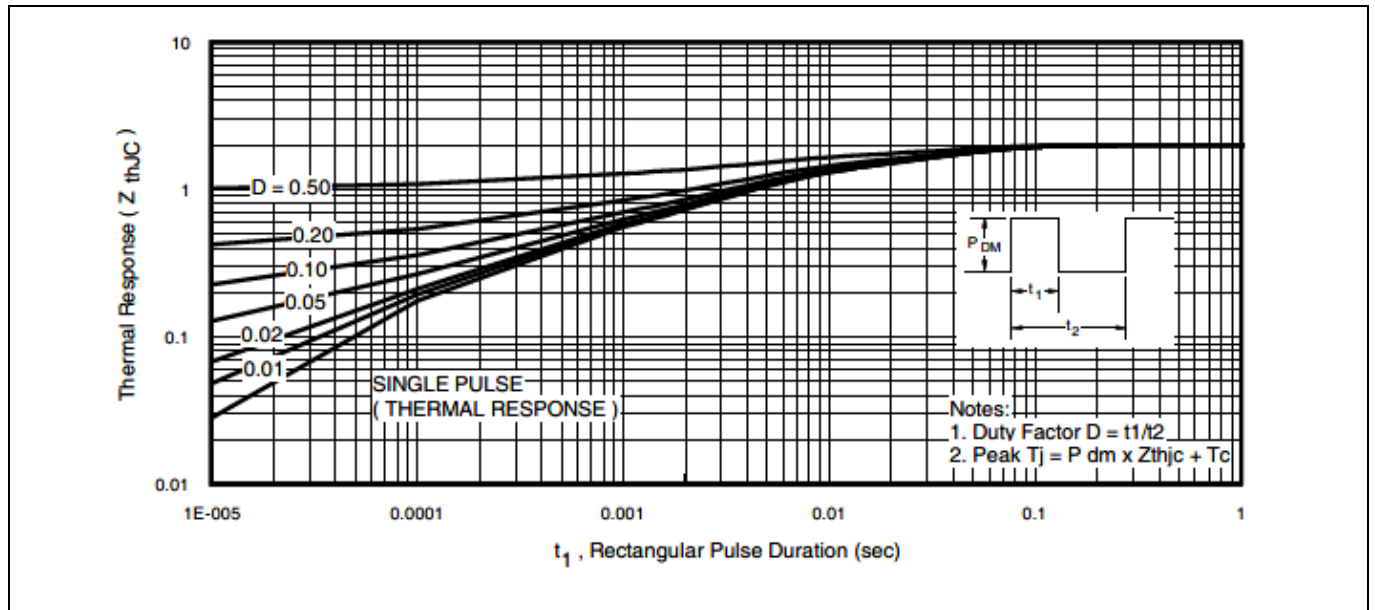


Figure 4 Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

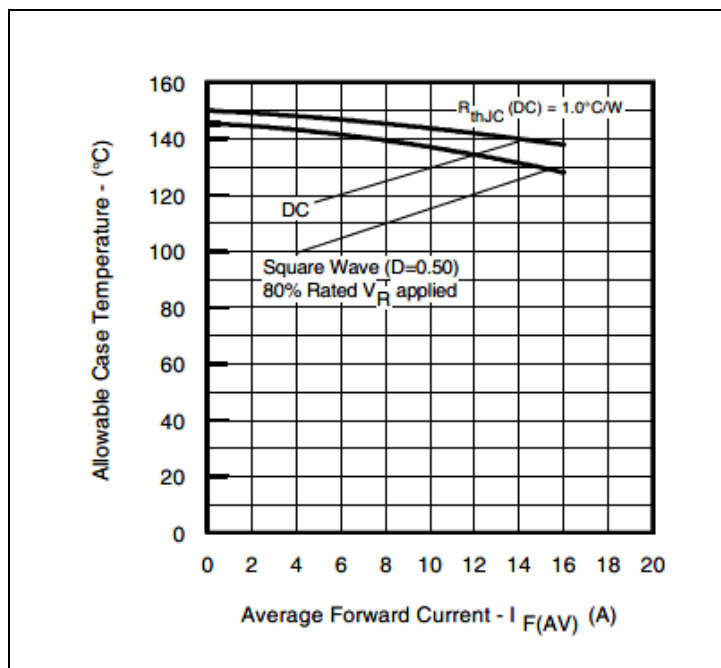


Figure 5 Maximum Allowable Case Temperature Vs. Average Forward Current (Per Package)

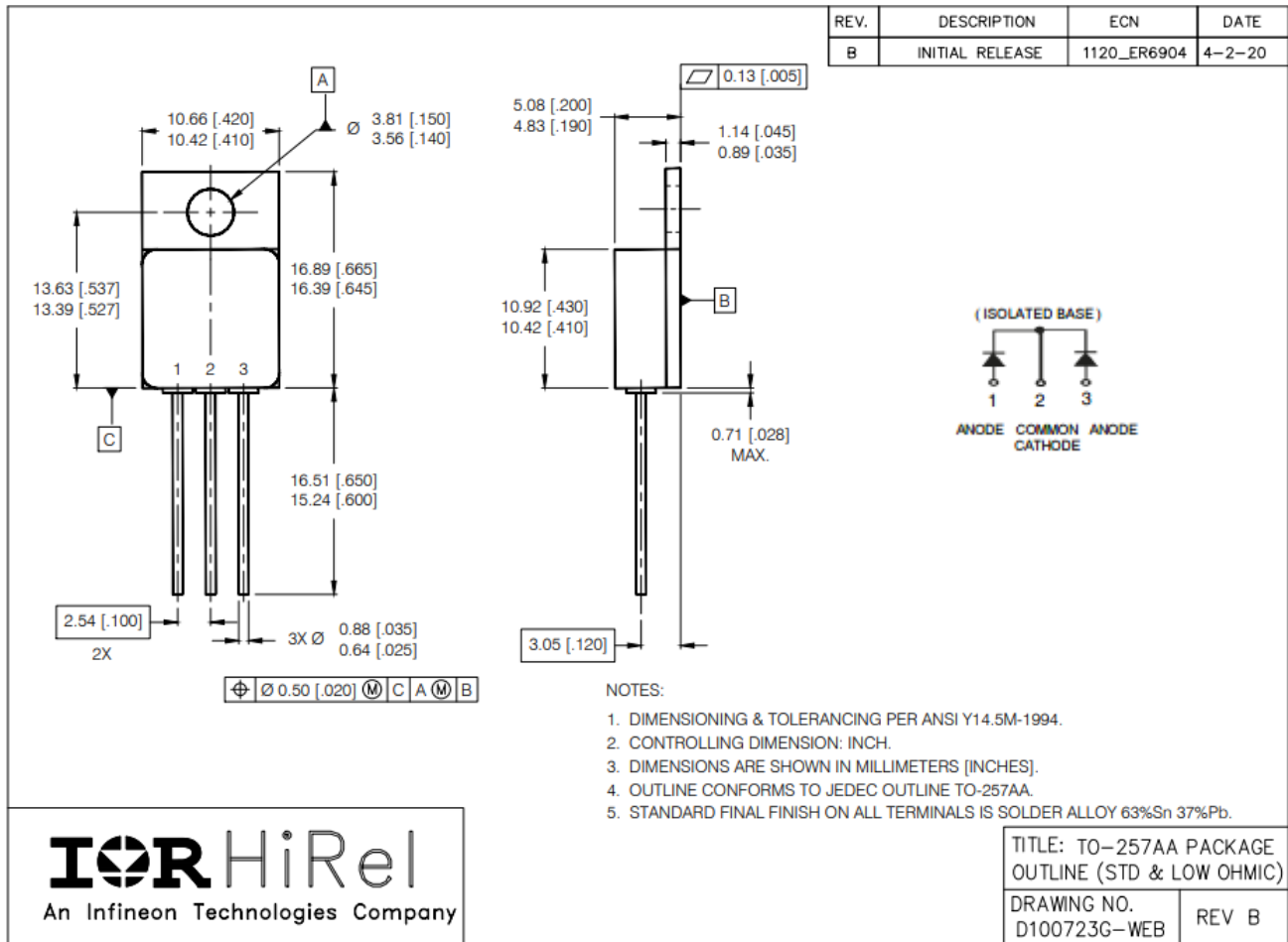
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Package Outline

4 Package Outline

Note: For the most updated package outline, please see the website: TO-257AA



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Revision history

Revision history

Document version	Date of release	Description of changes
	10/16/2000	Final datasheet (PD-93979)
Rev A	01/17/2011	Updated per ECN-18107
Rev B	07/25/2014	Updated per ECN-1120-02429
Rev C	08/03/2022	Updated per ECN-1120-09177

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