

Dr. Susanne Lachenmann

Member of the Infineon Technologies
Supervisory Board since 2015



The person

Date of birth: April 2, 1967
Nationality: German

Education

1995 Dr. rer. nat., Eberhard-Karls-Universität Tübingen
1993 Degree (Diplom) in physics, Eberhard-Karls-Universität Tübingen

Professional career

Since 2008 Lead Development Engineer for Design Enabling and Services,
Infineon Technologies AG
Design methodology, physical verification (Senior Staff Engineer)

2005 – 2008 Development Engineer, Qimonda AG
Design for KERF and test chips

1999 – 2005 Development Engineer for memory products,
Infineon Technologies AG
Various design divisions: KERF and test chips, embedded DRAM;
Design methodology, technology package development

1998 – 1999 Development Engineer for memory products, Siemens AG
Design methodology, technology package development

1995 – 1998 Research assistant at the Institute of Thin Film and Ion Technology,
Research Center Jülich