BGB719N7ESD

Miniature ESD robust Low Noise Amplifier for embedded FM Radio Antennas in Handsets
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1 Introduction

Features

- High performance FM Radio LNA with integrated biasing
- Frequency range: 10 MHz to 1 GHz
- Low external parts count
- Super miniature low profile leadless package TSNP-7-6, 1.26 x 1.4 x 0.37 mm
- High gain at only 2.8 mA current consumption
- Integrated active biasing circuit enables stable operation point against temperature-, supply voltage- and processing-variations
- Integrated ESD protection for all pins (1.5 kV, HBM)
- High input compression point
- High input impedance
- Excellent noise figure from latest SiGe:C technology
- Operation voltage: 1.5 V to 4.0 V
- Power-off function
- Pb-free (RoHS compliant) and halogen-free (WEEE compliant) product

Applications

- Low noise amplifier and active matching for FM reception with small antennas in all kinds of mobile devices such as cell phones, PDAs, portable FM Radio, MP3 players

Figure 1  BGB719N7ESD in TSNP-7-6 Package

1.1 Applications

FM Radio has a long history to its credit starting from its development in 1933. Today, FM radio is an integral part of almost all mobile phones. In a common mobile phone, the headset cable serves as antenna for FM reception, wherein the antenna size (~75 cm) is a bit relaxed. There is a clear market trend to be able to use FM radio also without the headset cable. The antenna needs then to be integrated inside the phone. But in this case, the space constraint poses a challenge on the antenna...
design. Shrinking the size of the antenna introduces a high loss in the system which deteriorates the receiver performance, namely the receiver sensitivity.

Infineon’s latest generation low noise amplifier (LNA) BGB719N7ESD is able to solve this problem by enhancing the receiver sensitivity. Using it in a hand held device also demands low current consumption, power-off function and high linearity due to the co-existence of cellular bands. The LNA is designed for worldwide FM band (76-108 MHz) and high ESD robustness at the RF-in port, which supports outstanding ESD robustness on system level. Infineon offers its LNA solution BGB719N7ESD, which fulfills all these performance criteria in a very small and leadless package TSNP-7-6 (1.26 x 1.4 x 0.375 mm). A further highlight of the BGB719N7ESD is an integrated active biasing which enables consistent operation with varying temperature and process variations. It finds its application in all kinds of mobile devices like mobile phones, PDAs, portable FM radio, MP3 players etc. Putting Infineon’s ESD protection diode ESD0P8RFL in front of the LNA improves the system’s ESD performance up to 8 kV contact discharge (IEC61000-4-2) at RF input. The diode is mounted in the small leadless TSLP-4-7 package (1.2 x 0.8 x 0.39 mm) and has a parasitic capacitance of only 0.8 pF.

With this application proposal Infineon offers a perfect solution for an ESD robust LNA for embedded FM radio antennas in handsets. The design is suited for miniature and slim handset design due to the small form factor of the TSNP packages. The LNA fits easily into a 8mm x 8mm sized area when using 0402 capacitors.

2 Performance Overview

Table 1 gives a quick overview on the performance of the FM Antenna LNA described in this application note. All measurements were performed in a 50Ohm environment.

![Figure 2 FM Radio application schematic](image-url)
### Table 1  Electrical Characteristics (at room temperature)

\( T_A = 25^\circ C, \ V_{CC} = 3.0V, \ V_{PD} = 3.0 \ V, \ ICC_Q = 3.0 \ mA, \ f = 100MHz \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
<th>Comment/Test Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Frequency Range</td>
<td>Freq</td>
<td>100</td>
<td>MHz</td>
<td></td>
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<tr>
<td>DC Voltage</td>
<td>Vcc</td>
<td>3</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>DC Current</td>
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<td>mA</td>
<td></td>
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<td>Gain</td>
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<td>dB</td>
<td></td>
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<td>Noise Figure</td>
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<td></td>
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<tr>
<td>Input Return Loss</td>
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<td>dB</td>
<td></td>
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<td>Output Return Loss</td>
<td>RLout</td>
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<td>dB</td>
<td></td>
</tr>
<tr>
<td>Reverse Isolation</td>
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<td>53</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Input P1dB</td>
<td>IP1dB</td>
<td>-6</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Input IP3</td>
<td>IIP3</td>
<td>-14</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Stability</td>
<td>k</td>
<td>&gt;1</td>
<td>--</td>
<td>Unconditionally stable up to 10 GHz</td>
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</table>

### 3 Application Information

In this section, the application circuit for the BGB719N7ESD is described. The circuit requires minimal usage of external SMD components due to the integration of the biasing circuit which saves PCB space and therefore cost.

The BGB719N7ESD can be easily matched to electrically short half-loop antennas and monopol antennas. Therefore a single passive element needs to be placed at the input of the LNA. In case of a half-loop antenna a shunt capacitance in the range of 35 pF is needed. If the application uses a monopol antenna, a shunt inductance in the range of 375 nH is required.

The application schematic is shown in Figure 3 and the function of each component is explained in Table 2.

**Figure 3  Schematics of the BGB719N7ESD application circuit**
### Table 2  Bill-of-Materials

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Value</th>
<th>Unit</th>
<th>Size</th>
<th>Manufacturer</th>
<th>Comment</th>
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</thead>
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<td>Matching Element</td>
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<td></td>
<td></td>
<td>Various/0402</td>
<td>Shunt C or L depending on the used FM antenna concept</td>
</tr>
<tr>
<td>CIN</td>
<td>330</td>
<td>pF</td>
<td></td>
<td>Various/0402</td>
<td>DC Blocking</td>
</tr>
<tr>
<td>COUT</td>
<td>330</td>
<td>pF</td>
<td></td>
<td>Various/0402</td>
<td>DC Blocking</td>
</tr>
<tr>
<td>CBYP</td>
<td>47</td>
<td>nF</td>
<td></td>
<td>Various/0402</td>
<td>Bypass Capacitor</td>
</tr>
</tbody>
</table>

4  Measured Graphs

Note: All measurements were performed in a 50Ohm environment.

![](image)  Insertion Power Gain InBand
**Figure 5** Input Matching

**Figure 6** Output Matching
**Z Parameters Input output matching**

Figure 7

**Reverse Isolation**

Figure 8
5 Layout

Figure 9 shows the layout and the component placement of the printed circuit board used to assemble and test the LNA.

![Figure 9 - BGB719N7ESD evaluation board layout](image)
Authors

Thomas Schwingshackl, Application Engineer of Business Unit “RF and Protection Devices”
Ralph Kuhn, Senior Staff Application Engineer of Business Unit “RF and Protection Devices”