

# Driving decarbonization and digitalization. Together.



## Staff Engineer Verification

### Job description

Candidate should have working experience with AMS Verification on multiple SOC's or sub-systems. One should have proficiency in AMS simulation environment using Cadence/Synopsys/Mentor tools. Knowledge of digital design techniques, Verilog HDL, and standard RTL coding styles, as well as analog circuit basics, with previous analog design experience a plus. Candidate should be familiar with the concepts of behavioral modeling - both digital (Verilog-D) and analog (Verilog-A or Verilog-AMS). Experience in SV and UVM testbench development/modifications from mixed signal perspective is a plus. Functional knowledge of analog and mixed signal building blocks, such as comparators, op-amps, switched cap circuits, various types of ADCs and DACs, current mirrors, charge pumps, and regulators is expected. Working knowledge of Perl / Skill/ Python/Tcl or other scripting relevant language is a plus. Candidate should have ability to lead a project team, and work collaboratively in a multi-site development environment.

In your new role you will:

- Behavioral modeling: Verilog, Wreal or SV-RNM -Full
- AMS Verification for SoC or IPs -Full
- Test plan preparation as per the dynamics of product specifications - Full
- Dealing challenges with AMS methodologies of Cadence: irun/xrun or Synopsys: XA-VCS or Mentor Eldo ADMS -partial
- Testcase Debug & proposing new scenarios - Partial
- Handling project dynamics on scope, schedule and effort – coming up with alternative verification plans, Mentoring Junior engineer – Partial
- Ability to drive MSV project independently
- Drive enhancements in known methodologies

### Profile

You are best equipped for this task if you have:

- Bachelors with 5+ years or Masters with 4+ years of experience
- Analog: functional spec understanding of standard power management blocks, clock circuits and data converters. Loop analysis is an added advantage
- HDL/HVL: Verilog/Verilog-ams, SV/UVM added advantage
- Tools: Cadence Xcelium + spectre/ Synopsys XA-VCS/ Mentor Eldo ADMS
- Automation: Perl/python/shell

### At a glance

Location:

Job ID: **HRC0852237**

Start date: **as soon as possible**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

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Job ID: **HRC0852237**  
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### Contact

[swati.gupta@infineon.com](mailto:swati.gupta@infineon.com)



- Schedule and result oriented execution mindset, flexible in working as per the project scope needs, Exploring and experimentation for continuous methodology improvements
- Ability to drive projects and debug independently

## Why Us

### **Driving decarbonization and digitalization. Together.**

Infineon designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

### **We are on a journey to create the best Infineon for everyone.**

This means we embrace diversity and inclusion and welcome everyone for who they are. At Infineon, we offer a working environment characterized by trust, openness, respect and tolerance and are committed to give all applicants and employees equal opportunities. We base our recruiting decisions on the applicant's experience and skills.

Please let your recruiter know if they need to pay special attention to something in order to enable your participation in the interview process.

[Click here](#) for more information about Diversity & Inclusion at Infineon.

