



## Senior Engineer Process (Wafer test)

### Job description

In your new role you will:

- Key job role as Unit Process Engineer in Wafer test department.
- Provide process engineering support on IC product testing on daily problematic lot
- Provide engineering support on new system setup involving PCU hardware and networking connection for new tester within wafer test
- Interact with multi sites test development personnel to bring the stable test package release before mass production.
- Optimize and Maximize Test setup flexibility on tester under responsibility.
- Provide process engineering solution to ensure safe and stable ramp up on line quality topic and tester capacity optimization

### Profile

You are best equipped for this task if you have:

- Bachelor Degree or Master in Electrical/ Electronic, Manufacturing engineering, Mechanical engineering or equivalent engineering.
- Minimum five(5) years of working experience.
- Proficiency in typical process tool e.g. FMEA/SPC/DOE/8D
- Knowledge in Semiconductor test and process control methodology
- Knowledge in MicroFlex & V93K test cell and have excellent test (software /hardware/test program) troubleshooting skill.
- Hands on experience in Testcell hardware and network system
- Proficiency in IT network and Testcell hardware setup will be an added advantage

### At a glance

Location: **Kulim**  
Job ID: **316184**  
Start date: **immediately**  
Entry level: **3-5 years**  
Type: **Full time**  
Contract: **Permanent**

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