



## Senior Engineer Mask Design

### Job description

#### Job Description:

- To implement the physical layout of circuit based on schematics provided by circuit design engineers
- Check the correctness of layout using physical verification tools such as DRC and LVS.
- To work with circuit design engineers in the evaluation of various circuit options and their impact on layout, and determine the optimize approach.
- To perform top level routing and connection of input output signals. Giving extra care to signals identified as sensitive to minimize possible coupling and parasitic.
- To perform top level bussing to satisfy current density and minimize IR drop throughout the chip.
- To develop standard physical cell library for new process technology.
- To handle floorplan from scratch for a IP independently (Hierarchical design).
- To involve in simple skill code scripting

### Profile

#### Job Specification:

- Working experience in related field with technical / functional in years: 3-5
- Fluent in language such as English and software skills + level such as Cadence Virtuoso & Unix
- Education background: Degree in Electronic & Electrical Engineering

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### At a glance

Location:

Job ID: **360160**

Start date: **Oct 03, 2022**

Entry level: **3-5 years**

Type: **Full time**

Contract: **Permanent**

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