



Principal Engineer Shift-Left Design Methodologies (f/m/div)*

Job description

Is automatic code generation your passion? Then you are right here! With us you would not only develop design models in UML but also the tools that generate code from them. These are used worldwide by our circuit designers from all departments - from automotive to security - and can significantly increase efficiency. You cover analog and digital equally and are always on the lookout for hidden potential together with the users. With this position, you will enter our "Technical Ladder" career path. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

Infineon developed a strategy and an infrastructure supporting that strategy that significantly boosts design productivity. Infineon searches a senior person that drives this methodology forward – in application and in infrastructure:

- **Develop Infineon's shift left methodologies** like top level connectivity, power and early space exploration topics
- **Design, develop, maintain, enhance the SoC Automation methodology** enabling shift left (before RTL)
- **Define data sources, data flow, develop and constantly optimize the generator** to achieve maximum performance, feature set and maintainability
- **Improve existing software modules** including refactoring and maintenance of existing code
- **Demonstrate the tool performance and provide status tracking** information to the stakeholders
- **Be an accountable partner and collaborate with our worldwide user community** in technical teaching and guidance as well as all business lines expert groups to get requirements and rollout methodologies
- **Lead a team of developers and be responsible for all the technical aspects** of the same
- **Act as a mentor and coach for junior developers** and be responsible to identify and motivate new talents by maintaining university collaborations

Profile

You push ideas to their full implementation and application. Furthermore, you are open minded and like to communicate such as you know how to establish sustainable relationships and networks. Moreover, you cooperate across boundaries and appreciate the contributions of other people.

At a glance

Location:

Job ID: **347183**

Start date: **as soon as possible**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **347183**
www.infineon.com/jobs

Contact

Mona Straßmair
Talent Attraction Manager



You are best equipped for this task if you have:

- A **degree in Electrical Engineering, Information Science** or similar
- Approximately **6+ years of experience in R&D in semiconductor industry**
- Proven **experience in architecting and developing complex scalable software tools** from idea to production with a very good design architecture/pattern and excellent performance
- Several years of **work experience in Electronic Design Automation (EDA) area or software design and implementation**
- Very good **programming skills with focus on Python/C++/C in UNIX environments**; programming experience in Windows environment is a plus
- Outstanding **problem-solving and customer communication skills**
- Knowledge in one or more of the following topics: **Digital Design, Data Structures and Algorithms, Design Patterns**
- **Fluent English skills** with German as a plus

Benefits

- **Munich:**

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high-level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

