



Senior Engineer Mask Design

Job description

Job Description:

- Participate in block/IP floor planning from scratch, performing routing/layout verification
- Conduct thorough layout design reviews internally & external review with circuit designers
- Extensive use of CAD Tools (Cadence virtuoso & Mentor caliber)
- Capable in guiding/coaching junior engineer for on time delivery & quality

Profile

Job Specification:

- Bachelor degree in Electrical/Electronic Engineering/Physics with VLSI exposure of equivalent 4~6 years of job experience in layout design
- Hands on experience in analog layout from scratch
- Deep understanding of analog circuit layout concepts in CMOS technology
- Possess strong technical, analytical & problem solving skills in layout design
- Ability to work as strong team player and participate in cross functional activities
- Good interpersonal, verbal and communication skill with good initiative at work

Why Us

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We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

At a glance

Location:

Job ID: **340434**

Start date: **as soon as possible**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

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