



Senior Staff AMS Design Support Application Engineer

Job description

You will be part of the Global Design Applications Support Group focusing on using your AMS Design Expertise, EDA tools and layout knowledge, Design Environment and flows, and methodologies skills to support the IoT design team in Colorado Springs in designing Analog/Mixed Signals Integrated Circuits. You will be using the various EDA IC design tools from Cadence, Mentor, and Synopsys to assist in troubleshooting design issues and ensuring successful tape-outs. The goal is to enable the design teams to improve efficiency and productivity and reduce design cycle time

In your new role, you will:

- Support Infineon's methodologies & flow within the **EDA design platform for Analog and Mixed-Signal IC Design**
- Focus on enabling a high-performance AMS design environment
- Contribute to the Improvement of the AMS Implementation methodology
- Support the AMS design team with design environment setup and product database management
- Strong knowledge in the **AMS IC domain: AMS Schematic generation and simulation, Physical Layout, and verification** utilizing **Cadence Virtuoso** and Mentor Caliber tools.
- Interfacing with EDA vendors to resolve tool issues related to design challenges
- Strong experience in Data Management Software such as Cliosoft, Clearcase, etc.
- Strong collaboration with **AMS IC Design teams**
- Extensive experience in **Cadence CAD tool usage for AMS design.**

#LI-MH1

Profile

You are best equipped for this task if you have:

- At least 5+ years of relevant experience
- BSEE minimum requirement, MSEE preferred
- Knowledge of Industry **EDA tools and semiconductor design techniques**
- Strong problem-solving skills with the excellent debugging ability

At a glance

Location:

Job ID: **334364**

Start date: **as soon as possible**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

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Job ID: **334364**
www.infineon.com/jobs



- Excellent interpersonal skills and the ability to operate in both centralized and distributed team environment
- Must have strong teamwork, communication skills, and work ethics with the ability to address changes and issues with the design teams in a consistent and efficient manner.
- Work with the **Design Teams to ensure integrity, completeness, and quality of solution provided.**
- Must be able to understand user requirement and translate it into comprehensive and valuable solutions.

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your on-boarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE:

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

