

# Driving decarbonization and digitalization. Together.



## Senior Staff Engineer Physical SoC Implementation (f/m/div)

### Job description

Infineon is one of the major global companies for semiconductors developing products that are essential to shape our future mobility. High performance Physical Design using newest automotive semiconductor technologies is key to enable these products. Do you want to be part of this challenge? Then join our team as Senior Staff Engineer Physical SoC Implementation. Bring in your experience and work with a team of experts across Infineon's sites and departments. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Take over the technical leadership for the physical design** of highly complex digital designs
- **Translate product requirements into physical implementations** using newest automotive semiconductor technologies
- **Work independently on different phases of the RTL2GDS workflow** like Synthesis, Floorplans, Clock Tree Design, Routing, Static Timing Analysis and efficient Sign off using state-of-the-art EDA
- **Master the challenges of hierarchical deep sub-micron designs**
- **Analyze technical problems and drive issue resolution** with various colleagues from other departments
- **Create an expert network and drive innovation**, methodology and **continuous improvement** of our workflow

### Profile

You live up to high quality standards, strive for continuous improvement and you develop and implement effective solutions. As a true team player, you cooperate across boundaries and contribute with your hands-on and driver mentality. You focus on and listen to the stakeholders and consider their position to balance the requirements and interests. Finally, you are able to handle abstract matters and communicate complex topics in a condensed form.

You are best equipped for this task if you have:

- A degree in **Electrical Engineering, Microelectronics**, Physics, or a similar field
- **Relevant experience in Physical Design** of highly complex SoCs - with experience in **Synthesis, Place & Route and Timing Closure**

### At a glance

Location: **Duisburg (Germany)**  
Job ID: **HRC0481047**  
Start date: **as soon as possible**  
Entry level: **1-3 years**  
Type: **Full time**  
Contract: **Permanent**

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Job ID: **HRC0481047**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)



### Contact

**Verena Schaefer**  
Recruiter



- **Insights into technical leadership** in physical design projects
- **Programming skills and knowledge in scripting languages** like Tcl, Perl or Python
- **Technical understanding in all aspects of RTL2GDS**
- Basic experience in **RTL coding**
- **Team oriented** and structured working style
- **Fluent English language skills** – with German being an added plus

## Why Us

### **Driving decarbonization and digitalization. Together.**

Infineon designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

### **– At EPOS we develop highly complex embedded microchips for different applications –**

We are a small Infineon company located in Duisburg that shapes the future of all. We are developers in leading edge technologies for Advanced Driver Assistance Systems, BT applications and we provide services for RTL2GDS in state-of-the-art CMOS technologies.

The central R&D organization „**Design Enabling and Services**“ (DES) the design environments (design-systems) to the different product development teams at Infineon. With leading edge design methods, complex building blocks and a wide range of product development services DES enables Infineon's advanced IC development. This means the complete value creation chain from abstract system models down to the fully verified product layout which ensures high quality manufacturing readiness.

### **We are on a journey to create the best Infineon for everyone.**

This means we embrace diversity and inclusion and welcome everyone for who they are. At Infineon, we offer a working environment characterized by trust, openness, respect and tolerance and are committed to give all applicants and employees equal opportunities. We base our recruiting decisions on the applicant´s experience and skills.

We look forward to receiving your resume, even if you do not entirely meet all the requirements of the job posting.

Please let your recruiter know if they need to pay special attention to something in order to enable your participation in the interview process.

[Click here](#) for more information about Diversity & Inclusion at Infineon.

