Driving decarbonization and digitalization. Together.

Internship: Lab Verification Engineer in the AMS Lab of the Development Center Villach (f/m/div)*

Job description

Are you a motivated student, studying in the field of Electronics Engineering, who is looking for an internship to complement the acquired theoretical knowledge with practical experience? We are looking for someone just like you!

In a world that becomes more and more digitized, the interfaces from the analog to the digital domain are the key enabler. Be part of the biggest Analog-Mixed-Signal (AMS) verification laboratory team in Infineon that is taking care about analog and digital measurements. There is a broad bandwidth of devices that are verified in our labs:

- Analog blocks in Infineon microcontrollers
- Analog to Digital (ADC) and Digital to Analog (DAC) converters
- High speed interfaces
- High Precision Oscillators and PLLs
- Power Management Systems (PMS)
- Battery Management Systems (BMS)
- DCDC Converter for low voltage applications and Linear Voltage Regulators (LDOs)
- RF blocks for radar systems
- Bias & Control circuits for 5G antennas

Take the chance to expand your theoretical knowledge from the university and enrich it with practical / hands-on experience in the areas of:

- **Semiconductor integrated circuits**
- Usage of the **latest measurement equipment** in the AMS area
- **Board assembly, rework** and **soldering**
- **How to debug** and find the root cause for unexpected behavior
- **Influence** of **parasitics** to the performance of electronic circuits
- **AMS measurement techniques**
- **Big data handling** and **data post-processing**
- Automation of lab **test setups**
- **Preparation** and **presentation** of measurement results
- **High quality documentation** of measurement results according to IATF16949 and ISO26262

At a glance

Location:	
Job ID:	**HRC0163394**
Start date:	**as soon as possible**
Entry level:	**0-1 year**
Type:	**Full time / Part time**
Contract:	**Temporary**

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: HRC0163394
www.infineon.com/jobs

Contact

Nico Steinhauser
Recruiter

In addition to the technical know-how, you will be part of a youthful and active international team where more than 100 students have had internships to date, many of which are now employed engineers in the lab and around Infineon worldwide.

Seems interesting? We ensure it is! Use the chance and submit your application to become a member of our lab community!

Further information:
Type of employment: Temporary / part-time (Flexible working hours from Monday to Friday between 6 a.m. and 7 p.m.)
Duration: min. 6 months

Profile

As our ideal candidate, you offer additonally:

- Studying **Electronic Engineering**, **Microelectronics** or similar
- Knowledge in using **lab equipment**
- Knowledge in using **programming languages** (like C/C#/C++, ...)
- Good **English skills**
- Willingness to do an internship in duration of minimum **6 months**, preferably **1 year** (or more)

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry (full-time), employment group B for bachelor students, employment group D for master students (https://www.feei.at/wp-content/uploads/2022/05/minimum-salaries-white-collar-workers-2023.pdf).

Please attach the following documents (German or English) to your application:

- Motivation letter
- CV
- Certificate of matriculation at a university (you must be enrolled and not on academic leave)
- Latest Transcript of records (not older than 6 months)
- Highest completed educational certificate (Matura certificate for Bachelor students, Bachelor certificate for Master students)
- Reference letter (optional)

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization **„Design Enabling and Services“ (DES)** provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

Infineon Hub - Connect. Create. Challenge.

The iHub at TU Wien represents an inspiring tech platform, networking area and event location, connecting Infineon Austria with tech experts, science specialists and young professionals.

Check out our upcoming events:
Infineon iHub