



## Prin Elect Design Engr

### Job description

12+ Years hands on experience in relevant domain. Very good knowledge of Verilog /System Verilog and UVM.

- Candidate will be leading/involved in definition, verification architecture and development of constraint-random verification environment from scratch at both sub-system and standalone IP level. Candidate will develop expertise in multiple areas of verification – RTL, Gate level and Power aware simulations and drive verification closure on complex modules and SoCs using SV/UVM based methodology.
- Must have:
  - Good understanding of ASIC logic verification concepts and techniques.
  - Ability to develop verification architecture documents for complex modules/systems.
  - Ability to architect complex module / sub system / Full chip test benches and build verification infrastructure.

### Profile

- Should be a good mentor and guide for junior engineers in the team.
- Candidate should have worked on ARM micro controller based Single core/Multi core CPU Subsystems or associated component IP.
- Prior hands on experience on IPs with ARM CM0/CM0\_/CM3/CM4/CM7/CM33 /CM55, Socrates/NIC, Debug & Test Architectures is preferred.
- Exposure to deployment of automation and formal verification methodologies is a plus.

### Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns;

### At a glance

Location: **Bangalore (India)**  
Job ID: **CY23781**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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### Contact

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Crèche facility; Annual success bonus; Medclaim (dependents & top up),  
Personal accident, Term life; National Pension Scheme; Health promotion  
programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick  
leave, long term illness leave; On site Yoga classes, Sports club

## Why Us

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

