



Mixed-Signal Verification Engineer (f/m/div)*

Job description

Do you have experience in Mixed-Signal Verification or want to grow in this field? Would you enjoy working on Power Management Integrated Circuits? In our experienced and dynamic team, you will have the opportunity to develop next generation's automotive products. Come and join us in Padova where you will find a challenging and international environment, as well as the right mentorship to develop your skills further!

As a Verification Engineer you will work closely with the design and concept team to ensure that exhaustive verification design and verification goals are achieved.

In your new role you will:

- **Perform top-level verification** of mixed-signal devices;
- **Work close with design and concept** engineers in order **to achieve very high-quality deliveries**;
- **Constantly align with digital and post-silicon counterparts** to achieve the highest synergies in targets, implementation, and methods;
- **Create specifications and documentation**;
- **Plan & track tasks**, including effort estimation.

Please note that we offer a permanent external contract if all requirements are met. Otherwise, we offer a temporary external contract.

Profile

With your curious and enthusiastic approach and your cooperative way of thinking, being part of a team makes you feel at home. You have exceptional problem-solving and analytical skills and you always look at the big picture. Moreover, you deal with your daily work in a meticulous and organized way.

You are best equipped for this role if you have:

- A **degree in Electronic/Automation Engineering, Mathematics, Physics**, or equivalent field of studies;
- **Some experience in mixed-signal verification** (up to 3 years) - experience with UVM methodology is a plus;
- **Good knowledge of microelectronic devices**, common architectures, and related analog/mixed-signal circuit design;

At a glance

Location: **Padua (Padova) (Italy)**
Job ID: **69390**
Start date: **as soon as possible**
Entry level: **0-1 year**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **69390**
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Contact

Ana Rita Costa
Talent Attraction Manager



- **Basic knowledge of hardware description language** (Spice), with VHDL, Verilog, and System Verilog as a plus;
- **Experience with common simulation tools** (by Cadence, Mentor, and Synopsys), and **good knowledge of Unix OS and scripting languages** (Perl, Python);
- **Proactivity and focus on results**, while keeping a team player mentality;
- **Fluency in English** - Italian and/or German is an advantage.

Benefits

- **Padua (Padova):** Coaching, mentoring networking possibilities ; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible Working Time; Part-time work possible; Medical coverage; On-site canteen; Wage payment in case of sick leave; Corporate pension benefits; Performance bonus; Accessibility, access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

– Automotive (ATV) shapes the future of mobility with micro-electronics enabling clean, safe and smart cars –

Semiconductors are essential to realize key trends like eMobility, automated driving and secure, connected cars. Infineon **ATV** is the #1 semiconductor partner in the fast-changing automotive world, based on our system knowledge coupled with our passion for innovation and quality. We are a key driver in the ever-advancing pace of digitalization in the automotive industry.

[Click here](#) for more information about working at ATV with interesting employee and management insights and an overview with more #ATVDreamJobs.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

