



## Digital Layout Engineer (f/m/div)\*

### Job description

People with creative and innovative minds, who love being challenged every day, with a positive, problem-solving attitude are exactly what we need in Bucharest! We're looking for a new Digital Layout Engineer to surprise us with fresh and unique ideas.

There are lots of new techniques and methods you will learn and apply. A friendly and supporting team will help in whatever you need!

If all this already put a smile in your face, just take a look in what you will do:

- Be in charge of the **area estimation, floor planning, power routing, CTS, timing-driven, shielding, optimization, timing ECOs, physical verification** (DRC, ERC, LVS, ESD, etc.), as well power analysis (EM / IR-Drop) and tape-out layout sign-off.
- **Develop scripts and methods** for design automation and cycle time reduction.
- Interact with **synthesis, STA, and top-level responsible** people within the product development team, as well as with the local project managers.
- Be in charge of **optimization of chip and module areas**.
- Coordinate **layout deliveries** within the project (block-level or digital on top).

### Profile

You don't need to fly, pass across walls, or breathe underwater, we accept every other superpower like a structured, thorough, and results-oriented work style, team spirit, incredible communication skills, and, of course, a hands-on attitude.

Besides those, is also important:

- Degree in **Electrical Engineering, Computer Science** or **Automation**
- At least **3 years** of experience in a similar position
- Knowledge of fundamental **semiconductor principles and operation**
- Knowledge of **timing principles and their application** in semiconductor designs.
- Experience with **SoC practices** (for example, multiple voltages and clock domains, integration of mixed-signal IPs, and I/O integration)
- Knowledge of DfT/DfM (Design for Testability/Design for Manufacturing) principles and **Synthesis and STA** is a plus.
- Good **English** language knowledge (both written and spoken).

### Benefits

### At a glance

Location:	<b>Bucharest (Romania)</b>
Job ID:	<b>66424</b>
Start date:	<b>as soon as possible</b>
Entry level:	<b>3-5 years</b>
Type:	<b>Full time</b>
Contract:	<b>Temporary</b>

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **66424**  
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### Contact

**Guilherme Lima**  
Talent Attraction Manager



- **Bucharest:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working hours at many sites; Home office options; Medical coverage; Health promotion programs; On-site gym with special rates; On-site canteens; Wage payment in case of sick leave based on applicable law; Corporate pension benefits for engineers; Performance bonus options; Accessibility, access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

