



Senior Principal Architect

Job description

Join Infineon's System Resource Center of Excellence design team.

In your new role you will:

Join Infineon's System Resource Center of Excellence design team. Define and develop mission-critical IP for our **System-On-Chip IoT & Automotive products!**

Looking for a **Digital & Mixed-signal IC** industry expert to architect and lead the development of System Resource IP used in all our SOCs. This team is in charge of developing the Power Systems, Clocks, Reset, and Test IP. Experience with the **IoT or Automotive system** space is a major benefit.

Profile

You are best equipped for this task if you will:

- Work with the CTO to define platform, **SOC, and IP architecture**
- Work with product management to develop requirements and roadmaps
- Work with analog **IP teams to develop** world-class performance and customer value
- Drive continuous improvement of design flows and work methods
- Work closely with the software team to guarantee consistent and effective hardware/software interface
- Be expected to be a subject matter expert in logic and mixed-signal design. Proven track record leading cross-functional teams driving system/feature /hardware/software trade-offs. They will be expected to lead process improvement activities at the subject level and business process level, and have proven ability to develop and mentor engineers. Must have strong skills with **RTL, simulation, and verification**. Excellent communication, documentation, and leadership skills are mandatory.

At a glance

Location: **San Jose, CA (USA)**

Job ID: **57976**

Start date: **immediately**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

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