



Process Development Engineer SiC Epitaxy (f/m /div)*

Job description

You have experience (for instance a PhD) in the field of SiC epitaxial growth? Then this job is a great opportunity for you. Become part of a successful team on the leading edge of industrial technology!

In your new role you will:

- Be responsible for the **SiC epitaxy process development and characterization** with application specific optimization
- Work in close collaboration with the **device development team**
- Handover **qualified processes** to production

Profile

You are best equipped for this task if you have:

- A university / university of applied sciences degree (Master or PhD) in **Electrical Engineering, Physics or Material Science** with focus on Solid State Physics or Semiconductor Physics
- **4+ years work experience** in the field of **SiC epitaxial growth**
- Fluent **English** skills with **German** skills as a plus

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry, employment group G (<https://www.feei.at/leistungen/informations-service/mindestlohne-und-gehalter-2019>). The monthly salary is paid 14 times p.a. A higher payment is negotiable depending on your expertise and skills. Furthermore, Infineon offers additional employee benefits.

At a glance

Location:	Villach
Job ID:	39264
Start date:	Nov 01, 2019
Entry level:	Fresh Graduate (without work experience)
Type:	Full time
Contract:	Permanent

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **39264**
www.infineon.com/jobs

Contact

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