



Student for IC Digital Design & Verification (f/m/div)*

Job description

Are you studying at university and want to apply your knowledge into practice? Would you like to start a challenging career while sharing your ideas with some of the best professionals of the field? Apply now and experience the daily business of an international company that is looking for students with great passion and first class skills.

In your new role, you will be able to:

- **Specify, design, implement, simulate and verify digital blocks;**
- **Perform RTL design and synthesis**, gate level simulation, power and static timing analysis;
- **Develop block-level functional test-benches** to check proper behavior of circuits;
- **Create verification plans and test suites** based on design specification;
- **Build verification environments** consisting in test-benches for top/block level;
- Make **regression simulation and reporting** using dedicated software tools and analysis of coverage and performance.

Profile

You are best equipped for this position if you had **lectures or lab courses** on:

- Electronic Devices and Circuits;
- Electronic Circuits Fundamentals;
- Digital Integrated Circuits;
- Microprocessors Architectures;
- FPGA implementation;
- Object oriented programing.

In addition, you should have **knowledge/experience** with the following **simulation tools**:

- Modelsim, Ncsim or similar digital simulators for design;
- Specman, Vmanager or similar for verification.

Also, you should have **programming skills** in:

- VHDL, Verilog and C/C++;
- Python, Perl or TCL for scripting;
- SystemVerilog or E for verification.

Please attach your **CV in English** so we can get to know you better.

At a glance

Job ID: **36983**
Start date: **immediately**
Entry level: **Internship**
Type: **Full time**
Contract: **Temporary**

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Contact

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