



## Staff CAD Engineer Chip-Package-Board CoDesign Methodology (f/m/div)\*

### Job description

Are you a strategic thinker that is committed and motivated to learn new languages and develop new skills? At Infineon Villach, we are looking for a passionate CAD Engineer to grow our team responsible for the chip, package and board design and verification software ecosystem.

In your new role you will:

- **Integrate new methods** and **software tooling** into our established **chip, package and board design flow**
- **Contribute to the development** of our **in-house software add-ons** for **EDA frameworks of different vendors** (like from Cadence, Mentor, Ansys, Keysight, )
- Further **improve existing software modules** (including the testing, refactoring, and maintenance of existing code)
- **Write technical documentation** and strive for **continuous quality improvements measures** in accordance to all relevant **quality assurance processes**
- **Conduct evaluations of existing tools & flow** and propose **improvement measures**
- **Train and support** our worldwide **hardware designer community**
- Build up **high-quality, innovative and performant solutions**
- Be the **1st level user support for in-house software solutions**

### Profile

You are best equipped for this task if you have:

- **A degree in Computer Science, Electrical Engineering** or related areas
- At least **1-3 years of professional work experience** in **software development and implementation within an EDA environment** or a **related technical area**
- Good **programming skills** with focus on **object-oriented software development** (e.g. Python, Java, C++) in **Unix environments** and **familiarity with working and scripting on Linux**;
- **Knowledge of the Cadence design environment** is a plus
- **Problem-solving skills** and a **keen eye for the details**
- Enjoy to **work in a team** and **improve the functionality and usability of our software applications**
- **Fluent English skills**, German is an advantage

### At a glance

Location: **Villach (Austria)**  
Job ID: **361622**  
Start date: **Oct 01, 2022**  
Entry level: **1-3 years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **361622**  
[www.infineon.com/jobs](https://www.infineon.com/jobs)

### Contact

Christine Felsberger



We offer competitive salaries and additional benefits based on your performance, experience and qualification. The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group F-G (<https://www.feei.at/wp-content/uploads/2022/05/minimum-salaries-white-collar-workers-2022.pdf>). The monthly salary is paid 14 times p.a. We offer a higher compensation depending on your expertise and skills.

## Benefits

- **Villach:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; Child care in Villach & Klagenfurt; On-site social counselling and works doctor; Health promotion programs; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

## Why Us

### Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

The City of **Villach** is located in the center of Carinthia, Austria's southernmost province, in close proximity to the Italian and Slovenian border. Due to its particular geographic location and the outstanding natural beauty of the region, Villach and the whole province of Carinthia have for generations been popular holiday destinations for people from all over the world. Its unique landscape combines mountains and valleys, lakes and rivers, city and countryside. In addition to the favorable geographic location, living in Austria also has many social, health-care-related and economic perks. The country's social and health care system is among the best in the world and for decades numerous international surveys have singled out Austria as a particularly safe and wealthy country with a high quality of life. Villach benefits from its status as a “small town,” offering everyday living at affordable prices in an outstanding setting. So it's no surprise that the city on the Drau is considered the “home of work-life balance”.

### Find out what you like most about Villach and join us:

<https://www.welcome2villach.at/>

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

