



Principal SoC Integration Engineer

Job description

The successful candidate will be part of a chip/SoC integration design team working in the Infineon IoT, Compute, and Wireless organization. As part of the team, you will develop the next generation of industry leading, low-power, best-in-class silicon products for the IoT, consumer, and industrial markets

In your new role you will:

- As a member of our SoC Integration team, you will need **(and develop) a breadth of knowledge**
- **Work with many teams (architecture, verification, backend, DFT, etc)** to ensure the SoC meets all requirements
- Be responsible for the development of **SoC-level RTL, including the integration of large numbers of complex IPs into a fully functional SoC, and working with the SoC architecture team** to define the integration
- Be responsible for “ **frontend**” design tasks and quality checks such a **Linting, Clock Domain Crossing, and Reset Domain Crossing**
- Be responsible for running **logic synthesis, logic equivalency checking, and supporting static timing analysis (STA)**
- Also have the options of **supporting paring/pinout/package design development and analog integration**

#LI-MH1

Profile

You are best equipped for this task if you have:

- **5 years or more of digital design or SoC integration** experience.
- Knowledge in **industry standard RTL design practices and simulation** required
- Knowledge of best practices with respect to **implementation of digital logic**.
- **Linting, Clock Domain Crossing, and Reset Domain Crossing** using tools such as Spyglass required
- Knowledge and experience in **logic equivalency tools strongly preferred**
- Knowledge and experience in an **industry synthesis tool strongly preferred**.
- **Basic STA (Static Timing Analysis) capabilities** preferred and ability to understand and debug STA reports expected
- **Strong scripting capabilities in Perl, Python, tcl, and/or shell scripting** strongly preferred.

At a glance

Location: **Austin, TX (United States)**
Job ID: **361609**
Start date: **Sep 05, 2022**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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Job ID: **361609**
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- **Experience with UPF and low power design techniques**, including low power verification tools such as CLP.
- Understanding of **common industry interfaces** such as AHB, AXI, and APB desired
- Understanding of **Design Verification** and ability to understand and debug RTL simulations expected
- Experience with **using version control EDA tools for tracking releases of RTL** and other IP/SoC collateral
- **Basic knowledge of ASIC test methodology basics** such as scan insertion and memory BIST a plus.
- **Passion for developing new and innovative ideas and methodologies**, and be able to promote them within the organization
- **Strong communication skills and willingness to work with multinational team** in multiple time zones
- **B.S. or M.S in Electrical/Computer Engineering**

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your onboarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE:

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

