



## Senior Engineer Mask Design

### Job description

#### Job Description:

- Participate in block/IP floor planning from scratch ,performing routing/layout verification
- Conduct thorough layout design reviews internally & external review with circuit designers
- Extensive use of CAD Tools (Cadence virtuoso & Mentor caliber)
- Capable in guiding/coaching junior engineer for on time delivery & quality
- Able to handle fullchip analog routes/PV using the innovus and calibre tool

### Profile

#### Job Specification:

- Bachelor degree in Electrical/Electronic Engineering/Physics with VLSI exposure of equivalent 4~6 years of job experience in layout design
- Hands on experience in analog layout from scratch
- Deep understanding of analog circuit ;layout concepts in CMOS technology
- Possess strong technical, analytical & problem solving skills in layout design
- Ability to work as strong team player and participate in cross functional activities
- Good interpersonal, verbal and communication skill with good initiative at work

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### At a glance

Location: **Penang (Malaysia)**  
Job ID: **360738**  
Start date: **Sep 19, 2022**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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