



Staff Engineer System Engineering Methodology (f/m/div)*

Job description

Are you looking for an expert position, where you can grow deeper into system engineering and at the same time expand your programming skills (Python, C++)? Are you the one who keeps the bigger picture in mind and likes to support others with technical knowledge? As Staff Engineer for System Engineering Methodologies you will join our System Engineering Methodology Team, which is globally harmonizing, optimizing, and automating Infineon's System Engineering methods. You will work in an international und multicultural environment and get deep insights into our organization. Does this sound like the perfect challenge for you? Then take this chance and send us your resume! With this position, you will enter our "Technical Ladder" career path. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- Be a **technical lead for modeling technologies for embedded systems** at higher abstractions
- Explore **methods of modelling embedded systems** at higher abstractions
- Lead in **virtual prototype development projects** which entails modelling of IP blocks and debugging embedded software within virtual environments
- **Define appropriate automations for the modeling methods** you come up with to ensure a broad acceptance in our engineering community
- Be responsible to **ideate and execute projects** which focus on improving modelling methodologies
- Work in a **multi-cultural** and **international** team

Profile

You push ideas to their full implementation and application. Furthermore, you are open minded and like to communicate such as you know how to establish sustainable relationships and networks. Moreover, you cooperate across boundaries and appreciate the contributions of other people

You are best equipped for this task if you have:

- A degree in **Electronical Engineering, Computer Science** or a similar qualification
- At least **3-5 years professional experience in modeling techniques**, such as SystemC TLM, QEMU, SysML, Python

At a glance

Location: **Munich (Germany)**
Job ID: **360063**
Start date: **Sep 01, 2022**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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Contact

Mona Straßmair
Talent Attraction Manager



- Good understanding of **HW/SW architectures in embedded systems**
- A fluency in **object-oriented programming**
- Good experience in using **HDL simulators**
- An **open mind** to communicate with and convince stakeholders
- Fluent **English** communication skills; German is a plus

Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (**DES**) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

