



Senior Design Application Engineer for Mixed-Signal IC development (f/m/div)*

Job description

Are you looking for a new challenge? Are you used to work proactively and interdisciplinary? Are you ready to bring your know-how into an innovative, future-oriented company? Then Infineon is the place to be for you!

In your new role you will:

- **Support R&D chip designers for daily issues** related to mixed-signal IC development flow and related software tools and methodology (1st level support)
- **Work closely with mixed-signal SoC (System on Chip) designers** and support them for transfer and integration of analog IPs/macros into the “digital on top” environment
- **Proactively consult chip design projects** to ensure proper and efficient usage of working environment, software tools and related methodology
- **Collect application specific requirements to the design environment** and provide fast solutions (e.g. hot fixes) where needed
- **Trigger and track implementation of enhancements of the design environment** to ensure on-time availability for productive usage
- **Work closely with internal software tool and methodology providers** on improving and developing new solutions
- **Promote new methodology solutions** and support roll-out of these in design projects
- **Share knowledge** within “Design Application Engineering Community”

Profile

You are best equipped for this task if you have:

- **A university bachelor or master degree in Electrical Engineering, Computer Science** or equivalent
- **At least 3 years of experience in mixed signal IC design/layout/verification** or support
- **Programming skills** (C, Perl, Tcl)
- **Knowledge of design languages** like System Verilog or VHDL
- **Experience with Unix/Linux**

At a glance

Location: **Villach (Austria)**
Job ID: **359565**
Start date: **Sep 01, 2022**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **359565**
www.infineon.com/jobs

Contact

Carmen Wernle
Talent Attraction Manager



- **Hands-on with some mixed-signal EDA tools** like Cadence (Virtuoso, Spectre, Xcelium, AMS-Designer, Genus, Innovus), Synopsys (DC, ICC) and Siemens /Mentor (Calibre)
- **Good English skills** with German skills as a strong plus

We offer competitive salaries and additional benefits based on your performance, experience and qualification. The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group G, depending on your expertise and skills (<https://www.feei.at/wp-content/uploads/2022/05/minimum-salaries-white-collar-workers-2022.pdf>). The monthly salary is paid 14 times p.a.

Benefits

- **Villach:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; Child care in Villach & Klagenfurt; On-site social counselling and works doctor; Health promotion programs; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“(DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

The **City of Villach** is located in the center of Carinthia, Austria's southernmost province, in close proximity to the Italian and Slovenian border. Due to its particular geographic location and the outstanding natural beauty of the region, Villach and the whole province of Carinthia have for generations been popular holiday destinations for people from all over the world. Its unique landscape combines mountains and valleys, lakes and rivers, city and countryside. In addition to the favorable geographic location, living in Austria also has many social, health-care-related and economic perks. The country's social and health care system is among the best in the world and for decades numerous international surveys have singled out Austria as a particularly safe and wealthy country with a high quality of life.

Find out what you like most about Villach and join us:

<https://www.welcome2villach.at/>

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

