



## Engineer/ Senior Engineer - Analog Mixed Signal IP Validation

### Job description

We are looking for an Engineer / Senior Engineer - AMS IP Validation to mainly perform laboratory validation and measurement of AMS IPs including deriving test validation plan, post-processing & documentation of test data.

In your new role, you will:

- Perform **laboratory validation and measurement of AMS IPs** including **deriving test validation plan, post-processing & documentation of test data**.
- **Design, modify and coordinate** with vendor for **assembling of PCB test board for AMS IPs test and validation**.
- **Develop new** and **maintain existing automated software** and **drivers** for the testing of the IPs.
- **Own the overall laboratory maintenance** such as ensuring all equipment are calibrated per plan schedule.
- **Assess, recommend and order new equipment** to ensure state-of-the-art measurement setup.
- Continuously **enhancing the efficiency of the validation** and increasing the **measurement setup accuracy**.

### Profile

#### Your Profile

- As a true team player, you treat your colleagues with respect and trust.
- You can convince with a structured and precise way of working.
- You are a good networker and have strong communication and problem-solving skills.
- You possess an innovative mindset and out-of-the-box thinking.

### At a glance

Location: **(Singapore)**  
Job ID: **356579**  
Start date: **as soon as possible**  
Entry level: **1-3 years**  
Type: **Full time**  
Contract: **Permanent**

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You are best suited for this position if you have:

- Bachelor's/ Master's degree in Electronics/Electrical Engineering or Physics
- About 3 year of relevant working experience in **laboratory automatic testing of AMS IPs such as PMU, PLLs, ADC/DAC and Temperature Sensors and other sensors.**
- Hands-on experience in **developing PCB boards using Altium** or other software.
- Experience in direct interaction with project members in defining test concepts and patterns.
- Proficient in **debugging hardware** and **software set-up** and familiar with basic laboratory equipment, like oscilloscope, soldering station, power supplies and signal generators.
- Familiar with **C#, C++ & python programming language** is required for programming of the **automated testing of the DUT.**
- Familiar with various protocol interfaces such as **SPI, I2C, RS232, JTAG or FPGA programming and evaluation boards** will be a plus.
- Fluent English skills.

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

*In accordance with the requirements set by the Singaporean Government, Infineon Technologies Asia Pacific Pte Ltd ("Infineon") can only allow individuals who are (a) fully vaccinated, (b) certified to be medically ineligible for a vaccine or (c) have recovered from COVID-19 within a prescribed period, onto company premises. Therefore, Infineon requires all new employees, as well as contractors and business partners, to be fully vaccinated against COVID-19. "Fully vaccinated" means individuals have completed the full regime of an approved COVID-19 Vaccine as listed under the World Health Organization (WHO) Emergency Use Listing (EUL) including the respective post-vaccination period to ensure the vaccine has become full effective. Anyone who is unable to be vaccinated due to an approved and/or recognised exemption condition may apply for special consideration.*

