



Principal Engineer Physical Design and Methodology - Technical Lead RTL2GDS (f/m/div)*

Job description

Looking for the next step in your career? Then join our Physical Design and Methodology team as a Principal Engineer and take over a technical leadership position. Here you will develop state-of-the-art SoC products for all of our divisions - from Automotive and Industrial Power Control to Connected Secure Systems and Power & Sensor Systems. Bring in your experience and build up an expert network across sites and departments! With this position you will be entering our „technical ladder”: our career path for experts. You will be able to focus deeply on technology - while further pursuing your career. As you continually expand your expert knowledge, puzzle over matters and solve problems, you will position yourself as a thought leader - thus helping our products improve our world a bit more each day.

In your new role you will:

- * **Take over technical leadership** for highly complex designs in multi-site Physical Design projects
- * **Translate product requirements** into layout specifics using our state-of-the-art EDA tools and flows
- * Work in all phases of the **RTL2GDS flow**
- * **Steer cross-functional teams** to meet Power Performance Area goals on schedule
- * **Drive methodology topics** within the digital implementation RTL2GDS domain, impacting projects of all our divisions
- * **Analyze and solve problems** of high complexity using your global expert network
- * Share your knowledge within the **Physical Design community**
- * **Act as mentor and coach** for the project team members

Profile

We are looking for your experience and your willingness to use it to achieve cross-project success. Moreover, as a technical lead you are able to plan and track work packages, delegate tasks and manage relationships to key stakeholders. With your problem-solving mindset and business impactful working style you contribute to accomplish our project targets. You should have these additional qualifications:

- * A degree in **Electrical Engineering, Microelectronics** or a similar field
- * Multiple years of experience in **Physical Design of highly complex SoCs**
- * A deep **technical understanding** in all aspects of **RTL2GDS**
- * Proven **technical leadership skills**
- * Sound programming skills and knowledge in scripting languages e.g. **Tcl, Perl or Python**
- * Basic experience in **RTL coding and SoC architecture** development
- * Ability to work with **international teams** as well as solid **communication and**

At a glance

Location: **Munich (Germany)**
Job ID: **356184**
Start date: **Sep 01, 2022**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **356184**
www.infineon.com/jobs

Contact

Alexander Greie
Team Lead Talent Attraction



presentation skills

* Very good **English** language skills – with **German** being an added plus

Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (**DES**) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high-level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

