



Senior / Staff Engineer - Mixed Signal Physical Layout

Job description

We are looking for a Senior / Staff Engineer - Mixed Signal Physical Layout for floor planning and area estimation of mixed-signal IP macros and subsystems. This new hire will also take charge of analog full custom layout of mixed-signal IP macros and subsystems.

In your new role you will:

- **Floor planning and area estimation of mixed-signal IP macros and subsystems**
- **Analog full custom layout of mixed-signal IP macros and subsystems**
- **Layout verification (LVS/DRC/ERC) for the IP macros and subsystems to ensure the layout is compliant with signoff quality.**
- Active communication and alignment with **circuit designers** to **optimize layout** for better power, performance & area.
- Active communication and alignment with **subsystem and top-level layout teams** to **guarantee** smooth integration into their chip top-level
- Work together with the **methodology flow team** to enhance layout efficiency

Profile

You are best equipped for this task if you have:

- Minimum in Bachelor's Degree in Engineering (Electrical & Electronics)
- At least 3 - 5 years of experience in **full custom layout in planar CMOS technology.**
- Good understanding of integrated circuits such as **matching, noise, coupling, shielding as well as all kinds of layout-dependent effects (LDE) in deep submicron technologies**
- Good knowledge of state-of-the-art **CAD tools (Cadence, Mentor)**
- Unix/Linux OS experience and basic know-how of programming languages (e.g. Skill, Python) and scripting.
- Fluent spoken English and written skills.
- Familiar with **Cadence Skill** and experience in **finFET layout** will be a plus
- Candidates with lesser years of experience are also encouraged to apply but will be considered for lower positions

Why Us

At a glance

Location: **(Singapore)**
Job ID: **355920**
Start date: **Sep 01, 2022**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **355920**
www.infineon.com/jobs



Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

In accordance with the requirements set by the Singaporean Government, Infineon Technologies Asia Pacific Pte Ltd ("Infineon") can only allow individuals who are (a) fully vaccinated, (b) certified to be medically ineligible for a vaccine or (c) have recovered from COVID-19 within a prescribed period, onto company premises. Therefore, Infineon requires all new employees, as well as contractors and business partners, to be fully vaccinated against COVID-19. "Fully vaccinated" means individuals have completed the full regime of an approved COVID-19 Vaccine as listed under the World Health Organization (WHO) Emergency Use Listing (EUL) including the respective post-vaccination period to ensure the vaccine has become full effective. Anyone who is unable to be vaccinated due to an approved and/or recognised exemption condition may apply for special consideration.

