



Senior Staff IO Design Engineer

Job description

The successful applicant should have 10+ years of IO circuit design experience with knowledge of IOs like LVDS, DDR, high speed IOs and GPIO. ESD design experience is desirable but no mandatory. The applicant will be responsible for designing and delivering IO and ESD designs from specs to silicon with the relevant deliverables to chip integration team. Candidate should be able to lead a small team of designers and come up with new architectures and plan schedules

Profile

IO circuit design,
ESD design, device physics and Reliability,
IO and ESD Architectures,
IO and ESD deliverables,
Layout design knowledge,
post silicon characterization
Candidate should have delivered few designs from spec to silicon with complete knowledge associated deliverables.
Understands importance of timelines.
Pays attention to details for deliverables quality.
Team work attitude.
Good problem solving communication skills

Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Medclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

Why Us

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At a glance

Location: **Bangalore (India)**
Job ID: **355710**
Start date: **Aug 31, 2022**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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