



Lead Principal Design Engineer

Job description

In your new role you will:

- Cross-group and cross-disciplinary role as the chip technical lead .
- Primary responsibility
 1. SoC Chip technical lead working closely with SoC CIC team
 2. Assist to define MCU SoC architectures, PPA analysis and investigations of software requirements.

Profile

You are best equipped for this task if you have:

- 15 years in Chip and SoC design experience and PhD degree;
- Proven track of SoC technical leadership;
- Involved in architecture definitions - Clocks, Resets, Boot, Power Management;
- Proficiency in Perl, Tcl, Tk, C/C++ languages.

Benefits

- **Shanghai, :** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Medical coverage; On-site social counselling and works doctor, in Wuxi only.; Provision of health tips, health knowledge sharing, annual medical check; Employees in Shanghai are entitled to use a gym located close to the office for free; On-site canteen, in Wuxi only.; Private insurance offers in some sites.; Wage payment in case of sick leave; Corporate pension benefits in some sites.

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

At a glance

Location: **Shanghai, (China)**
Job ID: **355543**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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