



GaN Power Technology Development Engineer (f/m /div)*

Job description

Do you want to contribute to a sustainable future by realizing leading edge innovations for efficient use of electrical energy and work in an expanding world-class technology development team with global footprint? Here is your chance! If you bring in some relevant pre-experience in semiconductor technology development, process integration or production combined with an affinity for wide bandgap technologies – apply now!

In your new job you will:

- Drive the **development of GaN power technologies** in collaboration with application, product, TCAD simulation and reliability teams
- Work together with unit process development experts to **optimize process modules and improve overall processing stability**
- **Set up new process of records** and take ownership for the complete **process flow** (process integration)
- Plan (using **design of experiment** techniques), coordinate and evaluate **experiments**
- Execute advanced **data analysis** and representation
- **Define and drive yield improvement measures** based on (optical & electrical) screening data

Profile

You describe yourself as an ambitious team player with outstanding technical expertise in semiconductor development and manufacturing. You use systematic approaches for straightforward problem solving. You like to use design-of-experiment methodology for fast and efficient learning cycles. You are strong in communication and in establishing networks in and around your working environment or are already part of our manufacturing or development teams. You are keen to develop your technical and social skills in the No.1 company for power semiconductors.

You are best equipped for this task if you have:

- A university degree (master or above) in **electrical engineering, microelectronics, physics** or other relevant studies
- **1-3 years of experience** in semiconductor technologies, for example TCAD simulation, technology development, process integration or production
- Experience with Si, SiC or GaN wafer **power technology** platforms

At a glance

Location: **Villach (Austria)**
Job ID: **355325**
Start date: **as soon as possible**
Entry level: **1-3 years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **355325**
www.infineon.com/jobs

Contact

Manuel Krebs
Talent Attraction Manager



- Worked in **multicultural, high-tech environment** project teams before
- Experience with **high-volume semiconductor fab systems** as an advantage
- Excellent **English** language skills as a must, fluency in **German** or the willingness to learn as a plus

We offer competitive salaries and additional benefits based on your performance, experience and qualification. The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group G (<https://www.feei.at/wp-content/uploads/2022/05/minimum-salaries-white-collar-workers-2022.pdf>). The monthly salary is paid 14 times p.a. We offer a higher compensation depending on your expertise and skills.

Benefits

- **Villach:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; Child care in Villach & Klagenfurt; On-site social counselling and works doctor; Health promotion programs; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

– Power & Sensor Systems (PSS) drives leading-edge power management, sensing and data transfer capabilities –

Infineon **PSS** semiconductors play a vital role in enabling intelligent power management, smart sensitivity as well as fast and reliable data processing in an increasingly digitalized world. Our leading-edge power devices make chargers, adapters, power tools and lighting systems smarter, smaller, lighter and more energy-efficient. Our trusted sensors increase the context sensitivity of “things” and systems such as HMI, and our RF chips power fast and reliable data communication. [Click here](#) for more information about working at PSS with interesting employee and management insights and an overview with more #PSSDreamJobs.

At **Infineon in Villach** you shape the technologies of tomorrow and work in an international environment with more than 3700 colleagues from over 65 nations. Your personal contribution will be valued and appreciated as the cornerstones of our success. And all that in beautiful surroundings which guarantee a high quality of life.

The **City of Villach** is located in the center of Carinthia, Austria’s southernmost province, in close proximity to the Italian and Slovenian border. Living in Austria has many social, health-care-related and economic perks. The country’s social and health care system is among the best in the world and for decades numerous international surveys have singled out Austria as a particularly safe and wealthy country with a high quality of life. Villach itself benefits from its status as a “small town”, offering everyday living at affordable prices in an outstanding setting.

Find out what you like most about Villach and join us:

<https://www.welcome2villach.at/>

** The term gender in the sense of the General Equal Treatment Act (GETA) or other*



national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.

