



Staff DFT Engineer

Job description

In your new role you will:

- DFT implementation, including SCAN, MBIST, etc.;
- MBIST chip level plan, RTL insertion, verification and pattern generation;
- Develop high coverage and cost effective test patterns, and take part in ATE bring-up;
- Co-work with Front End and PD for synthesis optimization and smooth timing signoff.

Profile

You are best equipped for this task if you have:

- Master degree;
- Experienced in MBIST flow, insertion/generation, verification and pattern generation;
- Assisted in SCAN ATPG.

Benefits

- **Shanghai, :** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Medical coverage; On-site social counselling and works doctor, in Wuxi only.; Provision of health tips, health knowledge sharing, annual medical check; Employees in Shanghai are entitled to use a gym located close to the office for free; On-site canteen, in Wuxi only.; Private insurance offers in some sites.; Wage payment in case of sick leave; Corporate pension benefits in some sites.

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

At a glance

Location: **Shanghai, (China)**
Job ID: **353568**
Start date: **as soon as possible**
Entry level: **1-3 years**
Type: **Full time**
Contract: **Permanent**

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