



Principal Verification Engineer

Job description

We have a great opportunity for a principal verification/digital design engineer to work with micro-architecture/architecture definition modeling and reviews. You will also be responsible for defining verification strategy for IP/Chip/System level verification. If you're looking to take your technical knowledge to the next level, apply now to join us in our mission for excellence. Infineon Technologies is one of the top 10 semiconductor companies in the world. We want to talk with you!

In your new role you will:

- Participating in architecture definition and modeling.
- Contributing to micro-architecture definition and reviews.
- Reviewing standard specs and incorporating any changes into the verification plan to ensure compliance.
- Defining verification strategy (constraint random, Formal, Directed etc.) for IP /Chip/System level verification.
- Defining and implementing verification environment architecture and methodology development.
- Driving block/chip/system level test plan development and execution.
- Working with ASIC designers and architects to produce thoroughly verified, robust IP
- Overseeing all verification for the IP/Chip/System and driving functional and code coverage closure.
- Actively participating in post-silicon bring-up, validation and compliance testing.
- Actively participating in cross functional collaboration with design, software and hardware teams to ensure a successful product delivery.
- Mentor other engineers and technically guide them.

Profile

You are best equipped for this task if you have:

- 12-18 Year experience
- Proven track record of taking several chips in from product definition to production.
- Experience in complex ASIC verification.
- Good understanding of ASIC design and verification methodologies and flows.
- Solid understanding of standard ASIC verification techniques, including:

At a glance

Location: **San Jose, CA (United States)**
Job ID: **353239**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **353239**
www.infineon.com/jobs



- Test planning
 - Testbench creation
 - Code and Functional coverage
 - Directed and random stimulus generation
 - Assertions
- Solid understanding of verification methodologies and one or more of the following standard testbench languages:
 - SystemVerilog (OVM/UVM)
 - C/C++
 - System Verilog DPI/Verilog PLI
 - Proficient in object oriented programming. Development of re-usable test-bench.
 - Experience with implementing constraint random verification methodology.
 - Good problem solving skills.
 - USB2.0/USB3.0/USBPD/Type-C knowledge is a plus.
 - Work effectively with both internal and external teams/customers is expected.
 - Experience with silicon debug which include logic and custom Analog Blocks working together.
 - Strong written and verbal communication skills.
 - Facilitator of direct and open communication, diversity of opinion, and debate.
 - In addition, be self-motivated with the initiative to seek constant improvements in the verification methodologies.
 - The candidate must also possess strong initiative, analytical/problem solving skills, team working skills, ability to multitask and be able to work within a diverse team environment.
 - Experience in emulation is plus.
 - Working knowledge of gate level

Benefits

- **San Jose, CA:** Medical, Dental & Vision Plans; Flexible Reimbursement Accounts (FSAs); Industry leading 401k Employer Contribution/Match; Company Performance Bonus; Holiday Pay & Paid Time Off (PTO); Flexible Working Conditions, Part-time Options; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Wide range of Training Offers & Career Development Planning; Coaching, Mentoring, Networking Possibilities; International assignments; Basic Life & Dependent Life Insurance; Paid Sick Leave, Accidental Death & Disability Insurance (AD&D) ; Short-term & Long-term Disability ; Employee Assistance Program (EAP) ; Health Promotion Programs ; Reduced Price for Public Transportation

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender



identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your on-boarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE :

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

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