



Senior Staff Engineer Mask Design

Job description

Job Description

- 1) Participate in block/IP/chip floor planning from scratch, performing routing & layout verification (LVS, DRC, antenna & others) and troubleshooting the results
- 2) Co lead/lead the physical layout of IP/full chip design by working closely with cross functional team leader
- 3) Conduct thorough layout design reviews internally and external review with circuit designers
- 4) Extensive use of CAD tools (cadence virtuoso VXL & mentor calibre) in IP/chip integration and verification
- 5) Capable in guiding/coaching junior engineers for on time delivery and quality

Profile

Job Specification

- 1) Bachelor degree in Electrical/Electronic Engineering/physics with VLSI exposure or equivalent 9 -15 years of job experience in analog layout design
- 2) Hands-on experience in analog layout from scratch. Knowledge & experience in HV layout will be a plus
- 3) Deep understanding of analog circuit layout concepts in submicron CMOS technologies
- 4) Posses strong technical, analytical & problem solving skills in analog layout design
- 5) Ability to work as strong team player and participate in cross functional activities
- 6) Good interpersonal, verbal and communication skill with good initiative at work

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At a glance

Location: **Penang (Malaysia)**
Job ID: **352716**
Start date: **Aug 15, 2022**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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