



## Digital Design Engineer

### Job description

Do you have the skillset to thrive in a high-paced environment? If you have the skillset to match Design verification, ASIC design, and excellent communication skills, this will be the perfect opportunity! Apply here at Infineon today!

#### In your new role you will:

- **Design and maintain IP** RTL
- Perform debug **testcases**
- Implement design solutions
- Work with **SoC team** on integration support for subsystem IP
- Work with **FW team** for cross functional debug of validation issues

### Profile

#### You are best equipped for this task if you have:

- BSEE, MSEE (preferred)
- Familiarity with ASIC design and Verilog HDL
- Design verification (UVM, directed tests, SV) is highly desirable
- Excellent organizational, communication, and documentation skills
- The ability to enjoy overcoming challenging issues, learn new skills, and have a passion for customer success

### Why Us

#### Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

*Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital*

### At a glance

Location:

Job ID: **349372**

Start date: **as soon as possible**

Entry level: **0-1 year**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **349372**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)



*status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws.* Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your onboarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

**IMPORTANT NOTICE:**

**Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.**

**Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.**

