



## Staff Engineer Analog Design Methodology for Software and Automation (f/m/div)\*

### Job description

We pave the way for high quality future product development at Infineon. We achieve this with continuous improvements to tools and processes, and by staying up to date with the latest computer-aided design methods. As a Staff Engineer Analog Design Methodology, you must maintain and improve analog generators for front end and back end design, while focusing on the needs of both internal and external stakeholders. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Use the Cadence Virtuoso Software Suite and Cadence-SKILL language** to program and maintain custom scripts and extensions for our Analog & Mixed-Signal (AMS) design tools
- **Develop our in-house software tools** for the automated design of AMS circuits using SKILL; PEARL is a plus
- **Improve existing software modules** including the testing, refactoring, and maintenance of existing code
- **Train and support our worldwide designer community**
- **Write technical documentation and strive for quality** by complying with all relevant quality assurance processes
- **Integrate new methods and software tooling** into our established design flow

### Profile

You work conscientiously on making things better, faster, and more efficient. Moreover, you cautiously listen to the customer and consider his point of view and ideas, and you share information comprehensively and effectively.

You are best equipped for this task if you have:

- A **degree in Electrical Engineering, Physics** or similar
- At least **3-5 years of work experience in software design** and implementation related to AMS Design
- **Experience with scripting for electrical design automation** (EDA) applications (Perl, Python and Cadence-SKILL is required)
- **Knowledge of the Cadence Virtuoso analog design environment** (experience with physical layout synthesis in analog mixed signal designs) is desired

### At a glance

Location: **Munich (Germany)**  
Job ID: **349071**  
Start date: **as soon as possible**  
Entry level: **3-5 years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **349071**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)

### Contact

**Mona Straßmair**  
Talent Attraction Manager



- **Familiarity with the Unix/Linux operating system** (e.g. shell scripting and basic tools) and LSF server farms
- **Fluent English skills;** German is an advantage

## Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization, **Design Enabling and Services (DES)**, delivers the software design environment to all Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services, DES supports Infineon's advanced IC development from early high-level system models to verified products ready for manufacturing.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

