



Layout Engineer (w/m/div)*

Job description

Are you interested in actively shaping the latest technologies of the future as part of a young and dedicated team and taking a look behind the scenes of a globally operating semiconductor company? The path from the idea of an innovation to its implementation in a chip layout sounds exciting to you? Then we should get to know each other, because we are looking for a committed personality who will work with our worldwide operating team to drive forward the development and optimization of MOSFET layouts.

Your new tasks as Layout Engineer for power MOSFETs will include:

- Creation of **power semiconductor layouts within product and technology development projects** (OptiMOS / GaN / SiC)
- **Worldwide cooperation** with our sites in Asia, Europe and America
- **Programming and automatization** of layouts
- Documentation and **quality reviews**
- Cross-site **layout support** for manufacturing and development topics

Profile

You are characterized by an independent way of working and drive your tasks with commitment, passion and proactivity. Your strong communication skills help you to collaborate across departments within the team and with internal partners.

You successfully meet the requirements if you have:

- Completed an **HTL or technical college** in the field of **computer science, electrical engineering, electronics, communications engineering**, or comparable
- 1-3 years of professional experience in the field of **layout (Cadence) and Unix**
- Already gained first experiences in **layout design** with e.g. Autodesk EAGLE or similar CAD tools
- Good knowledge of **programming languages** such as C++
- Distinct **spatial imagination** and technical understanding
- Very good **English and German skills** (or the willingness to learn German)

We offer competitive salaries and additional benefits based on your performance, experience and qualification. The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group E-F (<https://www.feei.at/wp-content/uploads/2022/05/minimum-salaries-white-collar-workers-2022.pdf>). The monthly salary is paid 14 times p.a. We offer a higher compensation depending on your expertise and skills.

At a glance

Location: **Villach (Austria)**
Job ID: **348945**
Start date: **as soon as possible**
Entry level: **0-1 year**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **348945**
www.infineon.com/jobs

Contact

Manuel Krebs

Talent Attraction Manager



Benefits

- **Villach:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; Child care in Villach & Klagenfurt; On-site social counselling and works doctor; Health promotion programs; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

– Power & Sensor Systems (PSS) drives leading-edge power management, sensing and data transfer capabilities –

Infineon **PSS** semiconductors play a vital role in enabling intelligent power management, smart sensitivity as well as fast and reliable data processing in an increasingly digitalized world. Our leading-edge power devices make chargers, adapters, power tools and lighting systems smarter, smaller, lighter and more energy-efficient. Our trusted sensors increase the context sensitivity of “things” and systems such as HMI, and our RF chips power fast and reliable data communication. [Click here](#) for more information about working at PSS with interesting employee and management insights and an overview with more #PSSDreamJobs.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

