



Lead Principal - Digital Design

Job description

Lead Principal - Digital Design

- Design of CPU Subsystem IPs including literature search, bench marking, feature definition, micro architecture, logic design and optimization for Power, Performance and Area for Multi-core CPU Subsystems for Complex SoCs targeting Consumer/Automotive and IoT applications.
- Complex IPs starting from the creation of the spec, design, verification, and finally high volume productization is a strong requirement.
 - Expertise in dealing and designing with complex IP's from different sources onto the same piece of silicon.
 - Understand system implications of the silicon design decisions.

At a glance

Location:

Job ID: **348681**

Start date: **as soon as possible**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

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Contact

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Profile

- Strong knowledge of ARM processors (Cortex-M0, M0+, M4, M7, M33, A5 etc.), micro-controller IP design using ARM processors, AHB, AXI is mandatory.
- Knowledge & experience of Microarchitecture, RTL, Synthesis, STA etc..
- Good problem solving skills. For Logic design, experience with Logic Synthesis, Linting, CDC and DFT tools is preferred.
- The candidate should be able to demonstrate the following behaviors:
- Work effectively with both internal and external teams/customers is expected.
- Mentor other engineers and technically guide them.
- Experience with silicon which include ARM micro controller and custom Analog Blocks working together.
- Strong written and verbal communication skills.
- Facilitator of direct and open communication, diversity of opinion, and debate.



- In addition, be self-motivated with the initiative to seek constant improvements in the logic design methodologies.
- The candidate must also possess strong initiative, analytical/problem solving skills, team working skills, ability to multitask and be able to work within a diverse team environment.

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