Staff Specialist Digital Verification (f/m/div)*

Job description

Do you want to become part of our Digital Verification Team? In this role you will work in an international team being responsible for the creation and improvements of verification environments and the execution on RTL and gate-level. If that sounds interesting to you, Apply now!

In your new role you will:

- **Create new** and **improve** existing **Digital Functional Verification Environments**
- **Apply Universal Verification Methodology** (UVM)
- **Define verification plans** and setup verification metrics in digital and mixed-signal environments
- **Execute tests** in these environments on RTL and gate-level
- **Closely cooperate with analog and digital designers** as well as concept and verification engineers

Profile

You are best equipped for this task if you have:

- **A degree in Electrical Engineering , Information Technology** or **Telematics** with focus on electronics
- Ideally **3-5 years** of related **work experience**
- **Good knowledge** of **VHDL, Verilog, C**
- Good knowledge of **Unix programming languages** such as Shell, Perl, TCL etc
- Experience at any level in **Specman/e, SystemVerilog**
- Knowledge of **SVAs, PSL, formal or mixed-signal verification** as a plus
- **Functional verification experience** as a plus
- **Fluent English skills** with German skills as a plus

We offer competitive salaries and additional benefits based on your performance, experience and qualification. The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group G (https://www.feei.at/wp-content/uploads/2021/04/minimum-salaries-white-collar-workers-2021.pdf). The monthly salary is paid 14 times p.a. We offer a higher compensation depending on your expertise and skills.

Why Us

At a glance

Location:

Job ID: **348671**

Start date: **Jul 01, 2022**

Entry level: **3-5 years**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **348671**
www.infineon.com/jobs

Contact

Carmen Wernle
Talent Attraction Manager

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization **„Design Enabling and Services"(DES)** provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

The **City of Villach** is located in the center of Carinthia, Austria's southernmost province, in close proximity to the Italian and Slovenian border. Due to its particular geographic location and the outstanding natural beauty of the region, Villach and the whole province of Carinthia have for generations been popular holiday destinations for people from all over the world. Its unique landscape combines mountains and valleys, lakes and rivers, city and countryside. In addition to the favorable geographic location, living in Austria also has many social, health-care-related and economic perks. The country's social and health care system is among the best in the world and for decades numerous international surveys have singled out Austria as a particularly safe and wealthy country with a high quality of life.

Find out what you like most about Villach and join us:
https://www.welcome2villach.at/

* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.