



Senior Staff Engineer Digital Design and Concept (f/m/div)*

Job description

In this role you will work on the concept and development of digital IP and subsystems for different application areas (e.g. communication, control, CPU) which are used in different business lines reach from automotive to consumer, industrial or security products. You will align with projects WHAT you need to develop on a high-level specification and HOW you want to implement your concept. Your your decisions on concept and design will have a strong focus on IP reuse and platform-based design. Of Course, you have to follow and apply industry standards (e.g. ISO26262, security, etc.) to ensure the quality requirements of the target applications. With this position you will be entering our „technical ladder“: our career path for experts. You will be able to focus deeply on technology – while further pursuing your career. As you continually expand your expert knowledge, puzzle over matters and solve problems, you will position yourself as a thought leader – thus helping our products improve our world a bit more each day.

In your new role you will:

- **Own complete design workpackages** (implementation, ensure code quality, documentation) and **rampup as Technical Lead** of cross-functional IP development teams
- **Drive innovative IP concept definition** based on the high-level product concept specification, incl. virtual prototyping
- **Integrate** digital, analog and mixed signal IP **on sub system and SoC level**
- **Drive and extend** the central **IP Roadmap and IP Reuse** through continuous alignment with Infineon partners
- Develop and spread **innovative and efficient methods for IP development** and integration based on internal and external tools
- Evaluate 3rd party IP and deliver it to Infineon projects
- **Give technical guidance** for less experienced team members

Profile

You see ‘the big picture’ of the projects and know where and how you and your concept can contribute best – also by sometimes thinking in unconventional ways. Furthermore, you like to consult colleagues using your expert knowledge on concept and IP development & functional safety, align with external IP providers and establish stable networks.

You are best equipped for this task if you have:

At a glance

Location:

Job ID: **348120**

Start date: **Jul 01, 2022**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **348120**
www.infineon.com/jobs

Contact

Bari Guritno

Talent Attraction Manager



- A Degree in **Electrical Engineering, Physics, Informatics** or similar
- At least **5 years of experience** in digital design for IP/SoC development and concept engineering
- Familiarity with both **soft macro and hard macro delivery flow**
- Architecture experience with **CPU subsystems** (e.g. ARM, Risc-V)
- Experience with **requirement-driven development flows** and ideally also with safety products – **ISO26262** qualification is an advantage
- Experience with **digital IP development** as well as **scripting languages** (e.g. TCL, Perl, Python)
- Expertise to **ensure HDL code quality** (e.g. Linting, CDC, DFT)
- Very good **English presentation skills** - German language is an advantage

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (**DES**) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineons advanced IC development from early high level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

