



Assistant Engineer Process

Job description

Must be able to work in a wafer fab environment and shift pattern

In your new role you will:

- Must be able to work in a wafer fab environment and shift pattern
- Dispose lot on hold.
- QMP pull back lot list.
- CDSEM recipe optimization and creation.
- Support daily tasks is given by UPE.
- Execute SAHD lot.

Profile

You are best equipped for this task if you have:

- Fab working experiences, in hold lot disposition.
- Experience in measurement tool recipe creation, such as CDSEM, METTOP, N&K.
- Can work on Shift.
- Able to communicate, read, and write in English.
- Able to work in Kulim Area.

Benefits

- **Kulim:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Holiday child care; Medical coverage; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Flexible transition into retirement

Why Us

Part of your life. Part of tomorrow.

At a glance

Location: **Kulim (Malaysia)**
Job ID: **348060**
Start date: **as soon as possible**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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