



## Senior Staff CAD Engineer (f/m/div)\*

### Job description

Are you passionate about using your communication and technical leadership skills to ensure seamless cooperation towards a common goal in an international environment? Do you want to have the overview from end-to-end instead of being the expert for just one step? Then this position might be the perfect match for you: as a Senior Staff CAD Engineer you will be part of the Design system Infrastructure team. Furthermore, you will be technically driving the Design Data and Configuration Management Methodology for Chip Design. You will be able to focus deeply on technology – while further pursuing your career. As you continually expand your expert knowledge, puzzle over matters and solve problems, you will position yourself as a thought leader – thus helping our products improve our world a bit more each day.

In your new role, you will:

- **As part of the Design system Infrastructure team** you will be technically driving the Design Data and Configuration Management Methodology for Chip Design
- **Bring state of the art methodology/solution from scratch** – understanding, explaining, evaluating and deploying the same, and keeping internal and external stakeholders in alignment
- **Provide technical leadership** to improve efficient and ensure proactive communication in a multi-site and multi-cultural environment
- **Work closely with EDA vendors** to improve the features, resolve issues identified
- **Provide consulting or Mentor** to Design Teams / DAE (Design Application Engineers) on Data and Configuration Management topics – train them on various topics and bring them up to speed to be independent on complex areas over the time
- **Organize and Steer Data Management community** to foster knowledge exchange

### Profile

You accept responsibility for common goals and puts the team's results above your own interests. You take on new challenges without knowing the right path to an eventual solution in advance. You focus your efforts on finding solutions that offer added value for both sides. You have good interpersonal skills and the ability to persuade and influence based on technical facts.

You are best equipped for this task if you have:

- A degree in **Electrical Engineering, Computer Science** or equivalent
- At least **5 years of semiconductor industry experience** in CAD for Digital and Analog design including roles of technical leadership

### At a glance

Location: **Munich (Germany)**  
Job ID: **347078**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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### Contact

**Bari Guritno**  
Talent Attraction Manager



- Deep know how of Data Management tools (e.g. **Clearcase, Git, DesignSync, Perforce**) and competence in Cadence Virtuoso is desirable
- Profound with **Unix/Linux** and expert in scripting languages like **Perl / Python/** etc...
- Proficiency in JIRA, IP Lifecycle management, PLM solutions like Windchill is preferred
- Ability to **prioritize work, set goals and meet deadlines**
- Willingness & ability to undertake occasional international travel
- Possess **excellent English communication skills** - German language is an advantage

## Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (**DES**) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high-level system models to verified products ready for manufacturing.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

