



Staff Engineer Quality Reliability

Job description

Responsible for Reliability Monitoring & Risk Assessment for assigned technology to ensure no reliability risk lots to Customer.

In your new role you will:

- Responsible for Reliability Monitoring & Risk Assessment for assigned technology to ensure no reliability risk lots to Customer.
- Support for technologies reliability test related monitoring, interpret reliability test result & make engineering judgement for lot release. Share and explain disposition justification & decision for deviation lots.
- Use Analysis tool (eg AA, Tool Commonality) to accommodate root cause investigation.
- Responsible to handle wafer level test using tester tools REL lab. Work on tester issues / improvement.
- Show leadership in quality topic: 8D lead especially on assigned technology & process whenever needed.
- Understand reliability test vs technologies (test structure design) & process assigned mainly for lot release judgement & consultant to operations test methodology for new process/tool qualification.
- Setup & use correctly tools (ALF, Tetris, SPACE - SPC tool) for assigned technologies for controlling & managing lots that required reliability test (lot sampling or wafer sampling, OOC lots triggering).
- Effectively communicate within Kulim + Business Unit & REL Team in Europe for reliability assessment.
- Checker for procedures– to ensure procedure are meeting quality target.
- Moderate FMEA & risk assessment are completely assessed for new tool/process release.
- Participate in transfer projects for technology qualification and drive technical projects yearly.

Profile

You are best equipped for this task if you have:

- Degree / Master / PhD in Physics / Electronics Engineering / Microelectronics.
- Work Experience: 2- 10 years.
- Minimum 2 years of experience in semiconductor technology & manufacturing w/ basic reliability background.

At a glance

Location: **Kulim (Malaysia)**
Job ID: **346968**
Start date: **as soon as possible**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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- High interest in depth of technical knowledge on wafer level reliability engineering.
- Passionate about Reliability Function & likes to deliver the best.
- Willing to explore in depth on technology & process assigned especially on potential reliability risks.
- Understanding of the function of each Semiconductor reliability test to interpret the test result with reliability statistics make an engineering judgement for lot release.
- Interest in interaction w/ all levels of employees internally or externally.
- Good communication skill & interpersonal skill.
- Ability to apply 8D, 3x5 Why Analysis, FMEA.
- Strong analytical skill with systematic approaches. Use analysis result to drive related parties for improvement.
- Initiate continuous improvement project for any reliability process weaknesses.
- Ability to present projects confidently to Kulim and Europe.

Benefits

- **Kulim:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Holiday child care; Medical coverage; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Flexible transition into retirement

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

