



Engineer Mask Design

Job description

Job Description :

- 1) VLSI component layout and translation of schematics into layout geometry.
- 2) Participate in cell/block/IP floor planning, performing layout verification and troubleshooting the results.
- 3) Extensive use of CAD tools (cadence virtuoso) in chip assembly and verification
- 4) implement ECOs and layout block reshaping.

Profile

Job Specification :

- 1) Bachelor degree in Electrical/Electronic Engineering/Physics with VLSI exposure of equivalent 1~3 years of job experience in layout design.
- 2) Ability to work with multiple groups as a project team members.
- 3) Good interpersonal, verbal and communication skill with good initiative at work.

Why Us

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At a glance

Location: **Penang (Malaysia)**
Job ID: **346904**
Start date: **as soon as possible**
Entry level: **1-3 years**
Type: **Full time**
Contract: **Permanent**

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